



GE Fanuc Automation

Programmable Control Products

*Series 90™ -20
Programmable
Controller*

User's Manual

GFK0551C

August 1995

Warnings, Cautions, and Notes as Used in this Publication

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Warning notices are used in this publication to emphasize that hazardous voltages, currents, temperatures, or other conditions that could cause personal injury exist in this equipment or may be associated with its use.

In situations where inattention could cause either personal injury or damage to equipment, a Warning notice is used.

Caution

Caution notices are used where equipment might be damaged if care is not taken.

Note

Notes merely call attention to information that is especially significant to understanding and operating the equipment.

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The Series 90-20 Programmable Logic Controller and its associated modules have been tested and found to meet or exceed the requirements of FCC Rule, Part 15, Subpart J. The following note is required to be published by the FCC.

NOTE

This equipment generates, uses, and can radiate radio frequency energy and if not installed in accordance with this instruction manual, may cause harmful interference to radio communications. It has been tested and found to comply with the limits for a Class A digital device pursuant to Part 15 of the FCC Rules, which are designed to provide reasonable protection against harmful interference when operated in a commercial environment. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user will be required to correct the interference at his own expense.

The following note is required to be published by the Canadian Department of Communications.

NOTE

This digital apparatus does not exceed the Class A limits for radio noise emissions from digital apparatus set out in the radio interference regulations of the Canadian Department of Communications.

Preface

This manual provides the information necessary to enable you to integrate a GE Fanuc Series 90™-20 Programmable Logic Controller (PLC) into a wide variety of control applications. The contents of this manual include descriptions of the hardware components, installation procedures, system operation information, and maintenance information for the Series 90-20 PLC.

Revisions to This Manual

This version of the manual (GFK-0551C) adds and corrects some information. Following is a list of changes to this manual as compared to the previous version (GFK-0551B).

- Changed references to programming software from Logicmaster 90-30/90-20 to Logicmaster 90/30/20/Micro.
- Added references to Series 90-30 model 351 CPU where applicable.
- Page 1-17, revised programming requirements for running Logicmaster 90/30/Micro programming software.
- Page 2-2, added Technical Help information.
- Page 2-22, 2-23, revised previous information and added additional information for IC693CBL303 cable.
- Page 2-26, corrected part number for XICOR EEPROM and added second part number.
- Page 5-11, 5-21, 5-31, corrected Bussman part number for fuse, FU2 (changed from GMG0.25 to GMD0.25).

Content of this Manual

This manual contains the following information:

Chapter 1. Introduction and Product Description: This chapter provides a general description of the functions and capabilities of the Series 90-20 PLC and descriptions of the components of a Series 90-20 PLC.

Chapter 2. Installation Procedures: This chapter leads you through the steps required for installation of a Series 90-20 PLC control system. Included are installation procedures for the hardware components, connection of field wiring, initial configuration using the Series 90-30 and 90-20 Hand-Held Programmer, and basic maintenance procedures.

Chapter 3. System Operation: This chapter provides you with technical details of operating features of the Series 90-20 PLC that you will need to be familiar with for an understanding of how the overall PLC system operates.

Chapter 4. Fault Descriptions and Corrections: This chapter provides information to aid you in troubleshooting a Series 90-20 PLC when using Logicmaster 90-30/20/Micro programming Software. Faults appearing in the PLC and I/O Fault Tables are described along with corrective actions for those faults.

Chapter 5. I/O Specifications: This chapter provides specifications and wiring information for the I/O circuits of the Series 90-20 PLC. It provides you with the information needed to specify and apply each of the fixed I/O configuration modules, and wire field devices to those modules.

Chapter 6. Series 90-20 High Speed Counter: This chapter provides specifications, hardware interface requirements, operation, and configuration programming information for the High Speed Counter that is built into the Series 90-20 PLC.

Appendix A. Glossary: A glossary of terms relevant to the Series 90-20 PLC and to Programmable Logic Controllers in general, and a glossary of basic instructions and reference types for the Series 90-20 PLC.

Appendix B. Instruction Timing: A group of tables that show the memory size in bytes and the execution time in microseconds for each of the Series 90-20 PLC programming functions.

Appendix C. Miniconverter Kit: This appendix contains detailed information about the Miniconverter Kit (IC690ACC901) for use with Series 90 PLCs.

Related Publications:

- *GFK-0401 - Workmaster™ II PLC Programming Unit Guide to Operation*
- *GFK-0402 - Hand-Held Programmer for Series 90™ -30/20/Micro PLC User's Manual*
- *GFK-0466 - Logicmaster™ 90 Series 90™ -30/20/Micro Programming Software User's Manual*
- *GFK-0467 - Series 90™ -30/20/Micro Programmable Controllers Reference Manual*

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Henry A. Konat
Senior Technical Writer

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Chapter 1

Introduction and Product Description

The Series 90-20 Programmable Logic Controller (PLC) is a member of the GE Fanuc Series 90 PLC family of Programmable Logic Controllers. The Series 90-20 PLC is easy to install and configure, offers advanced programming features, and is designed for compatibility with other PLCs in the Series 90 family of PLCs. The Series 90-20 PLC provides a cost-effective platform for low I/O count applications. The primary objectives of the Series 90-20 PLC are:

- To provide a small PLC that is easy to use, install, upgrade and maintain.
- To provide a cost effective family compatible PLC.
- To provide easier system integration through standard communication hardware and protocols.

Series 90-20 PLC System Components

The Series 90-20 PLC system components include:

- I/O Power Supply Base Module (this will be referred to as the baseplate for the Series 90-20 PLC).
- CPU Module (CPU 211).
- Hand-Held Programmer or Logicmaster 90-30/90-20 programming software.

Series 90-20 PLC Features

The Series 90-20 PLC combines the desired features of the traditional PLC, with many improvements and product enhancements. The features traditionally found in most PLCs, include:

- An industrial computer that has been hardened to operate in the harsh environment commonly encountered in the factory.
- Familiar relay ladder diagram programming.
- I/O control through user logic programming.
- Instruction set designed specifically for the industrial control and process environment.
- Communications with cell controllers, operator interface terminals, dumb terminals, personal computers, and similar devices.

The Series 90-20 PLC adds an array of features including:

- Family compatibility throughout the entire Series 90 product line.

- Sophisticated Logicmaster 90-30/90-20 programming software.
- A configuration software package which provides for easy system configuration.
- An alarm processor function.
- No jumpers to set on boards.
- Hand-Held Programmer for programming in Statement List language.
- Password protection to limit access to PLC contents.

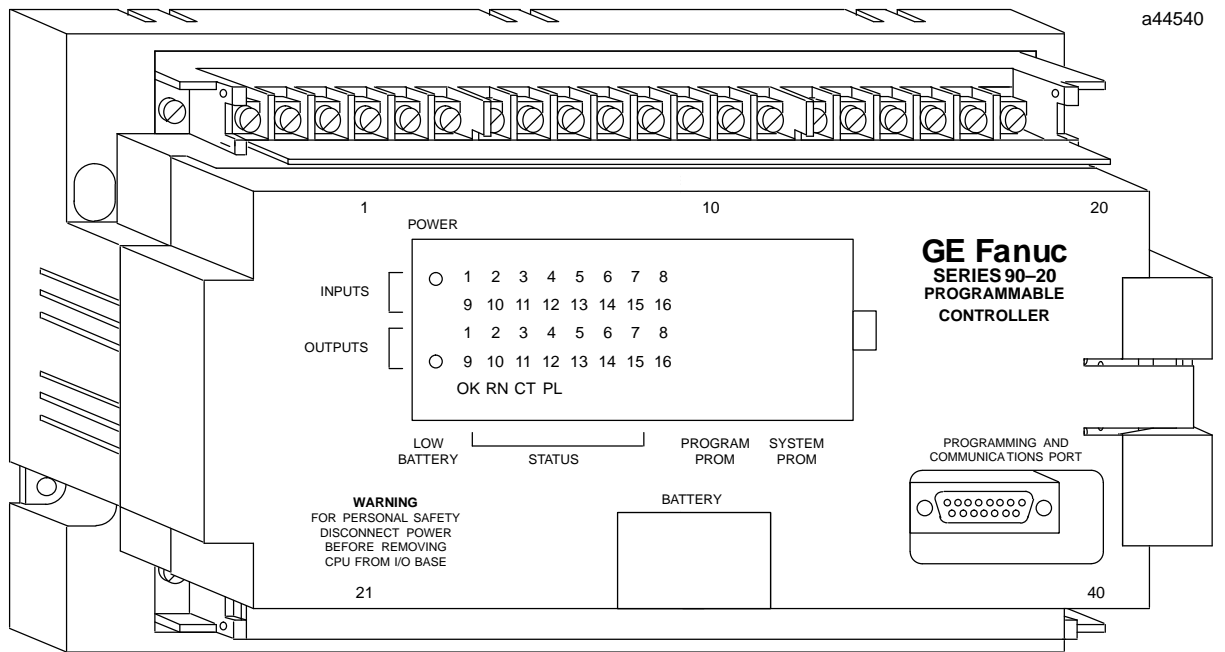


Figure 1-1. Series 90-20 CPU and I/O Power Supply Base Modules

Product Description

The Series 90-20 PLC hardware configuration consists of an I/O and Power Supply Base Module (baseplate) and a plug-on CPU module. The baseplate contains the discrete input and output circuits, the power supply and terminal strips for user field wiring. Different I/O and power supply combinations are available to meet the customers requirements for various applications. The CPU module executes and contains the user program, communicates to the programmer (Hand-Held Programmer or computer running Logicmaster 90-30 software), contains LED status indicators and a battery for backup of memory.

Table 1-1. Models of Series 90-20 PLCs

CatalogNumber	Description	I/O Points
IC692MAA541	I/O and Power Supply Base Module, 120 VACIn/120VACOut/120VAC Power Supply	16In/12 Out
IC692MDR541	I/O and Power Supply Base Module, 24 VDC In (Positive/Negative) RelayOut/120VAC Power Supply	16In/12 Out
IC692MDR741	I/O and Power Supply Base Module, 24 VDC In (Positive/Negative) RelayOut/240VAC Power Supply	16In/12 Out
IC692CPU211	CPU Module, Model CPU211	n/a

n/a = not applicable

CPU 211 Module

The primary capabilities of the Series 90-20, model CPU 211 Module are:

- 80188 Processor 8 Mhz
- Input Points - 16
- Output Points - 12
- High Speed Counter - 10Khz Type A Counter
- Maximum User Program - 1K (Words)
- Registers - 256 (Words)
- Internal Coils - 1024
- Memory Back-up with Lithium Battery. Typical Battery Life: 5 years
- Typical Scan Rate: 18.0 ms/1K of logic (boolean contacts)
- User PROM Option: user program can be stored to EEPROM; user program can be executed from user PROM (EPROM or EEPROM).
- LED status indicators for I/O and CPU status

LED Status Indicators

The CPU 211 module contains an LED matrix and two discrete LEDs that provide the user with a visual indication of the I/O and CPU status.

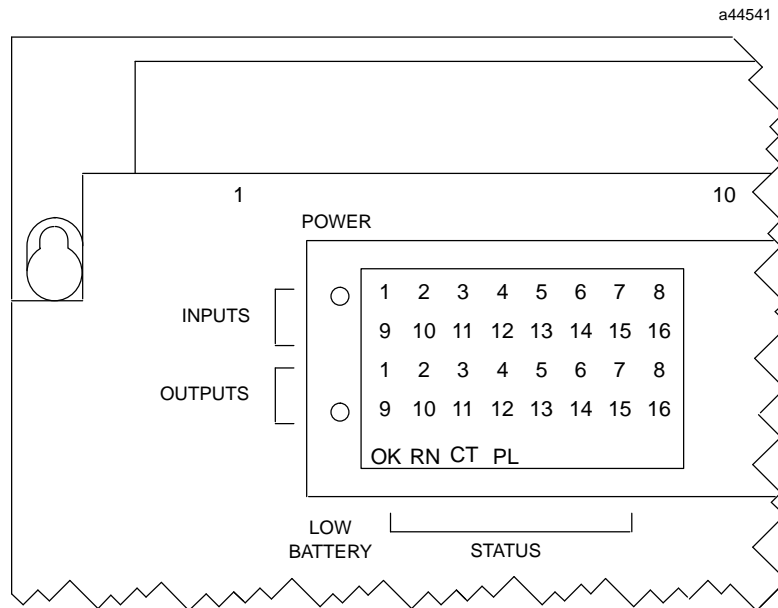


Figure 1-2. Series 90-20 LED Status Indicators

Discrete LEDs

- **POWER:** the top LED (green) provides an indication of the operating state of the power supply. The LED is ON when the power supply has a correct source of power and is operating properly and OFF when a power supply fault occurs or power is not applied.
- **LOW BATTERY:** the bottom LED (red) is ON if the memory backup battery voltage is too low to maintain the memory under a loss of power condition; otherwise it remains OFF. If this LED is ON the lithium battery must be replaced before removing power from the unit (see *Battery Replacement* in Chapter 2).

LED Matrix

- **INPUT STATUS:** the top two rows of LEDs indicate the status of the input points on the I/O baseplate. If the associated LED is ON, the voltage at the input point is high enough to energize the input circuit. If the LED is OFF, the voltage is too low to energize the input circuit (see *I/O Specifications* in Chapter 5 for thresholds). The input LEDs will indicate the input status in all CPU modes: STOP with I/O Disabled, STOP with I/O Enabled and RUN (Standard Sweep or Constant Sweep).
- **OUTPUT STATUS:** the third and fourth rows of the LED matrix indicate the status of the output points on the I/O baseplate. The LED is ON, when the output is commanded to turn ON (for example if %Q1 = 1, then output 1 LED will be ON).
- All outputs turn OFF in the STOP with I/O disabled mode. Outputs will hold last state or the present user commanded state in the STOP with I/O enabled mode. In the RUN mode, the outputs are controlled by the user's logic program.
- **USER DEFINED LEDs:** output LEDs, for example Q13 through Q16 indicate only the status of internal coils %Q13 through %Q16 since there are no physical output points on the I/O module for these references. These indicators are useful as user defined status indicators.
- **CPU STATUS:** the first two LEDs of the last row are used for CPU status.
 - **OK:** is ON if the PLC is operating properly and OFF if a problem is detected by the PLC.
 - **RN (RUN):** this LED is ON when the PLC is executing the logic program entered by the user (RUN mode).
- **High Speed Counter Status:**
 - **CT (COUNT):** this LED turns ON briefly when a count edge has been received by the High Speed Counter. If the count rate is high enough, the LED will appear to be ON continuously. Its purpose is to indicate active count pulses at the COUNT input on the I/O module.
 - **PL (PRELOAD/STROBE):** this LED turns ON when the High Speed Counter accumulator equals the preload value.

Battery Backup for RAM Memory

The long-life Lithium battery (IC693ACC301) used to maintain the contents of the CMOS RAM memory in the CPU is accessed by removing the cover plate located at the bottom of the CPU module. This battery is mounted on a plastic clip attached to the inside of this cover. The battery connects to the CPU through a cable which has one end

wired to the positive and negative sides of the battery and the other end wired to a connector that mates with one of two identical connectors mounted on the CPU circuit board. This battery may be replaced with power applied to the PLC. Alternatively, it may be replaced with power OFF or with the CPU module removed from the I/O baseplate by plugging a new battery into the unused connector on the CPU circuit board and then removing the old battery (see Battery Installation in Chapter 2, for details).

Caution

If a Low Battery Warning (BATT LED turns ON) occurs, replace the battery located in the power supply BEFORE removing power from the rack. Otherwise, there is a possibility that data will be corrupted or the application program will be cleared from memory.

CPU Serial Port

A 15 pin D-type, female connector, on the right side of the CPU module provides the connection to an RS-485 compatible serial port which is used to connect to Logicmaster 90-30/90-20 programming software, the Hand-Held Programmer or for general purpose communications using the Series 90 Protocol (SNP).

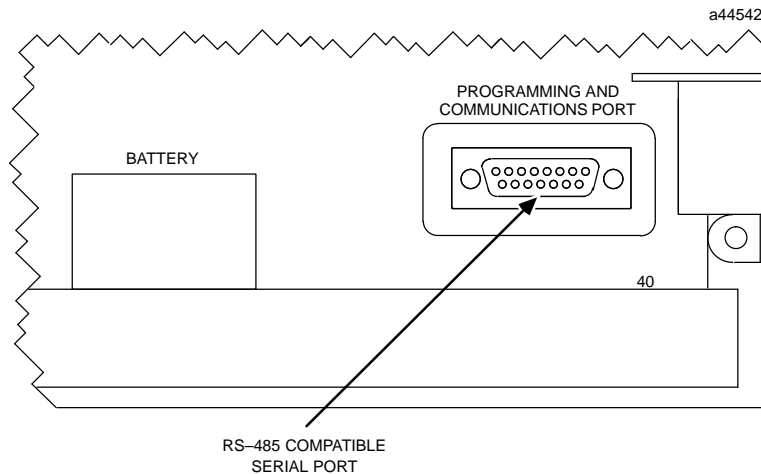


Figure 1-3. Serial Port for the Series 90-20 PLC

I/O Power Supply Base Module (Baseplate)

The I/O Power Supply Base Module (baseplate) provides user mounting locations, a power supply, I/O conditioning circuits (including the High Speed Counter input circuits) and terminal strips for user field wiring connections.

Mounting Locations

The baseplate unit can be mounted on a flat panel area by use of mounting screws on the ends of the unit (see installation instructions in Chapter 2 for more details). These mounting locations are in the same location as the Series 90-30 PLCs which are more advanced PLCs in the Series 90 family of PLCs.

Power Supply

The power supply converts the input source power to voltages required for internal circuitry. In addition, on modules with dc inputs it provides an isolated 24 VDC supply to power dc input circuits, switches and indicators. This 24 volt supply has a user replaceable fuse. You can access this fuse by removing the CPU module from the I/O base.

Input Circuits

The input circuits condition and filter high level dc or ac voltages (depending on the module type) so that they can be properly detected by the CPU module.

High Speed Counter Inputs

The High Speed Counter inputs condition dc signals which are used by the CPU module to implement a Type A counter. The Type A counter accepts a count input which increments a 16 bit accumulator and a preload/strobe input which can either preload the counter accumulator with a user defined value (PRELOAD mode) or strobe the accumulator (STROBE mode) into a 16 bit register (for more details on the High Speed Counter see Chapter 6).

Output Circuits

The output circuits allow the low level signals from the CPU module to control dc or ac output devices. Each common on a group of output points is fused and can be replaced by the user. Access to the fuses is obtained by removing the CPU module from the I/O base.

Connections to Removable Terminal Strips

The baseplate module provides two, 20 terminal removable terminal strips. These removable terminal strips allow modules to be changed without removing or rewiring field connections to the terminals. Terminal strip assignments are dependent on the module type but in general the input connections are on the top and the power supply and output connections are on the bottom.

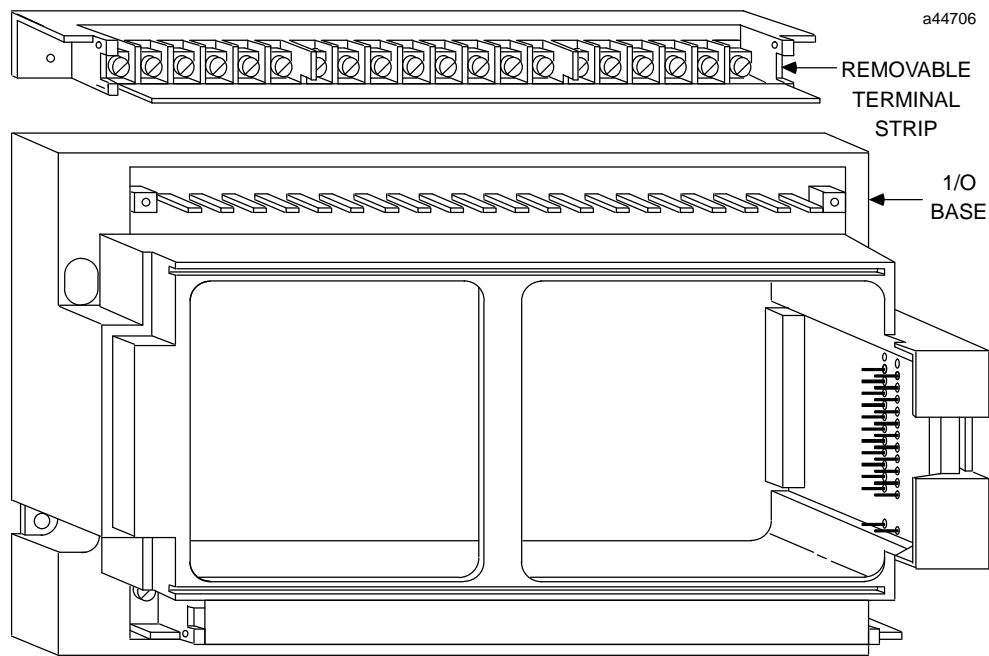


Figure 1-4. Removable Terminal Strip for User Field Wiring Connections

Programming and Configuration

Programming and configuration of the Series 90-20 PLC can be accomplished through two different methods.

- Logicmaster90-30/20/MicroProgramming software on a Workmaster II or Cimstar I industrial computer or an IBM® PC-XT, PC-AT,PS/2® (Personal System 2®) or compatible Personal Computer.
- Series90-30/20/MicroHand-HeldProgrammer (IC693PRG300).

Both configuration and programming can be done with the Logicmaster 90-30/20/Micro programmer off-line from the PLC. Configuration and programming with the Hand-Held Programmer must be done with the Hand-Held Programmer attached to and interfacing with the PLC.

Use of the programming and configuration software is described in the Logicmaster 90-30/20/MicroProgramming Software User's Manual, (GFK-0466). The Workmaster II computer is described in the Workmaster II PLC Programming Unit Guide to Operation Manual, GFK-0401. Use of the Hand-Held Programmer is described in the Series 90-30/20/MicroPLCHand-HeldProgrammer User's Manual, (GFK-0402).

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Programming and Configuration With the Hand-Held Programmer

The Hand-Held Programmer is used to develop, debug, and monitor ladder logic programs, and to monitor data tables. The Hand-Held Programmer can do the following:

- Statement List logic program development, including insert, edit, and delete functions. The Statement List programming instructions provide basic (boolean) instructions to execute logical operations such as AND and OR, and many functions to execute advanced operations including arithmetic operations, data conversion, and data transfer.
- On-line program changes.
- Search logic programs for instructions and/or specific references.
- Monitor reference data while viewing logic program.
- Monitor reference data in table form in binary, hexadecimal, or decimal formats.
- Monitor timer and counter values.
- View PLC scan time, firmware revision code and current logic memory use.
- Load, store, and verify program logic and configuration between the Hand-Held Programmer and a removable Memory Card (IC693ACC303) which allows programs to be moved between PLCs or loaded into multiple PLCs.
- Start or stop the PLC from any mode of operation.

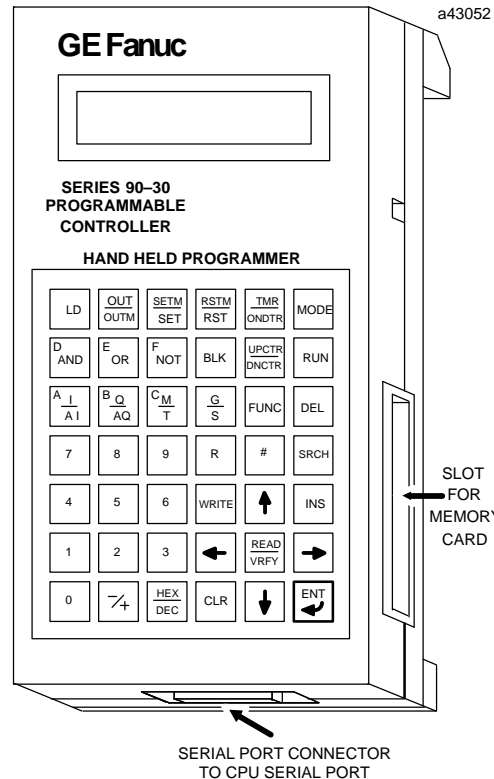


Figure 1-5. Hand-Held Programmer

The Hand-Held Programmer connects to the serial port (labeled Programming and Communications Port) on the CPU through a 15-pin D-type connector on the Series 90-20 CPU module. The physical connection is through a cable (IC693CBL303) that is 6 feet (2 meters) in length. This cable also provides power connections to the Hand-Held Programmer, and provides a signal that tells the PLC that a Hand-Held Programmer is attached. The Hand-Held Programmer can be connected or disconnected to a PLC that is powered-up.

Hand-Held Programmer Features

The keypad for the Hand-Held Programmer is a sealed type with tactile feedback, and has 42 keys, arranged in a matrix of six keys across by seven keys down. A two-line by 16 character LCD display screen provides a means of visual information to the user. The Hand-Held Programmer also provides an interface to a removable memory card (IC693ACC303). The Hand-Held Programmer can program EEPROM memory devices in the memory card, which will retain the program stored in it under no-power conditions. This memory cartridge, through the Hand-Held Programmer interface, provides a means for off-line storage and retrieval of the user's application program and system configuration data. The memory card plugs into a connector accessed through an opening on the lower right side of the Hand-Held Programmer. This feature provides program portability and a way to store different programs for use as required.

Hand-Held Programmer Modes Of Operation

The Hand-Held Programmer functionality is basically divided into four modes of operation which are selected through a key sequence on the keypad. These modes are: program mode, data mode, protection mode, and configuration mode.

Program Mode allows you to create, change, monitor, and debug Statement List logic. This mode also allows read, write, and verify functions with the optional memory card.

Data Mode allows you to view, and optionally alter values in various reference tables. Several display formats can be selected in which to view this data: binary, hexadecimal, signed decimal, and timer/counter.

Protection Mode provides a way to control access to (protection of) certain PLC functions, including program logic, reference data, and configuration information. The use of this function is optional; however, it is convenient in that it allows you to protect parts of the PLC system from accidental or deliberate modification. Protection is provided through four levels of passwords which you can assign.

Configuration Mode allows you to change CPU and High Speed Counter configuration items, such as baud rate, turning the keyclick feature on or off, High Speed Counter preload/strobe, and others.

Programming With Logicmaster 90-30/20/Micro Software

The Programming Software portion of the Logicmaster 90-30/20/Micro Software package can do the following:

- Develop ladder diagram programs off-line.
- Monitor and change reference values on-line.
- Edit a program on-line.
- Transfer programs and configurations between the PLC and programmer.
- Store programs automatically on disk.
- Annotate programs.
- Print programs with annotation and/or cross references.
- Display help information.
- Use symbolic references.
- Cut and paste program fragments.
- Print programs and configurations on various printers.

Configuration With Logicmaster 90-30/20/Micro Software

Configuration with the Configuration Software package, which is included as a part of the total Logicmaster 90-30/20/Micro Programming Software package, can do the following:

- Specify a name for the system.
- Enable the High Speed Counter and change parameters for the High Speed Counter.
- Configure CPU parameters.
- Archive or save the configuration in a file.
- Transfer configurations between the PLC and the programmer.

Instructions and Function Blocks

The Series 90-20 PLC supports many different instruction functions and function blocks. Detailed descriptions, including examples, on the use of these instructions can be found in GFK-0467, the Series 90-30/20/Micro Programmable Controllers Reference Manual, and GFK-0402, the Series 90-30/20/Micro Hand-Held Programmer User's Manual.

A summary of each of the instructions and functions is provided in the following group of tables.

Basic Instructions (Relay Ladder Contacts and Coils)

The following relay ladder contacts and coils are supported by the Series 90-20 PLC.

Basic Instruction	Description
--] [--	normally open contact
--] / [--	normally closed contact
--()--	coil
--(/)--	negated coil
--(S)--	set coil
--(R)--	reset coil
--(↑)--	positive transition coil
--(↓)--	negative transition coil
--(M)--	retentive coil
--(/M)--	negated retentive coil
--(SM)--	retentive set coil
--(RM)--	retentive reset coil
-----	horizontal link
	vertical link

Note

The mnemonics listed for the functions in the following tables are as they appear on the Hand-Held Programmer's display.

Timers and Counters

The Series 90-20 PLC supports two types of timers and two types of counters. All four function blocks are updated each time they are encountered in the logic; timers by the amount of time consumed by the last sweep, counters by one count.

Description	Mnemonic	Function Number
Stopwatch timer	TMR	10
On-delay timer	ONDTR	13
Up counter	UPCTR	15
Down counter	DNCTR	16

Math Functions

The Series 90-20 PLC supports six different math functions. Each function may be used on any of the following data types: Integer (INT) or Double Integer (DINT).

Description	Mnemonic	Function Number
Add	ADD	60
Subtract	SUB	62
Multiply	MUL	64
Divide	DIV	66
Modulo	MOD	68
Square Root	SQRT	70
Double Precision:		
Add	DPADD	61
Subtract	DPSUB	63
Multiply	DPMUL	65
Divide	DPDIV	67
Modulo	DPMOD	69
Square Root	DPSQRT	71

Relational Functions

The Series 90-20 PLC supports six different relational functions.

Description	Mnemonic	Function Number
Equal	EQ	52
Not Equal	NE	53
Greater Than	GT	57
Greater Than or Equal	GE	55
Less Than	LT	56
Less Than or Equal	LE	54
Double Precision:		
Equal	DPEQ	72
Not Equal	DPNE	73
Greater Than	DPGT	77
Greater Than or Equal	DPGE	75
Less Than	DPLT	76
Less Than or Equal To	DPLE	74

Bit Operation Functions

The Series 90-20 PLC supports 12 different bit operation functions.

Description	Mnemonic	Function Number
LogicalAnd	AND	23
LogicalOr	OR	25
Logicalexclusive OR	XOR	27
Logicalinvert	NOT	29
Shift bit left	SHL	30
Shift bit right	SHR	31
Rotate bit left	ROL	32
Rotate bit right	ROR	33
Set a bit to 1	BITSET	22
Set a bit to 0	BITCLR	24
Test a bit	BITTST	26
Locate a bit set to 1	BITPOS	28

Conversion Functions

The Series 90-20 PLC provides two different conversion functions: 4-digit binary coded decimal to 16-bit integer and 16-bit integer to 4-digit binary coded decimal.

Description	Mnemonic	Function Number
Integer To BCD	BCD	80
BCD To Integer	INT	81

Control Functions

The Series 90-20 PLC supports ten different Control Functions.

Description	Mnemonic	Function Number
Terminate program execution	ENDSW	0
No operation	NOOP	1
Nested jump	JUMP	3
Nested master control relay	MCR	4
Target number for jump	LABEL	7
Master control sequence end	ENDMCR	8
DoI/Oupdate	DOIO	85
System servicerequest:	SVCREQ	89
#6 Change/Read Checksum		
#13 Shut down PLC		
#14 Clear fault tables		
#15 Read last fault		
#16 Read elapsed time clock		
Standard PID algorithm	PIDISA	86
Independent term PID algorithm	PIDIND	87

Data Move Functions

The Series 90-20 PLC supports ten different Data Move functions.

Description	Mnemonic	Function Number
Constant block move	BMOVW	43
Block clear	BLKCL	44
Bit sequencer	SEQB	47
Shift register, word	SHFRW	45
Shift register, bit	SHFRB	46
Constant block move, integer	BMOVI	38
Multiple word move	MOVWN	42
Multiple integer move	MOVIN	37
Multiple bit move	MOVBN	40
Communications request	COMRQ	88

Table Functions

The Series 90-20 PLC supports seven different Table functions. The array search functions can be operated on by four different data types (Byte, Word, INT, DINT) and the array move function can be operated on by 5 different data types (Bit, Byte, Word, INT, DINT).

Description	Mnemonic	Function Numbers
Search equal to	SREQ	101 to 104
Search not equal to	SRNE	105 to 108
Search less than	SRLT	109 to 112
Search less than or equal to	SRLE	113 to 116
Search greater than	SRGT	117 to 120
Search greater than or equal to	SRGE	121 to 124
Copy array source to definition	MOVA	130 to 134

User References

Data in the Series 90-20 PLC programs is referenced by its address in the system. A reference specifies both a memory type and a precise address within that memory type. For example:

%I00001 specifies address 1 in input memory.
 %R00256 specifies address 256 in register memory.

The % symbol is used to distinguish machine references from nicknames and is only used with Logicmaster 90-30/20/Micro programming software. The % symbol is not used with the Hand-Held Programmer.

User Reference Types and Memory Size

The prefix of a user reference indicates where data is stored in the PLC. References in the Series 90-20 PLC are either discrete or register data types.

Table 1-2. Range and Size of User References for Series 90-20

Reference Type	Reference Range	Size
Userprogram logic	Notapplicable	1K words
Discrete inputs	%I0001 - %I0016	16 bits
Discrete inputs, internal	%I0017 - %I0048	32 bits
Discrete outputs	%Q0001 - %Q0012	12 bits
Discrete outputs, internal with LED indicators	%Q0013 - %Q0016	4 bits
Discrete outputs, internal	%Q0017 - %Q0048	32 bits
Discrete globals	%G0001 - %G1280	1280 bits
Discrete internal coils	%M0001 - %M1024	1024 bits
Discrete temporary coils	%T0001 - %T0256	256 bits
System status references	%S0001 - %S0032	32 bits
	%SA0001 - %SA0032	32 bits
	%SB0001 - %SB0032	32 bits
	%SC0001 - %SC0032	32 bits
System register references	%R0001 - %R0256	256 words
Analog and High Speed Counter inputs	%AI0001 - %AI0016	16 words
Analog outputs	%AQ0001 - %AQ0016	16 words
System registers †	%SR0001 - %SR0016	16 words

† For reference table viewing only; may not be referenced in a user logic program.

User Register References

Register data types are referenced as 16-bit words. The following types of references are word references:

- %AI - References analog inputs and the High Speed Counter. This prefix is followed by the actual address of the reference, for example: %AI0015 occupies one word containing 16 bits. %AI0016 would occupy the next 16 consecutive bits.
- %AQ - References analog outputs. This prefix is followed by the actual address of the reference, for example: %AQ0015 occupies one word containing 16 bits. %AQ0016 would occupy the next 16 consecutive bits.
- %R - This prefix is used to assign register references that will store word-oriented program data, such as the results of calculations. These references are retentive.

User Discrete References

Discrete references are addressed as individual bits of data. The following types of references are discrete references.

- **%I** - References discrete machine inputs. This prefix is followed by the reference's address in the status input table. For example, %I0012 is the reference for input number 12. The %I references are located in the input status table, which stores the states of inputs received from the hardware during the last input scan.
- **%Q** - References discrete machine outputs. This prefix is followed by the reference's address in the output status table. For example, %Q0012 is the reference for output 12. %Q references are located in the output status table, which stores the states of outputs as last set by the application program. The states of these references are retained through loss of power if not used as an output coil. When used with an output coil, %Q references are retentive with -(SM)-, -(RM)-, -(M)-, and -(/M)- coils and non-retentive with -(S)-, -(R)-, -()- and -(/)- coils.
- **%M** - This prefix is used to reference internal coils. They are used in boolean logic when the result will be used again in the program. The %M references are retained through loss of power if not used as an output coil. When used with an output coil, %M references are retentive with -(SM)-, -(RM)-, -(M)-, and -(/M)- coils and non-retentive with -(S)-, -(R)-, -()- and -(/)- coils. Since they do not represent actual machine outputs, any available location in %M memory can be assigned, for example, %M00064.
- **%T** - The %T prefix is used to reference temporary internal coils that are not retained through loss of power. Temporary coils function like the %M references, described above. However, they can be used as often as needed as conditional contacts to control logic within the user program.
- **%G** - The %G prefix is used to represent Global Data that is shared between multiple devices using the Genius Communications Module to communicate over a Genius I/O bus. These references have the same retentive properties as %M references.
- **%S** - The %S prefix references system memory. %S references are retentive. The %S memory for fault references has four sections: %S, %SA, %SB, and %SC. This memory is used by the PLC to store contact references that have special meaning, such as:

Reference	Nickname	Description
%SA0002	ov_swp	Exceeded constant sweep time
%SA0009	cfg_mm	System configuration mismatch
%SB0011	bad_pwd	Password Access Failure

The Series 90-20 PLC monitors internal operations for either system or user problems called faults. These faults are reported through the %S references and through an internal fault table. Access to %S information is available through Logicmaster 90-30/90-20 or with the Hand-Held Programmer. The fault table can only be accessed by Logicmaster 90-30/90-20. For more details on faults and fault reporting see Chapter 4.

Programmer Requirements For Running Logicmaster 90-30/90-20 Software

In order to run the Logicmaster 90-30/20/Micro programming software, you will need a programming computer, which can be one of the following:

- A computer with a hard disk:
 - A Workmaster II industrial computer with a 101-key keyboard, or
 - A personal computer with an Intel 80386 or higher processor and a minimum of 2 Megabytes of memory, or
 - A Zenith™ Mastersport™ SL notebook computer.
- At least 4 Megabytes of **free** disk space.
- Both the Logicmaster 90-30/Micro Release 5 WSI and the Standard Serial COM Port versions require a **minimum** of 556 KBytes (569,344 bytes) of available DOS application memory in order to run.

To run Logicmaster 90-30/20/Micro programming software, MS-DOS® Version 5.0 (or higher) must be installed on your computer. For detailed information on requirements for running Logicmaster software, refer to the *Logicmaster 90 Series 90-30/20/Micro Programming Software User's Manual*, GFK-0466. Logicmaster 90-30/20/Micro programming software provides foreign keyboard support, depending on the configuration of MS-DOS residing on the host computer. Consult your MS-DOS User's Manual for information on configuring for your country.

Logicmaster 90-30/20/Micro programming software communicates with the Series 90-20 PLC through a standard RS-232 port on the computer when used with an available RS-232 to RS-422/RS-485 converter or through a Work Station Interface (WSI) board (IC647WMI320) which must be installed in the computer to be used for programming. A serial cable provides the physical connection from the computer running Logicmaster 90-30/20/Micro programming software to the PLC. The connection to the Series 90-20 PLC is to a dedicated RS-485 compatible serial port connector located on the CPU module.

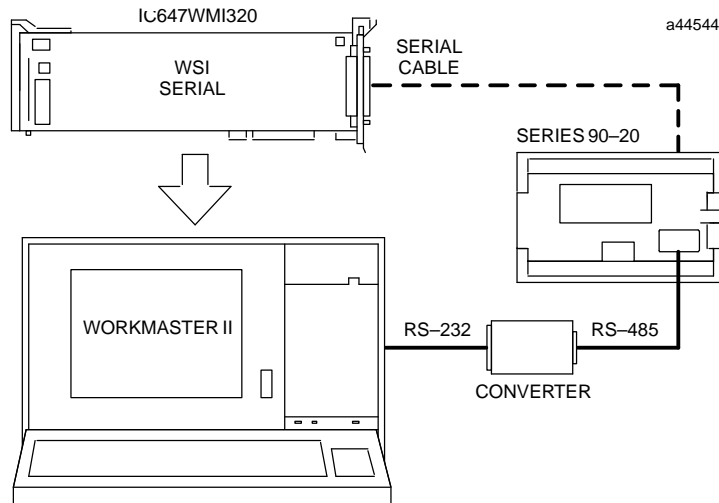


Figure 1-6. Logicmaster 90-30/90-20 Programmer Connection to the Series 90-20 PLC

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RS-422/RS-485 to RS-232 Converter

The RS-422/RS-485 to RS-232 Converter (IC690ACC900) provides an RS-232 interface to external devices requiring the RS-232 serial interface through conversion of the RS-422/RS-485 signals provided at the RS-422/RS-485 port in the Series 90-20 PLC. This converter provides a direct serial connection to a Workmaster II or other computer used as the programming device when running Logicmaster 90-30/20/Microprogramming software for the Series 90-20 PLC. You do not need to have a Work Station Interface installed in the Workmaster II computer when the converter is used.

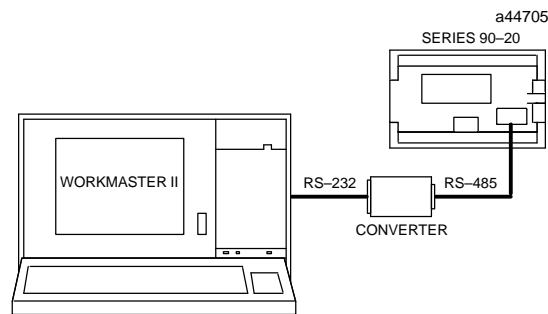


Figure 1-7. Example of RS-422/RS-485 to RS-232 Converter Connection in a Series 90-20 PLC System

This converter is a small, convenient, self-contained device which requires only a cable connection to the Series 90-20 RS-422/RS-485 port on one end and a cable connection to the RS-232 device on the opposite end.

The converter operates from a +5 VDC source, which is provided from the PLC backplane +5 VDC bus, through the cable connection. The pin assignments for the connections on the cable required for the RS-232 connection are compatible with available Series 90 Programmable Coprocessor Module (PCM) serial cables (IC690CBL701, PCM to Workmaster; IC690CBL705, PCM to Workmaster II; and IC690CBL702, PCM to PC-AT). The RS-422/RS-485 connection at the Series 90-20 serial port on the CPU module may be made with an available cable (used with the Hand-Held programmer), IC693CBL303.

The three PCM compatible cables (IC690CBL701/702/705) are 10 feet (3 meters) in length, and the Hand-Held Programmer compatible cable (IC693CBL303) is 6 feet (2 meters) in length. For those user's who may want to build their own cables, pin assignments and recommended cable types for both cables required for use with the converter are provided in Chapter 2, Installation.

Miniconverter Kit

The Miniconverter Kit (IC690ACC901) consists of an RS-422 (SNP) to RS-232 Miniconverter, a 6 foot (2 meter) serial extension cable, and a 9-pin to 25-pin Converter Plug assembly. The 15-pin SNP port connector on the Miniconverter plugs directly into the serial port connector on the Series 90-20 CPU. The 9-pin RS-232 port connector on the Miniconverter connects to an RS-232 compatible device.

When used with an IBM PC-AT, or compatible computer, one end of the extension cable plugs into the Miniconverter's 9-pin serial port connector, the other end plugs into the

9-pin serial port of the computer. The Converter plug (supplied with kit) is required to convert the 9-pin serial port connector on the Miniconverter to the 25-pin serial port connector on the GE Fanuc Workmaster II computer, or an IBM PC-XT or PS/2 Personal Computer.

The GE Fanuc Workmaster computer requires an additional adapter (not supplied with kit - please contact your local GE Fanuc PLC distributor) for use with the Miniconverter.

The Miniconverter is shown in the following figure. For more information on the Miniconverter, refer to Appendix C.

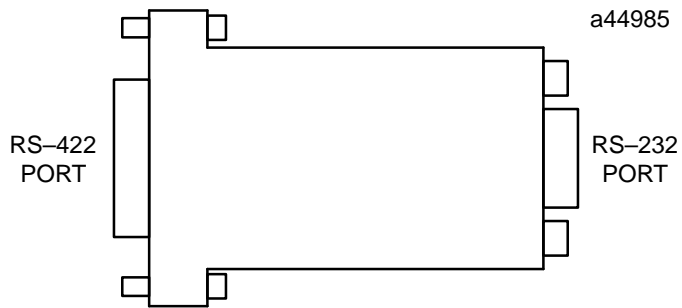


Figure 1-8. Series 90 SNP to RS-232 Adapter

Module Specifications

General specifications for the Series 90-20 Programmable Logic Controller are given in the following table.

Operating Temperature:	0° to 60°C 32° to 140°F (inlet air at bottom of rack)
Storage Temperature:	-40 ° to 85°C -40 ° to +185°F
Humidity:	5% to 95% (non-condensing)
Vibration:	3.5mm peak-to-peak displacement 5-10Hz, 1G 10-200 Hz
Shock:	15 g's for 11 msec
AC Power Source	Refer to I/O Power Supply specifications for each model in Chapter 5.
Module Weight:	
CPU Module:	0.69 pounds (.31 kg)
I/O Power Supply Base:	3.81 pounds (1.73 kg)
Module Dimensions:	
CPU and I/O Module Connected	Height: 6.4" (162.6mm) Width: 10.5" (266.7mm) Depth: 3.54" (89.9mm)
Back-up Battery Type:	Lithium, long-life
Typical Battery Life	About 5 years
Battery Shelf Life	8 to 10 years
Typical Scan Rate:	18.0ms/1K of logic (boolean contacts)
Maximum number of Discrete Physical I/O Points:	28 (16 inputs/12 outputs)

Chapter 2

Installation

This chapter describes the procedures for installing the Series 90-20 PLC and preparing the system for use. Included in this chapter are instructions for unpacking, inspecting, installing the I/O Power Supply Base on a panel, installing the CPU module and connecting required cables to programming devices.

You should verify that all components of the system have been received and that they agree with your order. If the modules you receive do not agree with your order, call GE Fanuc Programmable Control Customer Service, toll free, in Charlottesville, VA at 1-(800) 432-7521. A Customer Service representative will then give you further instructions. The hardware required for a Series 90-20 PLC system includes:

- I/O and Power Supply Base module, which contains a power supply, I/O circuitry and I/O terminal strips for user field connections
- CPU module
- Programmer, either Logicmaster 90-30/20/Micro and a computer to run it on, or a Series 90-30/20/Micro Hand-Held Programmer
- Appropriate cables to connect the programming device to the PLC.

Hardware Packaging

Each of these items are shipped separately and must be connected by the user. The I/O Power Supply Base Module is designed for panel mounting. The CPU Module connects to the I/O Power Supply Base Module. A copy of this manual, GFK-0551, is packed with the Hand-Held Programmer. This manual is also available on CD-ROM. Additional copies of the manual, if required, may be ordered through your local GE Fanuc PLC distributor or GE Fanuc sales office.

If the PLC is to be programmed using Logicmaster 90-30/20/Micro Programming software, a Workmaster II, Cimstar I, or an IBM or compatible computer is required to run the software. The Workmaster II or Cimstar I computer, if ordered, is packed in a separate shipping container. Logicmaster 90-30/20/Micro software can use either a Work Station Interface (WSI) board or a standard RS-232 interface with an available RS-422/RS-485 to RS-232 converter. A cable for connecting the Work Station Interface board installed in the computer or cables for connecting the converter must be ordered as separate items, and are packed separately.

GE Fanuc recommends that all shipping containers and packing material be saved should it be necessary to transport or ship any part of the system.

The Hand-Held Programmer, and the Hand-Held Programmer cable are both packed in separate shipping containers. The Series 90-30/20/Micro Hand-Held Programmer User's Manual, GFK-0402, is packed with the Hand-Held Programmer (IC693PRG300).

Visual Inspection

Upon receiving your Series 90-20 PLC system, carefully inspect all shipping containers for damage which may have incurred during shipping. If any part of the system is damaged, notify the carrier immediately. The damaged shipping container should be saved as evidence for inspection by the carrier.

As the consignee, it is your responsibility to register a claim with the carrier for damage incurred during shipment. However, GE Fanuc will fully cooperate with you, should such action be necessary.

Pre-installation Check

After unpacking the Series 90-20 PLC I/O Power Supply Base Module, CPU Module, and programmer, record all serial numbers. These serial numbers may be required if you should need to contact GE Fanuc Product Service during the warranty period of the equipment.

Verify that all components of the system have been received and that they agree with your order. If the system received does not agree with your order, call Programmable Control Customer Service, toll free, in Charlottesville, VA at 1-800-432-7521. A Customer Service representative will then give you further instructions.

Technical Help

If at any time you need technical help, PLC Technical Support can be reached at the PLC Hotline, or via Internet or fax as listed below.

Technical support for the PLC items described in this manual:

PLCHotline	1-800-828-5747(or804-978-5747)
Internetaddress	PLCHOTLINE@CHO.GE.COM
Faxnumber	804-978-5099

Packing List

The following described items make up a Series 90-20 PLC system. The first thing you should do is unpack all shipping cartons and verify the contents.

I/O Power Supply Base Module

The I/O Power Supply Base Module package should include the following items:

- Module - aluminum base module with an open plastic cover and removable terminal strips.
- IPI (Important Product Information) document - this includes information not in this manual when it was printed, such as updated specifications and errata.

CPU Module

The CPU Module package should include the following items:

- Module - this will be a plastic module containing one circuit board and a battery assembly.
- IPI Sheet (Important Product Information) this includes information not in this manual when it was printed, such as updated specifications and errata.
- Series 90-20 PLC User's Manual.

Minimum Requirements

In order to set-up and install the Series 90-20 PLC, you will need the following items:

- CPU Module
- I/O Power Supply Module
- Programming device: this can be one of the following items:
 - A. Hand-Held Programmer and cable
 - B. Logicmaster 90-30/20/Micro programming software, a Workmaster II or Cimstar I industrial computer, or an IBM PC-XT, AT, PS/2 or compatible personal computer and appropriate cables
 - C. Pre-programmed PROM that will run the Series 90-20 PLC
- Appropriate tools for mounting the PLC and connecting field wiring cables.

Pre-Installation Set-Up

You are not required to configure any jumpers or make any other settings on the modules.

Installation

This section describes the steps required for installation of your Series 90-20 PLC.

I/O Power Supply Base Installation

The Series 90-20 PLC I/O Power Supply Base Module must be panel mounted (normal orientation is on a vertical surface). Each Base has standard attachment flanges for mounting on an electrical panel. Base dimensions and proper spacing requirements for installation purposes are shown in the following figure.

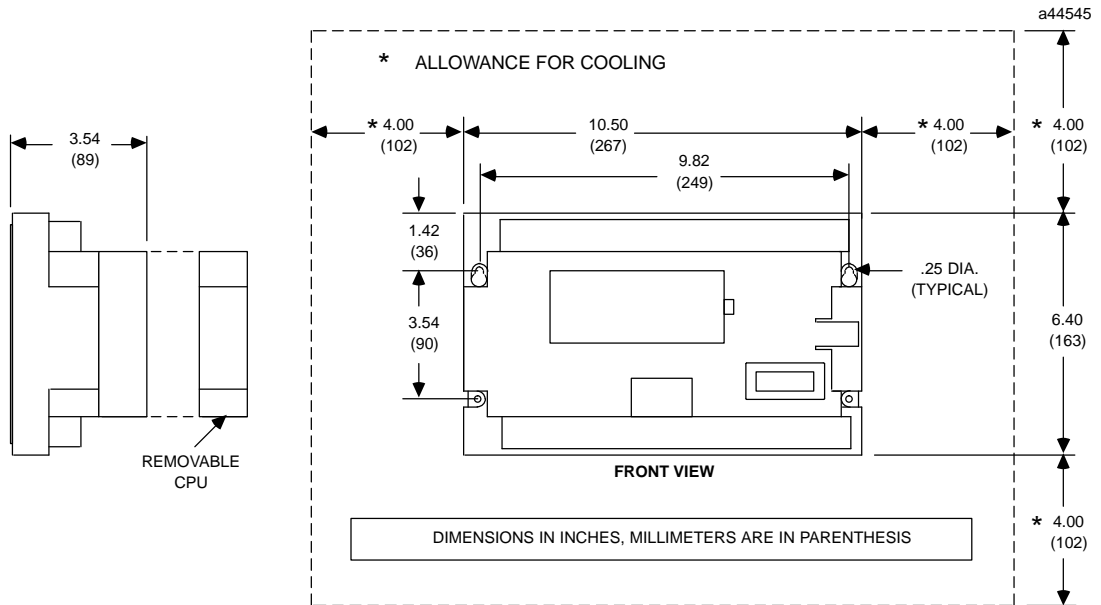


Figure 2-1. Series 90-20 Baseplate Mounting Dimensions and Spacing Requirements

Note

For non-standard orientation, derate the operating temperature (0°C to 60°C (32°F to 140°F)) by 10°C (18°F).

Power Supply Connections

Connect a power source meeting specifications of the I/O Base Module you are installing (See Chapter 5 for detailed specifications). For example, 120 VAC power supply modules require a 102 to 132 VAC, 50/60 Hz power source.

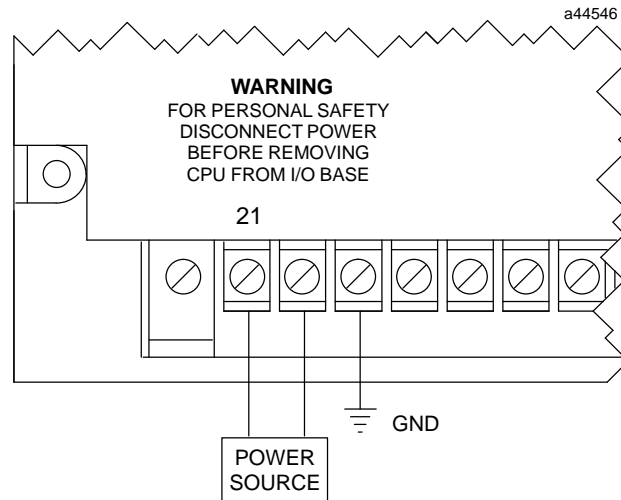


Figure 2-2. Series 90-20 Power Source Connections

Warning

Ensure that the protective cover is installed over terminals on the terminal board when power is applied to the unit. The cover protects against accidental shock hazard which could cause severe or fatal injury to the operator or maintenance personnel.

Grounding Requirements

The Series 90-20 PLC and the devices it is controlling must be properly grounded. This is particularly important for the reasons listed below.

- A low resistance path from all parts of a system to earth minimizes exposure to shock in the event of short circuits or equipment malfunction.
- The Series 90-20 PLC system requires proper grounding for correct operation.

The importance of a properly grounded system cannot be over emphasized.

Ground Conductors

- Ground conductors should be connected with separate branches routed to a central earth ground point. This method is shown in the following figure.
- Ground conductors should be as short and as large in size as possible. Braided straps or ground cables (typically green insulation with a yellow tracer - AWG #12 (3.33 mm²) or larger) can be used to minimize resistance. Conductors must always be large enough to carry the maximum short circuit current of the path being considered.

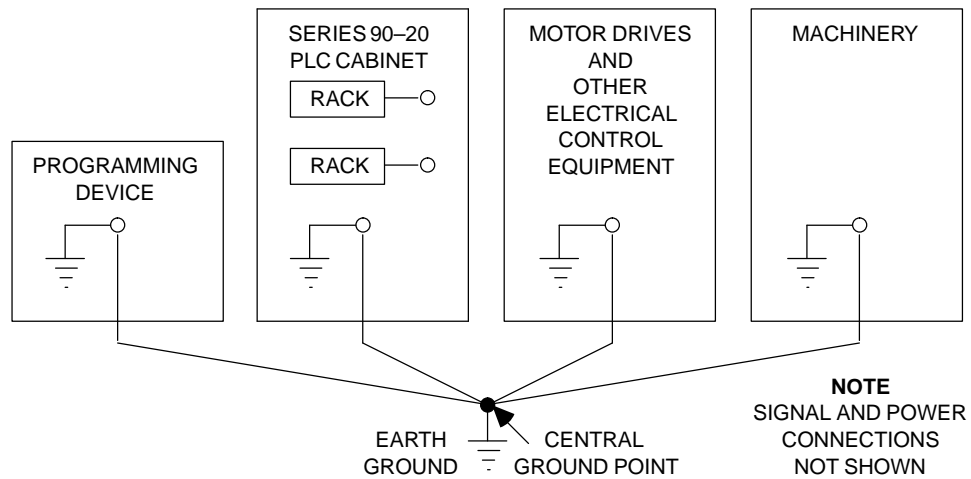


Figure 2-3. Recommended System Grounding

Series 90-20 Grounding Procedures

Equipment grounding recommendations and procedures are listed below. These grounding procedures must be properly followed for safe operation of your Series 90-20 PLC system.

The metal back of the I/O power supply base is ground, when properly installed. Safety and Reference ground connections should be made from one of the mounting tabs to earth ground using a minimum AWG #12 (3.33 mm²) wire and a ring terminal. Use of a nut and star washer for each wire on the ground connection lug is recommended to ensure adequate grounding.

Warning

The baseplate must be grounded to minimize electrical shock hazard which could result in severe personal injury to the operator or maintenance personnel.

The best way to provide proper ground connections is to ensure that the Series 90-20 PLC I/O base metal frame is directly connected to the control panel in which the baseplate or baseplates are mounted. This can be accomplished by connecting a ground strap from one of the ground lugs on either side of the baseplate to the control panel or cabinet following applicable electrical safety codes.

Logicmaster Programming Device Grounding

For proper operation, the programmer for Logicmaster 90-30/20/Micro (Workmaster II or Cimstar I, or IBM-PC or compatible computer) must have a ground connection in common with the Series 90-20 PLC. Normally, this common ground connection is provided by ensuring that the programmer's power cord is connected to the same power source (with the same ground reference point) as the baseplate.

I/O Installation and Wiring

The following discussion pertains to information on installing and wiring I/O points and information relevant to field wiring to and from those I/O points. **When installing a**

terminal board that has wiring attached verify that the terminal board is connected to the proper module type.

Wiring To I/O Circuits

In most cases, wiring connections to and from user supplied input and output field devices are terminated at two detachable terminal boards supplied with each I/O Power Supply Base module.

Terminal Board Wiring

Removable terminal boards make it easy to pre-wire field wiring to the user supplied input and output devices, and to replace modules in the field without disturbing existing field wiring.

The I/O terminal boards have 20 screw terminals and will accept up to one AWG #12 (3.33 mm²) wire or two AWG #14 (2.10 mm²) wires using lug type terminals.

The following figure shows the alignment and screw locations for installation or removal of a terminal board. Recommended procedures for installation and removal of a terminal board are described following the figure.

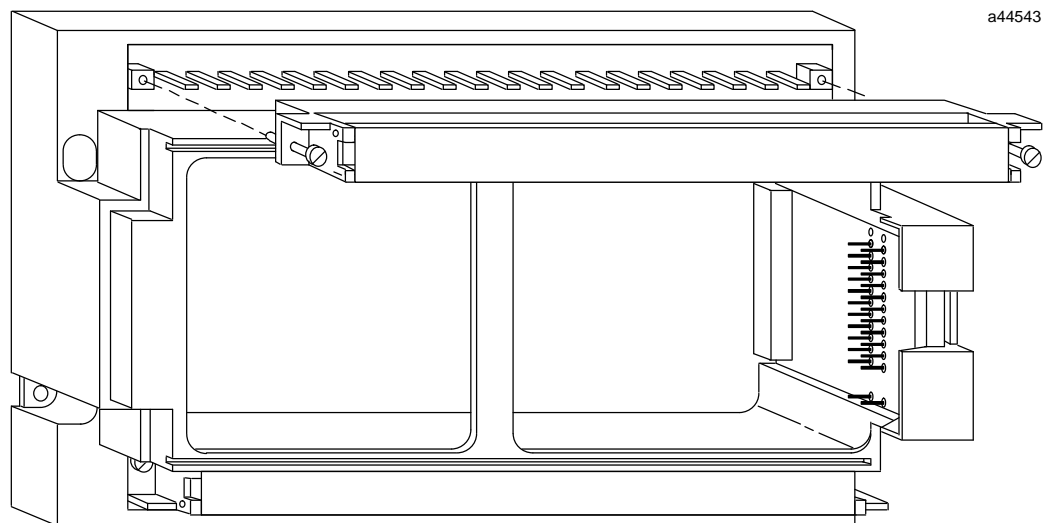


Figure 2-4. Installing/Removing a Terminal board

Installing a Terminal Board

To install a terminal board with no wires attached:

- Line up the terminal board with the terminal pins.
- Push the terminal board towards the module until it snaps into place.
- Lock the terminal board in place by tightening the screws at the end of the terminal board.

When installing a terminal board that has wiring attached verify that the terminal board is connected to the proper module type.

Caution

Check the label on the hinged door and the label on the module to ensure that they match. If a wired terminal board is installed on the wrong module type, damage to the module may incur.

Removing a Terminal Board

To remove a terminal board:

- Unscrew the lock down screws at each end of the terminal strip.
- Pull up on the terminal strip.

Field Wiring Considerations And Planning

It is recommended that the following procedures be followed when routing and connecting field wiring from user input devices to the PLC or to output devices (loads) to be controlled by the PLC.

- All low level signal wires should be run separately from other field wiring.
- AC power wiring should be run separately from DC field wiring.

Warning

You should calculate the maximum current for each wire and observe proper wiring practices. Failure to do so may cause injury to personnel or damage to equipment.

- Do not route field wiring close to any device that could be a potential source of electrical interference.
- If severe noise problems are present, additional power supply filtering or an isolation transformer may be required.
- Ensure that proper grounding procedures, as previously described, are followed to minimize potential safety hazards to personnel.
- Label all wires to and from I/O devices. Record circuit identification numbers or other pertinent data on the inserts which go in the terminal board cover.

I/O Connections

I/O connections are defined on the terminal board hinged cover label. In addition, wiring examples are given for each I/O baseplate in Chapter 5.

CPU Module Installation

Attachment to the I/O Power Supply Base

Attach the CPU module to the I/O Power Supply Base by placing the CPU module flush with the top of the I/O Power Supply Base and sliding the module towards the mating connector as shown in the following figure. The CPU module should be pushed toward the mating connector until the CPU hook fully engages the I/O Base catch.

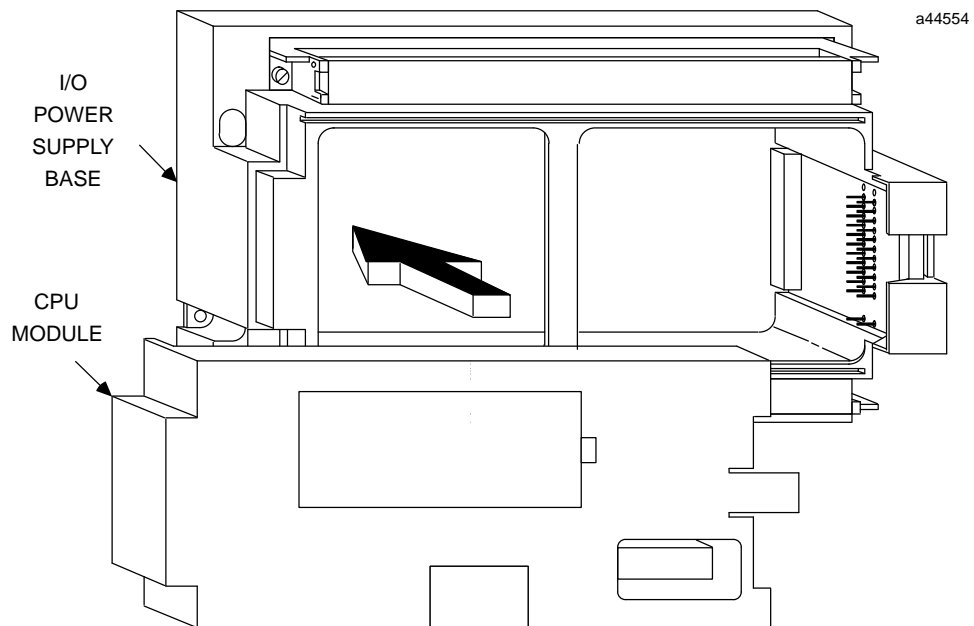


Figure 2-5. CPU Module Attachment to the I/O Power Supply Baseplate

The CPU module is shipped from the factory with an insert placed between the LED matrix and the LED cover to ensure that the matrix is not dislodged during shipment. Lift the cover, remove this insert and push the cover back down before using the Series 90-20 PLC.

Battery Installation

The CPU has battery-backed CMOS memory. This battery is connected at the factory. If the red *LOW BATTERY* LED comes on after providing power to the baseplate, it is an indication that the battery may not be connected. To access the battery, remove the plastic cover at the bottom of the CPU. To connect a battery, use the following procedure:

- Remove the cover at the bottom of the CPU Module to access the battery mounting clip on back of this cover and the two connector receptacles mounted on the printed circuit board.
- Orient the battery connector with one of the mating receptacles on the board. The guide tab on the battery connector should face away (be towards you) from the circuit board for proper orientation.
- Push the battery connector into the receptacle so that it locks into place.
- Replace the battery door cover.

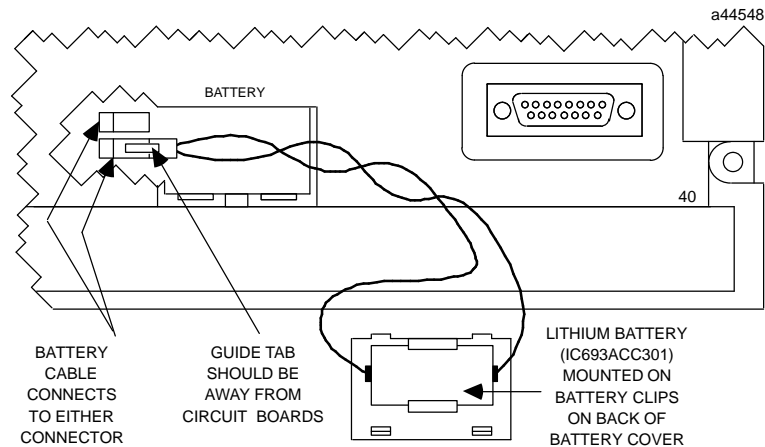


Figure 2-6. Battery Location and Mounting

Power-Up/Verification

After the proper power connections have been made to the I/O base and you have mounted the CPU module on the I/O base, the Series 90-20 PLC can be powered up to verify that the unit is installed correctly.

Normal Power-up Sequence

Apply the required power to the power inputs of the I/O base.

- The Power LED (Green) should turn ON.
- The CPU status LED labeled OK in the LED matrix should turn ON.
- If a user PROM has been installed, the CPU status LED labeled RN in the LED matrix should turn ON if the unit is configured to RUN on power-up.
- If any of the input points have been wired to field devices that energize those circuits, then the corresponding input LEDs should turn ON. Otherwise, all input LEDs will be OFF.
- If the RN LED is OFF, then all output LEDs should be OFF (in the Stop with I/O Disabled mode).

Error Detection And Correction

One of following conditions will be observed after applying power if there is a problem with the unit.

- Power LED OFF. This indicates that either an improper power source is connected to the module or that the I/O base power supply is not working properly.
 - Check that the proper power source is provided and is ON.
 - Check wiring to the I/O base unit to be sure wiring is connected correctly.
 - Check that the CPU Module is fully mated with the I/O base.

- Check that the line fuse located in the lower left corner of the I/O base unit is not blown. Refer to Chapter 5 for details on fuse location and replacement for each type of I/O base.

If none of these actions corrects the problem, then contact the GE Fanuc PLC Technical Hot Line at 1-(800)-828-5747 for assistance.

- Power LED ON but OK LED is OFF. This indicates that the power source is good but that the CPU module has detected an internal fault.
 - Remove power, then remove the CPU module and verify that the 24 volt fuse on the I/O circuit board is good. If it is open, replace the fuse with a .25A 3AG fuse (GE Fanuc replacement part number 259A9578P6).
 - Remove the CPU Module and check that the I/O baseplate pins that connect to the CPU module are all aligned properly. Then, reconnect the CPU module and ensure that the module is connected properly to the I/O baseplate.

If none of these actions corrects the problem, then contact the GE Fanuc PLC Technical Hot Line as described above.

After verifying that a good power sequence has occurred, attach a programming device (Hand-Held Programmer or computer with Logicmaster 90-30/20/Micro programming software) to start developing programs and setting up configuration for the unit.

Programming and Configuring the Series 90-20 PLC

The Series 90-20 PLC can be programmed and configured using either the Hand-Held programmer or with Logicmaster 90-30/20/Micro programming software. Both of these methods are described on the following pages.

Using the Hand-Held Programmer

The Hand-Held Programmer is a compact programming device which connects to the Series 90-20 PLC 15-pin serial port through a 6 foot (3 meters) cable (IC69CBL303).

The Hand-Held Programmer cable provides the connections which allow the Hand-Held Programmer and the PLC to communicate. This cable supports the RS-485 specification. It also provides the power connections for the Hand-Held Programmer, and a signal which indicates to the PLC that the Hand-Held Programmer, as opposed to another device is attached to the serial port. To connect the Hand-Held Programmer cable for the first time:

- Attach the 15-pin male D connector on one end to the mating 15-pin female D connector on the Hand-Held Programmer.
- Attach the connector on the other end of the cable to the connector on the CPU Module. These connections are shown in the following figure.

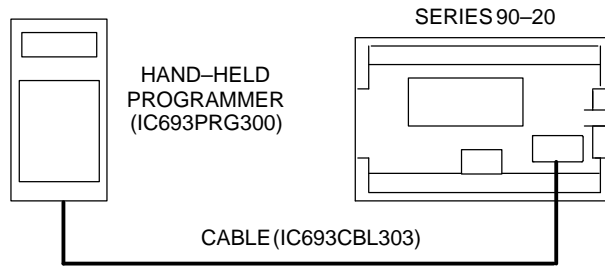


Figure 2-7. Hand-Held Programmer Cable Connection to a Series 90-20 PLC

Warning

Always connect the cable to the Hand-Held Programmer first, then connect the cable to the Series 90-20 PLC. This avoids any chance of shorting the +5 volt supply on the PLC which could cause incorrect operation of the Series 90-20 PLC. Incorrect operation of the PLC could damage the equipment or cause personal injury to an operator.

Initial Programming With the Hand-Held Programmer

The following screen (Main Menu) will be displayed on the Hand-Held Programmer after the Series 90-20 PLC has successfully completed its power-up sequence.

```
__ 1. PROGRAM <S
   2. DATA
```

This initial screen allows you to select the mode of operation of the program. The choices are: PROGRAM, DATA, PROTECT and CONFIGURATION. Use of the Hand-Held Programmer for the Series 90-20 PLC is the same as the Series 90-30 PLC in the PROGRAM, DATA and PROTECT modes. For information on using these modes please refer to the Series 90-30 and 90-20 PLC Hand-Held Programmer User's Manual, GFK-0402.

Hand-Held Programmer Configuration Screens for the Series 90-20 PLC

The configuration screens differ from the Series 90-30 PLC because the I/O and power supply on the Series 90-20 PLC are fixed. Enter the configuration mode by pressing the 4 key then the ENT key from the Main Menu screen. The up and down cursor keys allow you to move between power supply configuration, CPU configuration, Input configuration, Output configuration, and High Speed Counter configuration. The left and right arrows allow selection of parameters within each of the configurations.

```
R0:01 PLC <S
KEY CLK: OFF
```

This screen indicates that the CPU function is located in rack 0 and slot 01 (R01:01). For compatibility with the Series 90-30 PLCs, the different functions mimic the rack and slot locations. The Series 90-20 PLC system is always in rack 0. The following table shows the fixed slot assignments for the different functions.

Slot	Function	Fixed/Configurable
0	Power Supply	Fixed
1	CPU Parameters	Configurable
2	Input Locations	Fixed: %I1 to %I16
3	Output Locations	Fixed: %Q1 to %Q12
4	High Speed Counter	Configurable

If you want to transfer a Series 90-20 PLC developed program to a Series 90-30 PLC, the I/O modules in the Series 90-30 PLC must be in the above listed rack and slot locations for the program and configuration to work properly.

The screen shown above also shows the first configuration item which allows you to change the Hand-Held Programmer Key Click feature. The default is KEY CLK: OFF.

Selecting the up arrow key cause the next screen to be displayed:

```

R0:00 PWR SUP <S
IO BASE: I16/Q12

```

This screen indicates that the baseplate located at rack 0 and slot 00 is a generic 16 Input/12 Output type of module. Since this is the only type of baseplate available, it is not a configurable item.

Pressing the down arrow key causes the previous screen to be displayed:

```

R0:01 PLC <S
KEY CLK: OFF

```

Use the left and right arrow keys to view the other CPU parameters for configuration and the -/+ key to select the items within each parameter. Refer to the Series 90-30 and 90-20 PLC Hand-Held Programmer Manual, GFK-0402, for detailed information on configuration.

Pressing the down arrow key again causes the input screen to be displayed:

```

R0:02 I <S
I16:I0001-I0016

```

On the Series 90-20 PLC, the input points are configured in logical rack 0 and slot 02. If the program is transferred to a Series 90-30 PLC, the input module should be located in the first I/O slot (slot 02 on the Model 331, 340, 341, or 351, and slot 01 on the Model 311 and Model 313).

Pressing the down arrow key again causes the output screen to be displayed:

```
R0:03 Q          <S
Q16:Q0001-Q0016
```

On the Series 90-20 PLC, the output points are configured in logical rack 0 and slot 03. If the program is transferred to a Series 90-30 PLC, the output module should be located in the second I/O slot (slot 03 on the Model 331, 340, 341 and 351, and slot 02 on the Model 311 and Model 313).

Pressing the down arrow key again causes the first High Speed Counter screen to be displayed:

```
R0:04 HSC       <S
I16:I0033-I0048
```

On the Series 90-20 PLC, the High Speed Counter is configured in logical rack 0 and slot 04. If the program is transferred to a Series 90-30 PLC, the High Speed Counter module should be located in the third I/O slot (slot 04 on the Model 331, 340, 341 or 351, and slot 03 on the Model 311 and Model 313).

The rest of the High Speed Counter configuration screens are discussed in Chapter 6, *Series 90-20 High Speed Counter*.

Using Logicmaster 90-30/20/Micro Software

There are two types of Logicmaster 90-30/20/Micro programming software packages; each uses a different cable and interface hardware. The first type uses a standard RS-422 or RS-232 serial communications port on the IBM-PC compatible computer. Examples of cable connections for this type of interface are shown below. Note that the converter shown in the figures is catalog number IC690ACC900, you can also use the miniconverter kit, catalog number IC690ACC901 (see Appendix C for details).

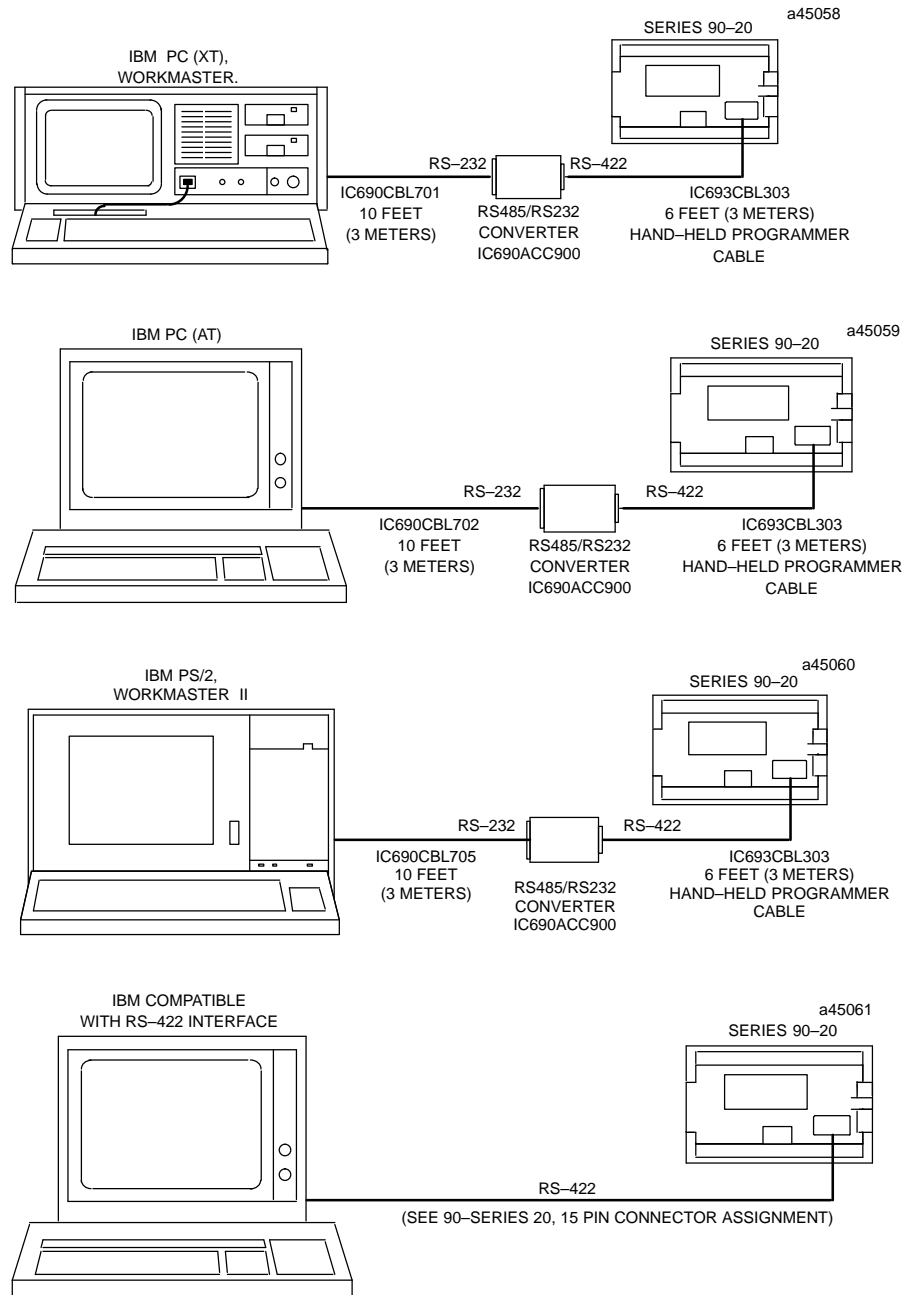


Figure 2-8. Examples of Serial Connection from Series 90-20 PLC to Programmer

Catalog numbers for the software package for the above connection are IC640HWP306 (software with converter and cable) or IC640SWP306 (software only).

The second type of Logicmaster 90-30/20/Micro Software package uses a Work Station Interface board installed in the computer. The cable connection for this configuration is from the connector on the Work Station Interface board to the Series 90-20 serial port as shown below.

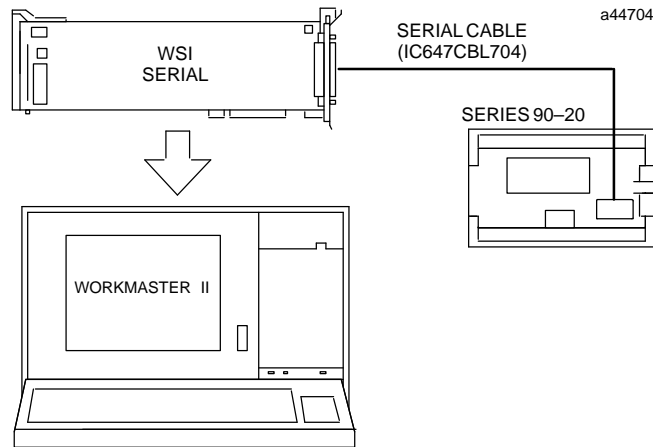


Figure 2-9. Logicmaster 90-30/20/Micro Programmer Connection through a Work Station Interface

Catalog numbers for the software package for the above connection are IC640HWP310 (for IBM PC-AT), IC640HWP322 (for IBM PS-2), and IC640SWP301 (software only).

Building a Serial Communications Cable

The following information is provided for those users who may want to build a serial cable with a different length for connecting a Series 90 PLC to a Workmaster II computer.

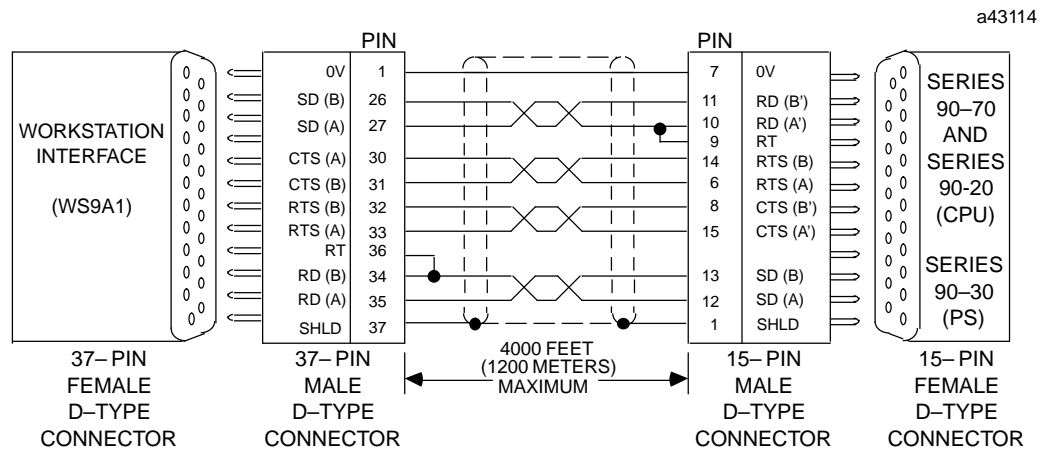


Figure 2-10. Series 90 PLC to Workmaster II Serial Cable

- Cable Type - 24 AWG (0.22 mm²), 30V computer grade. Extra flexible construction recommended for short lengths.
- Connectors - 37-pin male D-type with 4-40 screws and AMP hood No. 1-207908-0 or equivalent. 15-pin male D-type with M3 screws and AMP hood No. 1-207908-4, or equivalent. Note that an AMP connector is not supplied with M3 (metric) screws.
- Hardware kit - AMP 207871-1. Kit includes two M3 metric screws and two screw clips.

Serial Port Compatibility

Termination resistance for the Receive Data (RD) signal needs to be connected only on units at the end of the line. This termination is made on the Series 90 PLC products by connecting a jumper between pins 9 and 10 inside the 15-pin D-shell with the following exception. For the Series 90-20 PLC, catalog number IC692CPU211A, version R03 board and earlier (as marked on the circuit board inside the module), the termination for the RD at the PLC is implemented by a jumper between pins 9 and 11, instead of 9 and 10. The version is indicated by a label located on the lower left corner of the board, component side. Satisfactory operation of the Series 90-20 PLC has been demonstrated with the Hand-Held Programmer and Logicmaster 90-30/20/Micro programmer without modifying standard cables.

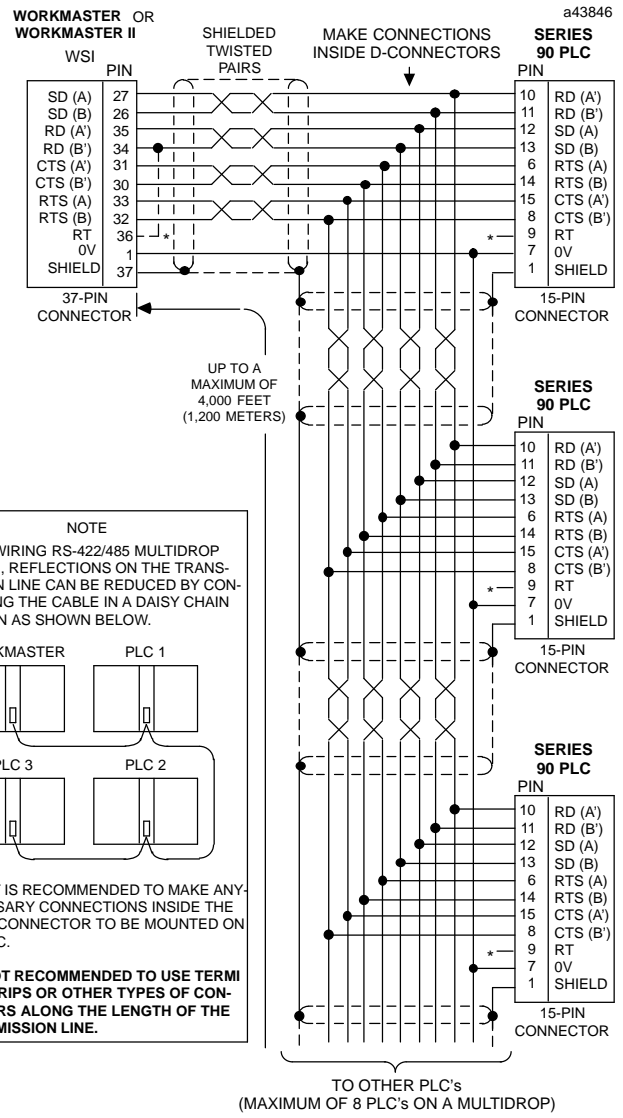
Multidrop Serial Data Configuration to Series 90 PLCs

A typical wiring diagram and requirements for connecting a Workmaster II, Workmaster, or IBM-PC or compatible computer to Series 90 PLCs in an 8-wire multidrop, serial data configuration is shown in the following figure. The 15-pin serial port connector for the Series 90-20 is located on the CPU Module. The 37-pin serial port connector for the Workmaster II and Workmaster computers is located on the Work Station Interface board installed in the programming computer. The cable type for these connections should be 24 AWG (0.22 mm²), 30V computer grade. Extra flexible construction is recommended for short lengths.

The maximum number of PLCs that can be included in a multidrop system for various cable lengths is listed below.

Cable Length	Maximum Number of PLCs
4000 feet	8
2000 feet	16
1000 feet	32

For additional information on serial communications, refer to the *Series 90 PLC Serial Communications User's Manual*, GFK-0582.



IMPORTANT !

DO NOT USE TERMINAL STRIPS OR OTHER TYPES OF CONNECTORS ALONG THE LENGTH OF THE TRANSMISSION LINE WHEN WIRING A MULTIDROP SERIAL DATA CONFIGURATION

* TERMINATION RESISTANCE FOR THE RECEIVE DATA (RD) SIGNAL NEEDS TO BE CONNECTED ONLY ON UNITS AT THE END OF THE LINES. THIS TERMINATION IS MADE ON THE SERIES 90 PLC PRODUCTS BY CONNECTING A JUMPER BETWEEN PIN 9 AND PIN 10 INSIDE THE 15-PIN D-SHELL, WITH THE FOLLOWING EXCEPTION. FOR SERIES 90-70 PLCs, CATALOG NUMBERS IC697CPU731 AND IC697CPU771, THE TERMINATION FOR RD AT THE PLC IS IMPLEMENTED BY A JUMPER BETWEEN PIN 9 AND PIN 11.

CAUTION

GROUND POTENTIAL: MULTIPLE UNITS, NOT CONNECTED TO THE SAME POWER SOURCE, MUST HAVE GROUND POTENTIAL WITHIN " 7V FOR PROPER OPERATION OF THIS SYSTEM. FAILURE TO PROVIDE A COMMON GROUND MAY CAUSE DAMAGE TO PLC COMPONENTS.

Figure 2-11. Example of Series 90 PLC to Programmer 8-Wire Multidrop, Serial Data Configuration

Installing the RS-422/RS-485 to RS-232 Converter

Installation of the RS-422/RS-485 to RS-232 Converter (IC693ACC900) consists of connecting two cables. Select the proper cables for your installation. Prewired cables are available, or if cables of different lengths are required by your application, you can build your own cables. Specifications for building these cables are provided later.

You do not need to connect an external source of power to the converter since the necessary power connections of +5 VDC and signal ground are derived from the baseplate's backplane bus through the cable which connects to the Series 90-20 PLC.

1. Select one of the three RS-232 compatible cables (10 feet in length) that will connect the programmer's (or other serial device) RS-232 serial port to the RS-232 port on the converter. The catalog numbers of these cables are: IC690CBL701 (use with Workmaster computer, or IBM PC-XT or compatible personal computer), IC690CBL702 (use with IBM-PC-AT or compatible personal computer), IC690CBL705 (use with Workmaster II computer, or IBM PS/2 or compatible personal computer).
2. The 6-foot cable (Hand-Held Programmer compatible) that connects from the RS-422/RS-485 port on the converter to the RS-485 port on the Series 90-20 PLC baseplate.

Installation of these cables should be done with the PLC powered-down.

- Connect the 25-pin male connector on the 10 foot cable to the 25-pin female connector on the converter.
- Connect the 9-pin female connector on the opposite end of this cable to the male RS-232 connector (serial port) on the selected programming (or other serial) device.
- Notice that both ends of the 6-foot RS-422/RS-485 compatible cable are the same; a 15-pin male connector is attached at both ends. Connect one end of this cable to the 15-pin female connector on the RS-422/RS-485 connector on the converter.
- Connect the other end of this cable to the 15-pin female connector, which interfaces to the RS-485 compatible serial port on the Series 90-20.

Jumper Configurations

There are three jumper locations on the converter board for selection of user options. Each jumper position has three pins, as shown in the following illustration. These jumper positions, labeled *JP2*, *JP3*, and *JP4*, are accessed by removing the square plastic cover on the top of the converter. Configuration can be changed as required by carefully removing one or more of the jumpers with a pair of needle nose pliers and placing it on the desired pair of pins.

Refer to the description of these selectable jumper positions in the following table and place the jumper on the selected pair of pins. The pin numbers are 1, 2, and 3. Default jumper locations are indicated by a rectangle around the pins to be jumpered for each position. The default configuration is for the jumper to be on pins 1 and 2. *Default positions are used when using a Series 90-20 PLC and Logicmaster 90-30/20/Micro programming software.*

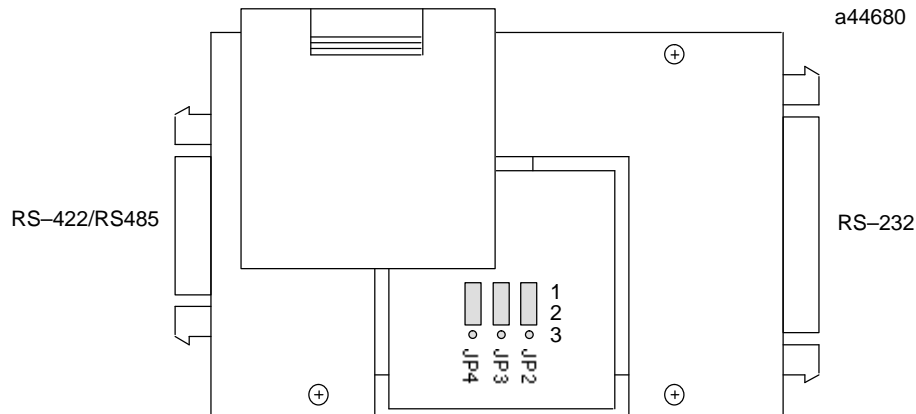


Figure 2-12. Location of Jumpers for User Options

Table 2-1. Jumper Configuration for RS-422/RS-485 to RS-232 Converter

Jumper Position	Label	Jumper Position	Description
JP2	DCD	<div style="display: flex; justify-content: space-around; width: 100px;"> 1 2 3 </div> <div style="display: flex; justify-content: space-around; width: 100px; margin-top: 10px;"> 1 2 3 </div>	<p>Default position 1 and 2 is used when the device communicating with the PLC does not supply the Carrier Detect signal.</p> <p>Use jumper positions 2 and 3 if the device does supply the Carrier Detect signal.</p>
JP3	MODEM	<div style="display: flex; justify-content: space-around; width: 100px;"> 1 2 3 </div> <div style="display: flex; justify-content: space-around; width: 100px; margin-top: 10px;"> 1 2 3 </div>	<p>Default position 1 and 2 is used when an attached Modem does not require the Clear To Send (CTS) signal.</p> <p>Jumper positions 2 and 3 are used when the attached Modem does require the CTS signal (most modems require this signal).</p>
JP4	ATTACH	<div style="display: flex; justify-content: space-around; width: 100px;"> 1 2 3 </div> <div style="display: flex; justify-content: space-around; width: 100px; margin-top: 10px;"> 1 2 3 </div>	<p>Default position 1 and 2 is used for most applications.</p> <p>Jumper positions 2 and 3 are used if the device communicating with the PLC is intended to emulate the Hand-Held Programmer protocol.</p>

RS-422/RS-485 to RS-232 Converter Wiring Information

The following information is provided for those who may wish to build their own cables.

RS-232 Interface Pin Assignments

The RS-232 interface of the converter is accessible through the 25-pin D subminiature female connector. The pin assignment is compatible with the three cables described above (IC697CBL701/702/705). The opposite end of the connector, which connects to your RS-232 compatible device, should be selected to fit the requirements of the device. Most of these devices use a 15-pin D subminiature female connector.

Pin assignments for the RS-232 interface of the converter are listed in the following table.

Table 2-2. RS-232 Interface Pin Assignments

Pin	Signal Name	Description	Signal Flow
1	Shield	No connection	-
2	SD	Transmitted Data	Out
3	RD	Received Data	In
4	RTS	Request To Send	Out
5	CTS	Clear To Send	In
6	NC	No connection	-
7	SG	Signal Ground	-
8	DCD	Data Carrier Detect	In
9/19	NC	No connection	-
20	DTR	Data Terminal Ready	Out
21 to 25	NC	No connection	-

RS-422/RS-485 Interface Pin Assignments

The RS-422/RS-485 interface of the converter is accessible through the 15-pin D subminiature female connector. The pin assignment is compatible with the cable for the Hand-Held Programmer (IC693CBL303), as described above. Both ends of this cable are the same and have a 15-pin male, D subminiature connector attached.

Pin assignments for the RS-422/RS-485 interface are as shown in the following table.

Table 2-3. RS-422/RS-485 Interface Pin Assignments

Pin	Signal Name	Description	Signal Flow
1	CableShield	Shield	-
2	DCD(A)	Differential Data Carrier Detect	Out
3	DCD(B)	Differential Data Carrier Detect	Out
4	ATCH/	Attach (used with HHP)	n/a
5	+5 VDC	Logic Power	In
6	RTS(A)	Differential Request To Send	Out
7	SG	Logic Power	In
8	CTS(B')	Differential Clear To Send	In
9	RT	Resistor Terminator	n/a
10	RD(A')	Differential Receive Data	In
11	RD(B')	Differential Receive Data	In
12	SD(A)	Differential Transmit Data	Out
13	SD(B)	Differential Transmit Data	Out
14	RTS(B)	Differential Request To Send	Out
15	CTS(A')	Differential Clear To Send	In

Wiring Connections for IC693CBL303 Cable

The specifications and wiring information for this cable are provided below. Note that in addition to being used to connect the RS-485 serial port on the PLC to the RS-422/RS-485 to RS-232 converter (IC690ACC900). This cable is also used to connect the PLC to the Hand-Held Programmer.

Cable Specifications

The prewired cable (IC693CBL303) is 6 feet (2 meters) long. This is the cable used with the Hand-Held programmer. If a different length cable is required for connection to the converter, refer to the information below for specifications and wiring information.

This information is essential if you intend to build your own cable. The recommended cable types for this cable are listed below and depend on the length of the cable.

Specifications for IC693CBL303 Prewired cable

Item	Description
Connectors Same connector is on both ends	15-pin male, D-Subminiature Type, Canon DA15S (solder pot)
Hood	AMP 207470-1 connector shell
Hardware kit	AMP 207871-1 Kit includes 2 metric screws and 2 screw clips
Cable Type	Belden 9508: AWG #24 (.22 mm ²)
Cable Length	6 feet (2 meters)

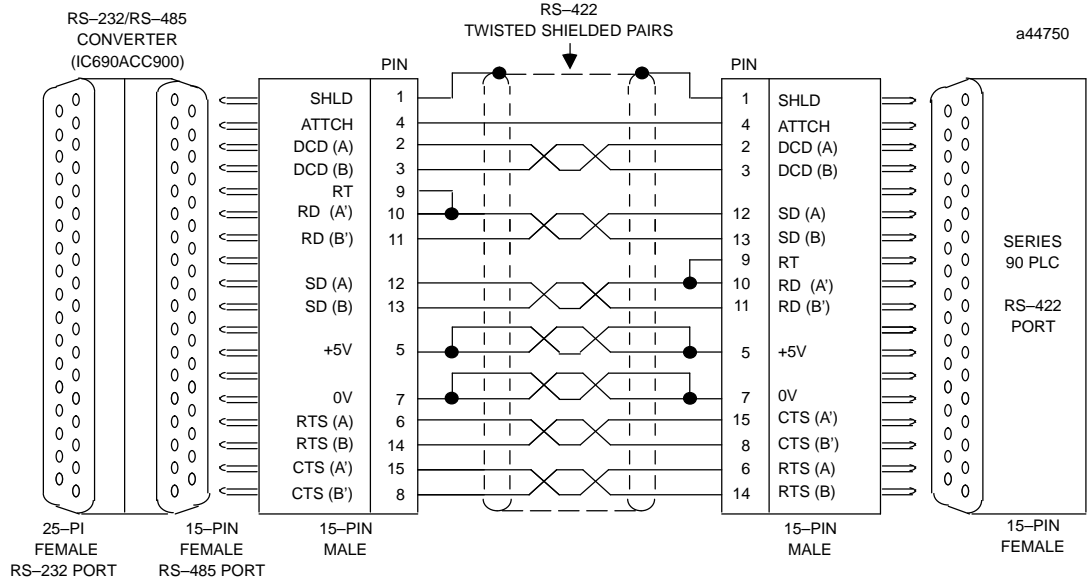
Wire Types for Custom Cables

Cable Length	AWG Wire Size	Catalog Number
6 feet (2m)	24 (.199mm ²)	Belden 9508
30 feet (10m)	22 (.329mm ²)	Belden 9309
>30 feet to 980 feet (300m)	22 (.329mm ²)	Same as for 30 feet. In addition, the +5 VDC logic power source for the converter cannot be supplied by the PLC. It must be provided by an external power supply connected to the +5V and SG pins at the converter end of the connector. The +5V pin at the PLC connector must not be connected to the cable. The +5V and SG connections from the power supply must be isolated from its own power line ground connection. Be sure that there is no connection between the external supply and the PLC except the SG cable connection.

1. Catalog numbers are provided as suggestions only. Any cable having the same electrical characteristics is acceptable. It is strongly recommended that you use stranded wire. Since it is sometimes hard to find a cable with the desired number of twisted pairs (the Belden 9309 has an extra pair), you may end up with a cable with extra pairs.
2. A greater cable length between the PLC and the converter increases the possibility of noise coupling into the data and converter logic power circuits within the cable. The cable should be as short as possible in noisy environments. In extreme cases, additional noise protection measures, such as double-shielded cables, may be required.

Wiring Diagram

The following wiring diagram is for the IC693CBL303 cable and for cables that may be built for connection to the IC690ACC900 converter.



NOTE: PINS 9 AND 10 ARE JUMPED AT BOTH ENDS OF CABLE TO CONNECT TERMINATING RESISTORS FOR THE RD SIGNAL WHICH IS INSIDE THE PLC POWER SUPPLY.

Figure 2-13. Wiring Connections for IC693CBL303

Maintenance and Optional Procedures

The following information describes operations and procedures that are required to maintain a Series 90-20 PLC system. If at any time you need technical help, PLC Technical Support can be reached at the PLC Hotline, or via Internet or fax as listed on page 2-2.

Replacing A Battery

If the LOW BATTERY LED on the CPU Module turns ON, replace the battery. Battery replacement must be done with power on if the user program stored in RAM memory is to be retained. To replace a battery (catalog number IC693ACC301), use the following procedure:

- Remove the battery cover at the bottom of the CPU module.
- Remove the battery from its mounting clip.
- Firmly insert the replacement battery into the battery mounting clip.
- With the needle nose pliers, push the new battery connector firmly into the open receptacle on the board.
- Remove the old battery cable connector from its receptacle and discard the old battery.
- Replace the battery cover.

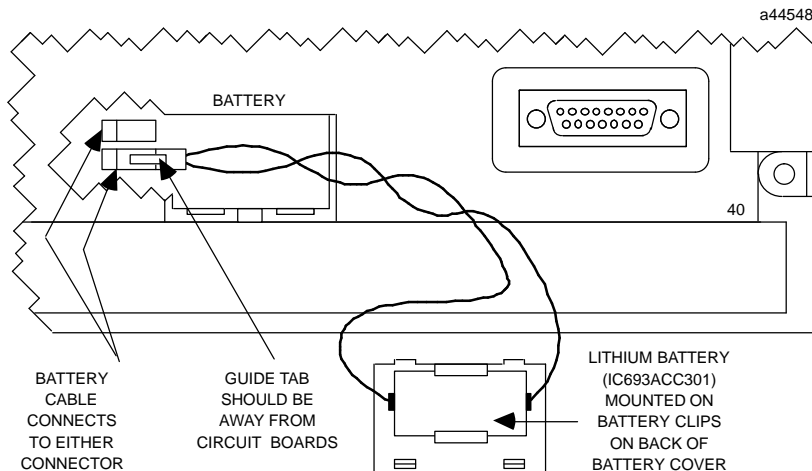


Figure 2-14. Battery Replacement

Warning

Observe the following precautions when handling a Lithium battery. Do not discard the battery in fire. Do not attempt to recharge the battery. The battery may burst or burn or release hazardous materials.

Caution

Do not attempt to remove the CPU module circuit board from its module housing. Failure to observe this caution may result in an electrostatic build-up and discharge resulting in damage to MOS circuits. Discharge of the non-rechargeable lithium battery may occur if the board contacts a conducting surface, causing loss of memory contents.

User PROM Option

Application programs are normally developed in the CPU's RAM memory and executed from RAM memory. If additional program integrity is desired, or operation of the PLC without a battery is desired, an optional EEPROM or EPROM can be installed in a spare socket (labeled PROGRAM PROM). EEPROMs can be written to and read from using the HHP, and can be read from EEPROM to user RAM memory and then to the Memory Card. EPROMs can be read when installed in the PLC, however they must be programmed with an external PROM burning device.

A typical scheme for using these devices is to develop programs using an EEPROM. When the program in RAM has been developed and debugged, it is saved to EEPROM. See the Hand-Held Programmer User's Manual, GFK-0402, on how to Save EEPROM. The EEPROM can then be removed from the PLC and used as a master to make backup or multiple copies of the program to EPROM memory. The EPROM can then be installed in the socket provided in the CPU and used as a non-volatile memory for operation without a battery, or to run the same program in multiple PLCs.

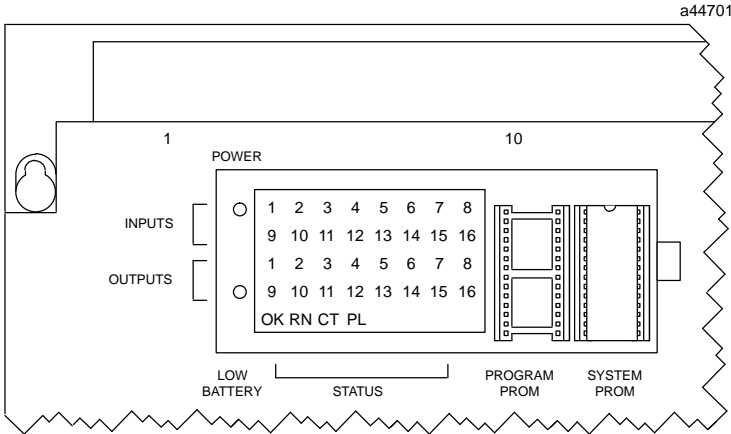


Figure 2-15. Location of Socket for User PROM Option

When the EEPROM or EPROM is installed, the application program stored in the device is automatically loaded into RAM memory whenever the CPU is powered-up. However, this only happens, if EEPROM is selected as the PROGRAM SOURCE parameter during configuration with the Hand-Held Programmer or Logicmaster 90-30/20/Micro configuration software.

EEPROM and EPROM memory chips can be ordered from GE Fanuc. Catalog numbers for these devices are:

Table 2-4. EEPROM and EPROM Memory Catalog Numbers

Catalog Number	Description	PROM Part Number	Third Party Source	
			Vendor	Part Number
IC693ACC305 (Qty 4)	28C256 EEPROM, 350ns	44A725999-000	XICOR	X28C256Por
			XICOR	X28C256P25
IC693ACC306 (Qty 4)	32Kx8 UVEPROM, 150ns	44A723379-000	NEC	UPD27C256AD-15
			Atmel	AT27C256-15DC1
			Toshiba	TC57256AD-15
			Hitachi	HN27C256AG-15
			AMD	AM27C256-150DC
			Intel	TD27C256A-1

EEPROM Insertion and Removal

The EEPROM location is accessed by removing the LED lens on the CPU module. The EEPROM socket is located above the words PROGRAM PROM. It is important that the person performing the insertion or removal be grounded before handling the EEPROM device to avoid static discharge which may damage the device.

Warning

Remove power when inserting an EEPROM into or removing an EEPROM from the CPU module.

Inserting an EEPROM

Place the EEPROM in the socket with the polarity of the EEPROM oriented towards the top of the unit. Apply even pressure and push the EEPROM into the EEPROM socket. Follow directions for programming the device as described in the Series 90-30 and 90-20 HHP User's Manual, GFK-0402.

Removing an EEPROM

Using a small slotted screw driver, insert the screw driver through the small rectangular opening in top of the plastic cover just above the PROGRAM PROM location. Place the end of the screw driver just under the edge of PROM to start lifting the EEPROM out of the socket.

Caution

Be sure that the screw driver does not go under the socket to avoid damage to the EEPROM socket.

After the EEPROM starts to lift up, slide the screw driver carefully under the EEPROM to fully disengage it from the socket.

Replacing Fuses

The Series 90-20 PLC provides replaceable fuses for output points and for a user accessible 24 volt power supply (on the DC input versions of baseplate only). To replace these fuses:

- Remove power from the unit.
- Remove the CPU module by lifting the latch on the right side of the unit and sliding the CPU towards the left. Lift the CPU off of the I/O base unit.
- Confirm that power is removed from the I/O Power Supply baseplate.

Refer to Chapter 5 for I/O power supply fuse location and replacement information.

Chapter 3

System Operation

This chapter describes system operations of the Series 90-20 PLC which you should be familiar with to understand operation of the PLC system. It includes a discussion of the PLC system sweep sequences, the system power-up and power-down sequences, system clocks and timers, system security through password assignment, and I/O system.

PLC Sweep Summary

The logic program in the Series 90-20 PLC executes in a repetitive fashion until stopped by a command from the programmer, or by a command from another device. The sequence of operations necessary to execute a program one time is called a *sweep*. In addition to executing the logic program, the sweep includes obtaining data from input devices, sending data to output devices, performing internal housekeeping, and servicing the programmer.

The following configuration items affect the Series 90-20 PLC sweep:

Stop Mode: Stop with I/O Disabled
Stop with I/O Enabled

Constant Sweep Mode: Enable/Disable

In addition, the DO I/O function will cause I/O scanning in the middle of the logic solution.

Standard Program Sweep

The CPU operates by executing an application program, updating I/O, and performing communications and other tasks. This occurs in a repetitive cycle called the CPU sweep. There are seven parts to the execution sequence of the Standard Program Sweep.

- Start-of-Sweep Housekeeping
- Input Scan (Read Inputs)
- Application Program Logic Solution
- Output Scan (Update Outputs)
- Programmer Service
- Non-Programmer Service
- Diagnostics

All of these steps, except Programmer Service execute every sweep. *Programmer Service* only occurs if a board fault has been detected or if the programming device issues a service request.

The sequence of the Standard Program Sweep is shown in the following figure.

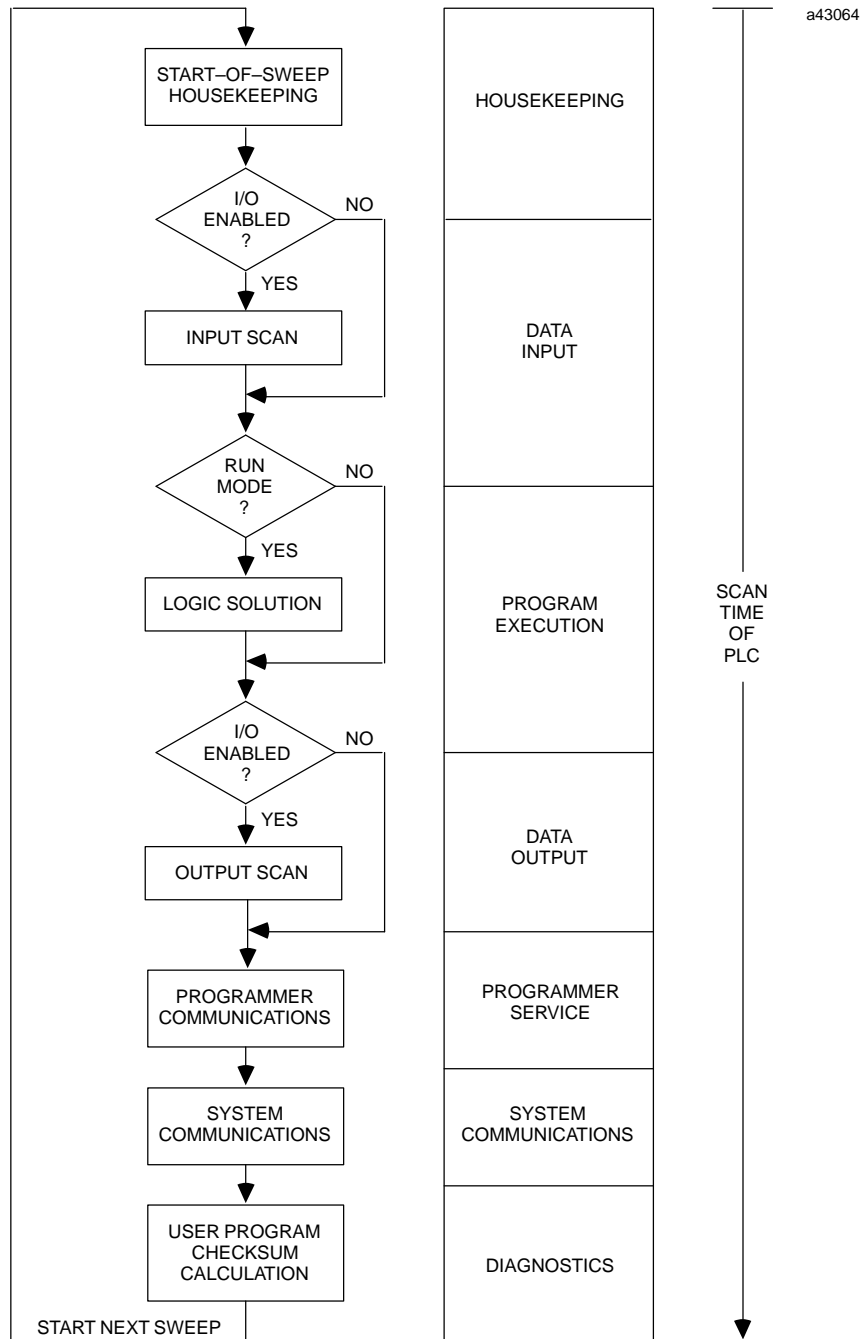


Figure 3-1. PLC Sweep

As shown in the PLC sweep sequence, several items are included in the sweep. These items contribute to the total sweep time as shown in the following table.

Table 3-1. Sweep Time Contribution

SweepComponent		Description	TimeContribution
A	Housekeeping	<ul style="list-style-type: none"> • Schedule start of next sweep • Determine mode of next sweep • Update %SA, %SB, %SC and %S tables • Reset watchdog timer 	0.74 msec
B	Data Input	Input data is received from inputs.	0.345msec/16pointinputs
C	Program Execution	User logic is solved	18µS per coil or contact. Execution time is dependent on the length of the program and the type of instructions used in the program. Instruction execution times are listed in Appendix B.
D	Data output	Output data is sent to outputs.	0.270msec/12pointoutputs
E	Programmer Service	Service requests from programming device.	0.4 msec overhead, 6 msec maximum <ul style="list-style-type: none"> • 1 msec average for Logicmaster 90 • 3 msec average for Hand-Held Programmer
F	Diagnostics	Verify user program integrity	0.4 msec

Sweep Time Calculation

As shown in the previous table, six items contribute to the sweep time of the PLC. The sweep time consists of fixed times (housekeeping and diagnostics) and variable times. The variable times vary according to the I/O configuration, size of the user program, and the type of programming device connected to the PLC.

Housekeeping

The housekeeping portion of the sweep performs all of the tasks necessary to prepare for the start of the sweep. If the PLC is in the constant sweep mode the sweep will be delayed until the required sweep time elapses. If the required time had already elapsed, the **ov_swp**%SA0002 contact is set and the sweep continues without delay.

Next the timer values (hundredths, tenths, and seconds) are updated by calculating the difference from the start of the previous sweep and the new sweep time. In order not to lose accuracy, the actual start of sweep is recorded in 100 msec increments. Each timer has a remainder field which contains the number of 100 msec ticks that have occurred since the last time the timer value was incremented.

Input Scan

Scanning of inputs occurs during the Input Scan portion of the sweep, just prior to the Logic Solution. During this part of the sweep all Series 90-20 Inputs will be scanned and their data stored in %I (discrete inputs) or %AI (analog inputs) memory, as appropriate. The %AI1 through %AI15 analog inputs are used by the High Speed Counter (HSC). If the CPU is in STOP mode and the CPU is configured to not scan I/O in STOP mode, the input scan will be skipped.

Application Program Logic Scan or Solution

The *Application Program Logic Scan* is when the application logic program actually executes. The Logic Solution always begins with the first instruction in the application (user's) program immediately following the completion of the Input Scan. Solving the logic provides a new set of outputs. The Logic Solution ends when the END instruction is executed. The Application program is executed by the 80C188 microprocessor in the CPU.

Many program control capabilities are provided by the Control Functions, which are described in the *Series 90-30/20/Micro Programmable Controllers Reference Manual*, GFK-0467, and in the *Hand-Held Programmer User's Manual for Series 90-30/20/Micro Programmable Controllers*, GFK-0402. A list of execution times for each programming function can be found in Appendix B in this manual.

Output Scan

Outputs are scanned during the Output Scan portion of the sweep, immediately following the Logic Solution. During the Output Scan, all Series 90-20 PLC Outputs are scanned in the same order as for the Input Scan: from lowest to highest reference address. Outputs are updated using data from the %Q (for discrete outputs) memories. However, if the CPU is in the STOP mode and the CPU is configured to not scan I/O during STOP mode, the output scan will be skipped. The Output Scan is completed when all output data has been sent to all Series 90-20 Outputs.

Logic Program Checksum Calculation

At the end of every sweep a checksum calculation is performed on the application logic program. Since it would take too long to calculate the checksum over the entire program at one time, eight words of the program will be checksummed each sweep.

If the calculated checksum does not match the reference checksum, the Program Checksum Failure exception flag will be raised. This causes a fault entry to be inserted into the PLC Fault Table and the PLC mode to be changed to STOP.

Programmer Communications Window

This part of the sweep is dedicated to communicating with the programmer. If there is a programmer attached, the CPU executes the Programmer Communications Window. The Programmer Communications Window will not execute if there is no programmer attached.

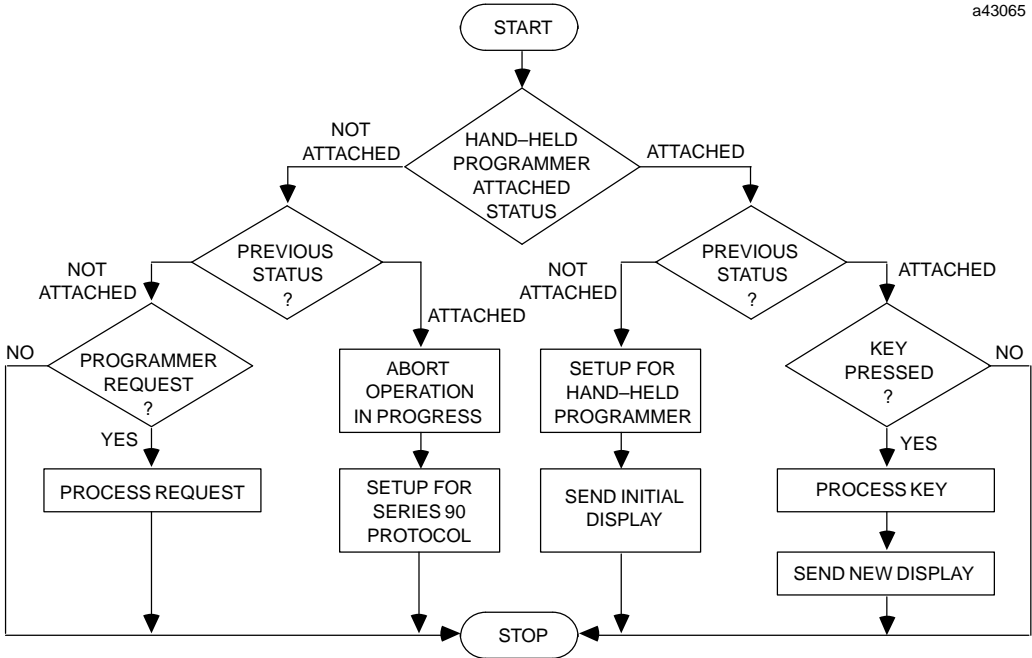


Figure 3-2. Programmer Communications Window Flow Chart

Support is provided for the Hand-Held Programmer and for other programmers that can connect to the serial port and use the SNP (Series Ninety Protocol) protocol.

The CPU performs one operation for the programmer each sweep, that is, it honors one service request or response to one key press. If the programmer makes a request that requires more than 6 milliseconds to process, the request processing will be spread out over several sweeps so that no sweep is impacted by more than 6 milliseconds. The previous figure is a flow chart for the Programmer Communications portion of the sweep.

System Communications Window

This is the part of the sweep where communications requests from intelligent option modules. The System Communications Window is not used in the Series 90-20 PLC.

Standard Program Sweep Variations

In addition to the normal execution of the Standard Program Sweep, certain variations can be encountered or forced. These variations are described in the following paragraphs.

Constant Sweep Time Mode

In the Standard Program Sweep, each sweep executes as quickly as possible with a varying amount of time consumed each sweep. An alternate to this is the *Constant Sweep Time* mode. In the Constant Sweep Time mode, each sweep consumes the same

amount of time, which can be selected at configuration to be from 5 to 200 milliseconds. At the start of each sweep, the PLC starts the constant sweep particular application. For more information on the constant sweep timer, refer to the description of the constant sweep timer (see *Clocks and Timers*).

PLC Sweep When in STOP Mode

When the PLC is in STOP mode, the Application Program is not executed, you can choose whether or not the I/O is scanned, and communications with the programmer will continue. For efficiency, the operating system uses larger time-slice values than those that are typically used in RUN mode (usually about 50 ms per window).

PLC Sweep When Using Do I/O

The Do I/O function, whenever it is executed, enables the logic program to execute an input or output scan *during* program execution in addition to the normal I/O update portion of the sweep.

Software Structure

The Series 90-20 PLC software structure supports both program execution and basic housekeeping tasks such as diagnostic routines, input/output scanners, and alarm processing. The operating system also contains routines to communicate with the programmer; these routines provide for the upload and download of application programs, return of status information, and control of the PLC. The application (user logic) program which controls the end process to which the PLC is applied, is called a control program.

Program Structure

Each control program is comprised of a single program block. This includes the user program and some system overhead. The program block must be less than or equal to 1024 (1K) words.

Data Structure

The Series 90-20 PLC has nine data memories, each designed for a specific purpose. The following table lists these memories.

Table 3-2. Memory Data Types

Memory Type	User Reference	Data Type
Discrete Input	%I	bit
Discrete Output	%Q	bit
Discrete User Internals	%M	bit
Discrete Temporaries	%T	bit
Discrete System	%S†	bit
Discrete Global	%G	bit
Register	%R	word
Analog Input	%AI	word
Analog Output	%AQ	word

† System discrete bits include System bits, fault bits, and reserved bits.

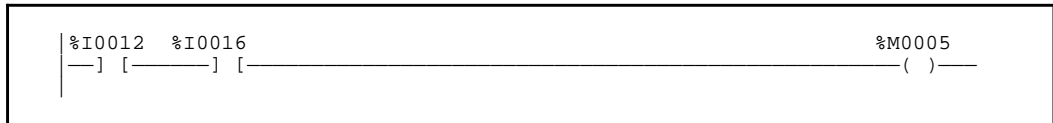
‡ The % symbol is used to distinguish machine references from

nicknamesandisonlyusedwithLogicmaster90-30/20/Micro programmingsoftware.

Discrete Memory Reference Definitions

Discrete Inputs (%I) reference input points which are the state of the inputs as detected during the last input scan. Discrete Outputs (%Q) reference output points which are the state of the outputs as last set by the application program. User Internals (%M) are internal coils used for boolean logic when the result of a rung is only required to be used later in the program as conditional logic. Temporaries (%T) are used with coils similar to %M references, except that %T references are not saved across a power failure; that is, they are non-retentive. %M and %Q memories are retentive unless used with a normal coil, e.g., --()--.

User internals (%M) are useful when the coil in a rung is only required to be used later in the logic solution as conditional logic and not outside of the PLC. The following example shows %I0012 and %I0016 being used to set user internal %M0005, similar to the use of a control relay in electro-mechanical logic.



System Discretes (S, SA, SB, SC) include: System Bits, used internally by the CPU; Fault Bits, for holding system fault data; and Reserved Bits, for future system expansion.

Many of the Fault Bits are referenced by the application program to determine what faults exist in the Series 90-20 PLC system. Examples of these fault contacts are over sweep condition (ov_swp), and low battery (low_bat). The first scan contact (fst_scn) also resides here. Refer to Chapter 4 for more information on fault bits.

Override Bits

The Series 90-20 PLC has no override capability.

Transition Bits

Transition bits are discrete memory locations used internally by the PLC when solving logic involving transitional coils. This data is not accessible to the user. The PLC sets and resets this transition data based upon changes in the associated status table.

Power-Up and Power-Down Sequence

Power-Up Sequence

The power-up sequence for the Series 90-20 PLC consists of the following sequence of events.

1. The CPU, will run diagnostics on itself. This includes checking a portion of battery-backed RAM to determine whether or not the RAM contains valid data.
2. If an EPROM or EEPROM is present and the PROM power-up option in the PROM specifies that the PROM contents should be used, the contents of PROM are copied into RAM memory.

3. The CPU interrogates the I/O module in the system to determine which type is present.
4. The hardware configuration is compared with software configuration to ensure that they are the same. Any mismatches detected will be considered as faults and will be alarmed. Also, if the I/O is specified in the software configuration but is not present in the actual hardware configuration, this condition is a fault and will be alarmed.
5. If there is no software configuration, the CPU will use the default configuration.
6. The CPU establishes the communications channel between itself and the intelligent modules.
7. In the final step of the execution, the mode of the first sweep is determined based on CPU configuration. If RUN mode, the sweep proceeds as described under *STOP-to-RUN Mode transition*. The following figure shows the decision sequence followed by the CPU when it decides whether to copy from PROM or to power-up in STOP or RUN mode. Abbreviations used are:

clear	Press CLR and M/T simultaneously (using HHP)
rom_sum	ROM checksum is good
prom_rom	Run from ROM (parameter in ROM)
ram_sum	RAM checksum is good
prom_ram	Run from ROM (parameter in RAM)
ld_not	Press LD and NOT simultaneously (using HHP)
ostop	Press NOT and RUN simultaneously (using HHP)
pstop	Power-up in STOP mode
prun	Power-up in RUN mode
bal	Battery voltage is low
pdsm	Power-down in STOP mode

8. If a PROM is not present

rom_sum	= false
prom_ram	= false

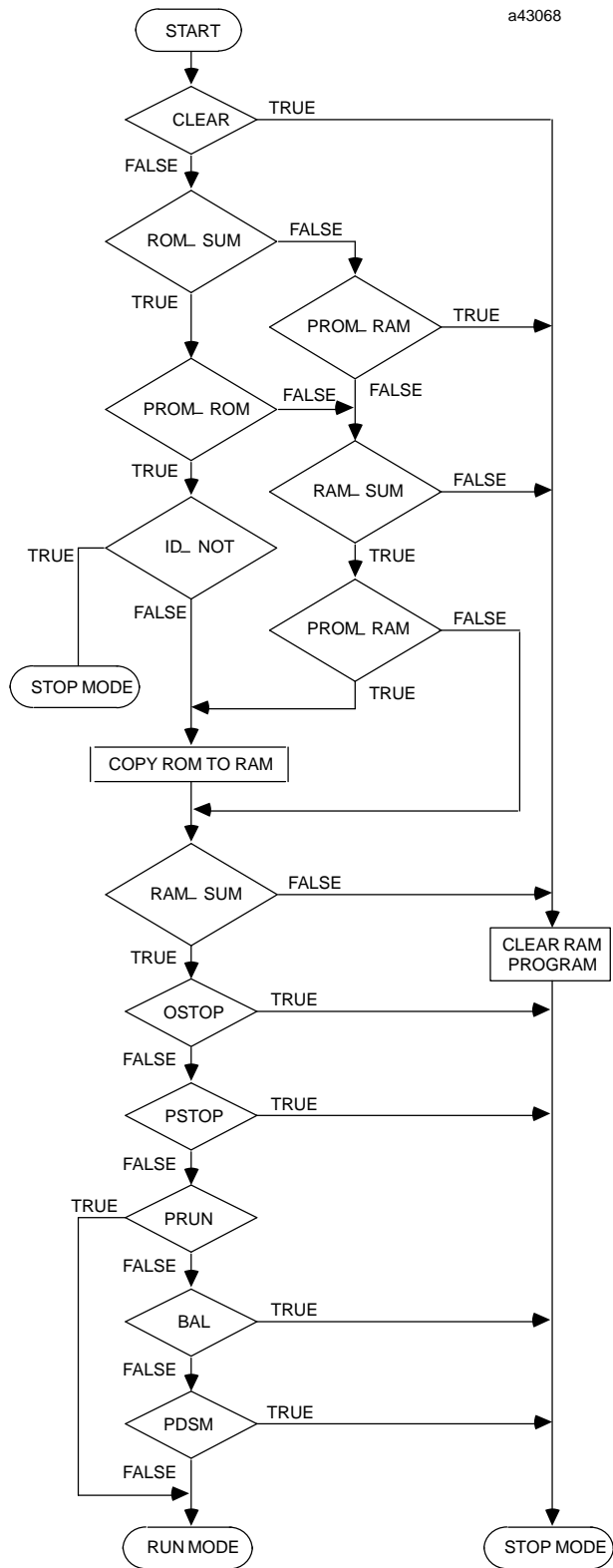


Figure 3-3. Power-up Decision Sequence

Power-Down Conditions

System power-down occurs under the following conditions:

1. The power supply detects that incoming AC power has dropped out for more than one-half power cycle.
2. The 24 volt output supply accessed on terminals 1 and 2 on I/O bases with DC inputs is less than 21 volts. This could occur if the output is shorted, which will blow a replaceable fuse. See Chapter 5 for location of fuses and information on how to replace fuses.

Clocks and Timers

Clocks and timers provided by the Series 90-20 PLC are: an elapsed time clock, a watchdog timer, and a constant sweep timer. It also provides two types of timer function blocks: on-delay timer, and a start-reset timer. Four timed contacts, which cycle on and off for .01 second, .1 second, 1 second, and 1 minute intervals, are also available.

Elapsed Time Clock

The elapsed time clock uses 100 microsecond *ticks* to track the time elapsed since the CPU powered-on. The clock is not retentive across a power failure; it restarts on each power-up. Once per second the hardware interrupts the CPU to enable a seconds count to be recorded. This seconds count rolls over approximately 100 years after the clock begins timing.

Because the elapsed time clock provides the base for system software operations and timer function blocks, *it cannot be reset* from the user program or the programmer. However, the application program can read the current value of the elapsed time clock by using Function Number 16 of the SVC_REQ (SerViCe_REQest) Function.

Watchdog Timer

A watchdog timer in the Series 90-20 PLC is designed to catch catastrophic failure conditions. The timer value for the watchdog timer is 200 milliseconds; this is a fixed value which cannot be changed. The watchdog timer always starts from zero at the beginning of each sweep.

If the watchdog timeout value is exceeded, the OK LED goes off, the CPU goes through its power-up sequence and the PLC is left in STOP mode with a watchdog timer fault recorded.

Constant Sweep Timer

The Constant Sweep Timer controls the length of a program sweep when the Series 90-20 PLC operates in *Constant Sweep Time* mode. In this mode of operation, each sweep consumes the same amount of time. Typically, for most application programs, the *Input Scan*, *Application Program Logic Scan*, and *Output Scan* do not require exactly the same amount of execution time in each sweep. For additional information, refer to **Constant Sweep Time Mode**. The value of the Constant Sweep Timer is set by the programmer and can be any value from 5 to 200 milliseconds. The default value is 100 ms.

If the Constant Sweep Timer expires before the completion of the sweep and the previous sweep was not over-sweep, the PLC places an over-sweep alarm in the PLC Fault table. At the beginning of the next sweep, the PLC sets the `ov_swp` fault contact. The `ov_swp` contact is reset when the PLC is *not* in Constant Time Mode or the time of the last sweep did not exceed the Constant Sweep Timer.

System Security

Security in the Series 90-20 PLC is designed to prevent unauthorized changes to the contents of a PLC. There are three security or **privilege levels** available in the PLC, that have access to each level protected by a password. A fourth level, which is the default level, provides only the ability to read PLC data; no changes are permitted to the application program.

Each higher privilege level permits greater change capabilities than the lower level(s). Privilege levels accumulate in that the privileges granted at one level are a combination of that level, plus all lower levels. The levels and their privileges are:

Level 1

Any data, except passwords may be read. This includes all data memories (%I, %Q, %AQ, %R, etc.), fault tables, and all program block types: data, value, and constant. No values may be changed in the PLC.

Level 2

This level allows write access to the data memories (%I, %R, etc.).

Level 3

This level allows write access to the application program in STOP mode only.

Level 4

This is the default level for systems with no passwords. The default level for a system with passwords is to the highest unprotected level. This level, the highest, allows read and write access to all memories as well as passwords in both RUN and STOP mode (configuration data cannot be changed in RUN mode).

For more detailed information on system security, see Chapter 7 of the *Series 90-30/20/Micro Hand-Held Programmer User's Manual*, GFK-0402.

Passwords

There is one password for each privilege level in the PLC and each password can be unique; however, the same password can be used for more than one level. Passwords are one to four ASCII characters in length. Passwords can only be entered or changed with the Logicmaster 90-30/20/Micro programmer or the Hand-Held Programmer. The Hand-Held Programmer only allows the ASCII characters 0 to 9 and A to F.

When communicating over a serial link, a privilege level change is in effect only as long as communications between the PLC and the programmer are intact. There does not need to be any activity, but the communications link must not be broken. If there is no communication for 15 minutes, the privilege level returns to the highest unprotected level.

When the Logicmaster 90-30/20/Micro programmer is connected through the serial connection, either the PLC or the Work Station Interface board in the programmer may detect a disconnect. The PLC detects a disconnect when it addresses the Work Station Interface and receives no response. It then drops the programmer privilege level to the default privilege level, Level 4. The PLC detects a disconnect of the Hand-Held Programmer using a dedicated hardware signal. When the Hand-Held Programmer is disconnected, the PLC reverts to privilege Level 4. Upon connection of the PLC, the Work Station Interface requests the protection status of each privilege level from the PLC. The Work Station Interface then requests the PLC to move to the highest unprotected level, thereby giving the programmer access to the highest unprotected level without it having to request any particular level. When the Hand-Held Programmer is connected to the PLC, the PLC reverts to the highest unprotected level.

Privilege Level Change Requests

A programmer requests a privilege level change by supplying the new privilege level and the password for that level. A privilege level change will be denied if the password sent by the programmer does not agree with the password stored in the PLC's password access table for the requested level. The current privilege level will be maintained and no change will occur. If you attempt to access or modify information in the PLC using the Hand-Held Programmer without the proper privilege level, the Hand-Held Programmer will respond with an error message that the access is denied.

I/O System for the Series 90-20 PLC

The I/O system for the Series 90-20 PLC provides the interface between the Series 90-20 CPU and user supplied input devices and equipment to be controlled.

The Series 90-20 I/O module is combined with the power supply and is the baseplate for the Series 90-20 PLC. I/O modules support 16 inputs, 12 outputs and a High Speed Counter (HSC) function.

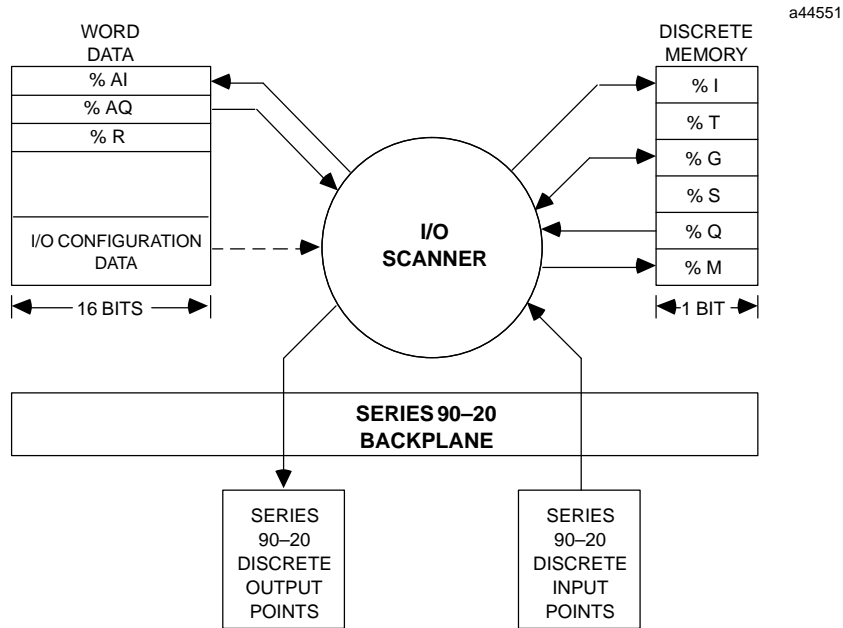


Figure 3-4. Series 90-20 I/O Structure

I/O Scan Sequence

A summary of the I/O Scan sequence is provided below. For more information on scanning and the CPU sweep, refer to the discussion on that subject earlier in this chapter.

Input Scan

Inputs to the Series 90-20 PLC are scanned from the lowest to the highest reference address.

Output Scan

Outputs are scanned during the Output Scan portion of the sweep, immediately following the Logic Solution. Output boards are scanned in the same order as for the Input Scan; from lowest to highest reference address.

Default Conditions for Series 90-20 Output Points

At power-up, output points default to outputs off. They will retain this default condition until the first output scan from the PLC.

Diagnostic Data

Diagnostic bits are available in the %S memory that will indicate a mismatch in I/O configuration. Diagnostic information is not available for individual I/O points. More information on fault handling can be found later in this chapter and in Chapter 4, Fault Descriptions and Corrections.

Chapter 4

Fault Descriptions and Corrections

This chapter is an aid to troubleshooting a Series 90-20 PLC system when using Logicmaster 90-30/20/Micro programming Software or the Hand-Held Programmer. It explains the Fault Descriptions appearing in the PLC Fault Table and the Fault Categories in the I/O Fault Table. The chapter is arranged first by *Fault Table* (PLC Fault Table then I/O Fault Table), then by *Fault Group* within each table and finally by *Fault Category* within each Fault Group. In addition, this chapter describes faults associated with %S references which can be observed using the Hand-Held Programmer.

Faults and Fault Handling

More information on Faults and Fault Handling may be found in the *Logicmaster 90-30/20/Micro Programming Software User's Manual*, GFK-0466, and *Series 90-30/20/Micro Programmable Controllers Reference Manual*, GFK-0467. For information on error detection and correction for Statement List programs and the Hand-Held Programmer, refer to GFK-0402, the *Hand-Held Programmer for Series 90-30/20/Micro Programmable Controllers User's Manual*.

Using This Chapter

Each fault entry in this chapter lists the Fault Description for the PLC FAULT TABLE or the Fault Category for the I/O FAULT TABLE. Find the Fault Description or Fault Category corresponding to the entry on the applicable fault table on your programmer's screen. Beneath it is a description of the cause of the fault. The error code number (in hexadecimal) which appears with most Fault Descriptions is relevant when more detailed information about a fault is needed. *There may be more than one entry for a fault group.*

Fault Handling

Faults occur in the Series 90-20 PLC system when certain failures or conditions which affect the operation and performance of the system happen. These conditions may affect the ability of the PLC to control a machine or process. Other conditions may only act as an alert, such as a low battery signal, which indicates that the voltage of the battery protecting the memory is low and needs to be changed.

Alarm Processor

The condition or failure itself is called a *fault*. When a fault is received and processed by the CPU, it is called an *alarm*. The software in the CPU which handles these conditions is called the *Alarm Processor*. The interface to the user for the Alarm Processor is through Logicmaster 90-30/20/Micro programming software. Any detected fault is recorded in a fault table and displayed on either the PLC Fault Table screen or the I/O Fault Table screens, as applicable.

Classes of Faults

The Series 90-20 PLC detects several classes of faults. These include internal failures, external failures, and operational failures. Following are several examples of these failures.

- Internal Failures
 - Non-responding modules
 - Low battery condition
 - Memory checksum errors
- Operational Failures
 - Communication failures
 - Configuration failures
 - Password access failures

System Reaction to Faults

Typically, hardware failures either require that the system be shut down, or that the failure can be tolerated. I/O failures may be tolerated by the PLC system, but may be intolerable by the application or the process being controlled. Operational failures can normally be tolerated. Series 90-20 PLC faults have two attributes:

Fault Table Affected: I/O Fault Table
 PLC Fault Table

Fault Action: Fatal
 Diagnostic
 Informational

Fault Action

Two fault tables are provided to make faults easier to find and to keep a single table from becoming too long. These tables are the *PLC Fault Table* and the *I/O Fault Table*. **Fatal** faults cause the fault to be recorded in the appropriate table, any diagnostic variables to be set, and the system to be halted. **Diagnostic** faults are recorded in the appropriate table and any diagnostic variables are set. **Informational** faults are only recorded in the appropriate table. Possible fault actions are listed in the following table.

Table 4-1. Fault Actions

Fault Action	Response by CPU
Fatal	Log fault in Fault Table Set fault references Go to STOP mode
Diagnostic	Log fault in Fault Table Set fault references
Informational	Log fault in Fault Table

When a fault is detected, the CPU uses a default Fault Action for that fault.

Fault Tables

Two fault tables are maintained in the PLC for logging faults: the *I/O Fault Table* for logging faults related to the I/O system, and the *PLC Fault Table* for logging all other faults. The following table lists all of the fault groups, their fault actions, the fault tables affected, and the mnemonic for system discrete %S points that are affected.

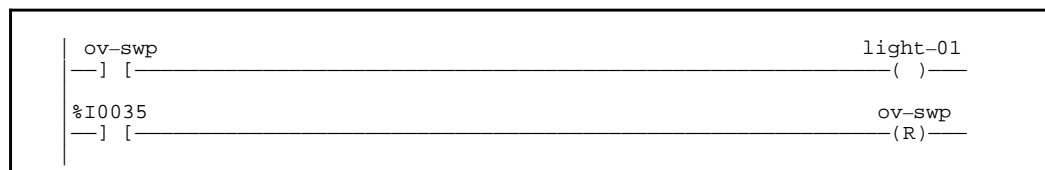
Table 4-2. Fault Summary

Fault Group	Fault Action	Fault Table	Special Discretes			
SystemConfigurationMismatch	Fatal	PLC Fault Table	sy_flt	any_flt	sy_pres	cfg_mm
PLC CPU Hardware Failure	Fatal	PLC Fault Table	sy_flt	any_flt	sy_pres	hrd_cpu
Program Checksum Failure	Fatal	PLC Fault Table	sy_flt	any_flt	sy_pres	pb_sum
Low Battery	Diagnostic	PLC Fault Table	sy_flt	any_flt	sy_pres	low_bat
PLC Fault Table Full	Diagnostic	-	sy_full			
I/O Fault Table Full	Diagnostic	-	io_full			
ApplicationFault	Diagnostic	PLC Fault Table	sy_flt	any_flt	sy_pres	apl_flt
No User Program on Power-up	Fatal	PLC Fault table	sy_flt	any_flt	no_prog	
Corrupted User RAM	Fatal	PLC Fault Table	sy_flt	any_flt	sy_pres	bad_ram
Password Access Failure	Diagnostic	PLC Fault Table	sy_flt	any_flt	sy_pres	bad_pwd
Null System Configuration	Diagnostic	PLC Fault Table	sy_flt	any_flt	sy_pres	nul_cfg
PLC Software Failure	Fatal	PLC Fault Table	sy_flt	any_flt	sy_pres	sft_cpu
PLC Store Failure	Fatal	PLC Fault Table	sy_flt	any_flt	sy_pres	stor_er
Constant Sweep Time Exceeded	Diagnostic	PLC Fault Table	sy_flt	any_flt	sy_pres	ov_swp
Unknown PLC Fault	Fatal	PLC Fault Table	sy_flt	any_flt	sy_pres	
Unknown I/O Fault	Fatal	I/O fault Table	io_flt	any_flt	io_pres	

Fault References

Fault references in the Series 90-20 PLC are of one type: fault summary references. Fault summary references are set to indicate *what* fault occurred. The fault reference remains on until the PLC is cleared or until cleared by the application program.

An example of a fault bit being set and then clearing the bit is shown in the following example. In this example, the coil *light_01* is turned on when an over sweep condition occurs; the light and the *ov_swp* contact remain on until the %I0035 contact is closed.



Fault Reference Definitions

The *Alarm Processor* maintains the states of the 128 system discrete bits in %S memory. These fault references can be used to indicate where a fault has occurred, and what type of fault it is. Fault references are assigned to %S, %SA, %SB, and %SC memory and they each have a nickname. These references are available for use in the application program as required. The mnemonic for these discrete bits and a description of each bit is provided below. Some discrete bits are reserved for future use.

Table 4-3. Fault References

Reference	Nickname	Definition
%S0001	fst_scn	Current sweep is the first sweep.
%S0002	lst_scn	Current sweep is the last sweep.
%S0003	T_10MS	0.01 second timer contact
%S0004	T_100MS	0.1 second timer contact
%S0005	T_SEC	1.0 second timer contact
%S0006	T_MIN	1.0 minute timer contact
%S0007	alw_on	Always On
%S0008	alw_off	Always Off
%S0009	sy_full	Set when the PLC Fault Table fills up. Cleared when an entry is removed from the PLC Fault Table and when the PLC Fault Table is cleared.
%S0010	io_full	Set when the I/O Fault Table fills up. Cleared when an entry is removed from the I/O Fault Table and when the I/O Fault Table is cleared.
%S0013	prg_chk	Set when background program check is active.
%SA0001	pb_sum	Set when a checksum calculated on the application program does not match the reference checksum. If the fault was due to a temporary failure, the discrete bit can be cleared by again storing the program to the CPU. If the fault was due to a hard RAM failure, then the CPU must be replaced.
%SA0002	ov_swp	Set when the PLC detects that the previous sweep took longer than the time specified by the user. Cleared when the PLC detects that the previous sweep did not take longer than the specified time. It is also cleared during the transition from STOP to RUN mode.
%SA0003	aplflt	Set when an application fault occurs. Cleared when the PLC transitions from STOP mode to RUN mode.
%SA0009	cfg_mm	Set when a configuration mismatch is detected during system power-up. Cleared by powering-up the PLC when no mismatches are present.
%SA0010	hrd_cpu	Set when the diagnostics detect a problem with the CPU hardware. Cleared by replacing the CPU module.
%SA0011	low_bat	Set when the Low Battery fault occurs. Cleared by replacing the battery and ensuring that the PLC powers-up without the low battery condition.
%SB0009	no_prog	Set when an attempt is made to put the PLC in RUN mode when there is no executable application program stored in the CPU. Cleared by storing an application program to the CPU and putting the PLC in RUN mode.
%SB0010	bad_ram	Set when the CPU detects corrupted RAM memory at power-up. Cleared when the CPU detects that RAM memory is valid at power-up.
%SB0011	bad_pwd	Set when a password access violation occurs. Cleared when a password is successfully used to gain a privilege level.
%SB0012	nul_cfg	Set when an attempt is made to put the PLC in RUN mode when there is no configuration data present. Cleared when configuration data is present and the PLC is put in the RUN mode.
%SB0013	sft_cpu	Set when the CPU detects an unrecoverable error in the software. Cleared by cycling power to the CPU.

Table 4-3. Fault References - Continued

Reference	Nickname	Definition
%SB0014	stor_er	Set when an error occurs during a programmer store operation. Cleared when a store operation is completed successfully.
%SC0009	anyflt	Set when any fault occurs. Cleared when both fault tables are cleared.
%SC0010	syflt	Set when any fault occurs that causes an entry to be placed in the PLC Fault Table. Cleared when the PLC Fault table is cleared.
%SC0011	ioflt	Set when any fault occurs that causes an entry to be placed in the I/O Fault Table. Cleared when the I/O Fault table is cleared.
%SC0012	sy_pres	Set as long as there is at least one entry in the PLC Fault Table. Cleared when the PLC Fault Table has no entries.
%SC0013	io_pres	Set as long as there is at least one entry in the I/O Fault Table. Cleared when the I/O Fault Table has no entries.

Fault Side Effects

Three non-configurable faults described previously have side effects associated with them that you need to be aware of. A description of the side effects associated with these faults is provided in the following paragraphs.

- **Null System Configuration for RUN Mode**

If a Null System Configuration is detected on a transition from STOP to one of the RUN modes this fault is generated. This fault is informational in nature so that if a programmer is present, you can be informed of the condition. Running without a configuration is not fatal; it is equivalent to having I/O scanning suspended. The side effect of this fault is to perform the function of a Suspend I/O instruction.

- **PLC CPU Software Failure**

Whenever a PLC CPU Software Failure is logged the Series 90-20 CPU *immediately* transitions into a special Error Sweep mode. The only activity permitted when the PLC is in this mode is communications with the programmer. The only method of clearing this condition is to reset the PLC (cycle power).

- **PLC Sequence Store Failure**

During a Sequence Store (a store of program blocks and other data preceded with the special Start-of-Sequence command and ending with the End-of-Sequence command), if communications with the programming device performing the store is interrupted or any other failure occurs which terminates the download, the PLC Sequence Store Failure fault is logged. As long as this fault is present in the system, the PLC will not transition to RUN mode.

Accessing Additional Fault Information

The Fault Table Displays seen on the screen of the programmer on which LogiMaster 90-30/20/Microprogramming software is running contain basic information regarding the fault. If more detailed information is needed a hexadecimal dump of the fault can be obtained by positioning the cursor on the fault entry and pressing the Ctrl-F key sequence.

The last entry, **Correction**, for each fault explanation in this chapter lists the action(s) to be taken to correct the fault. If more than one action is listed, try each action in the order listed. Note that the correction description for some of the faults in this chapter includes the following statement:

Correction: Display the PLC Fault Table on the Programmer Contact GE Fanuc Technical Support, giving them all the information contained in the fault entry.

The statement “all the information contained in the fault entry” implies that you should tell Technical Support both the information readable directly from the Fault Table *and the hexadecimal information you see when you press the Ctrl-F key sequence*. Technical Support personnel will give you further instructions for action to be taken.

An example of the I/O FAULT SCREEN displaying this information is shown below. In the example, the faults in the table are Loss of I/O Module and Addition of I/O Module. To initiate the hexadecimal display of additional fault information, use the cursor up/downkeys to select a fault entry. When you press the Ctrl-F key combination, the hexadecimal information will be displayed on the line directly below the function key display.

Quick Guide to Fault Explanation and Correction

The following tables are a guide to help find fault explanations in this chapter. They contain a list of Fault Description and Fault Category groups arranged by Fault Table and Fault Group, and page numbers where each Fault Explanation can be found. Entries listed under the Fault Description (PLC Fault Table) or Fault Category (I/O Fault Table) headings are as they appear on the programmer’s screen.

Table 4-4. Guide to Finding PLC Fault Table Explanations and Corrections

Fault Table	Fault Description	Page Number
PLC FAULT TABLE	Bad User RAM	4-7
	Password Failure	4-8
	Null System Configuration for Run Mode	4-8
	PLC CPU Software Failure	4-8

PLC Fault Table Explanations

Each fault explanation contains the following information:

Name:

Error Code:

Description:

Correction:

Corrupted Memory

Some faults can occur because the RAM memory on the PLC CPU board has failed or become corrupted. These same faults may also occur because the system has been powered off and the battery voltage is too low to maintain memory or through excessive noise being applied to I/O circuits. To avoid excessive duplication of corrective

action in the following explanations, the Correction entry contains the following statement when corrupted memory may be a cause of the error:

Perform the corrections for Corrupted Memory.

These corrections are summarized as follows:

- Correction:**
- (1) If the system has been powered off, replace the battery. Battery voltage may be insufficient to maintain memory contents.
 - (2) If a noise problem exists, reload the program and retry the application program.
 - (3) Suppress noise on I/O with snubber circuits or by routing wires away from noise sources.
 - (4) Replace the CPU module (CPU 211). The integrated circuits associated with the PLC CPU may be failing.

Non-Configurable Faults

The Fault Action of **Non-Configurable Faults** cannot be changed. **Fatal** faults cause the PLC to enter a form of STOP Mode at the end of the sweep the error occurred in. **Diagnostic** faults are recorded in the appropriate fault table and any associated diagnostic variables are set. **Informational** faults are only recorded in the appropriate fault table.

Bad User RAM

The Fault Group **Bad User RAM** (Group 130) occurs when the PLC CPU detects corrupted user RAM. The PLC CPU will remain in *STOP* mode until a valid user program and Configuration File are downloaded. The default Fault Action for this group is **Fatal**.

- Name:** Corrupted User RAM on Power-Up
- Error Code:** 1
- Description:** The PLC Operating Software (Operating Software) generates this error when it detects corrupted user RAM on Power-Up.
- Correction:**
- (1) Reload the Configuration File, user program, and references (if any).
 - (2) Replace the RAM memory backup battery on the power supply.
 - (3) Verify that excessive noise is not causing the problem. If it is, take appropriate measures as described previously.

- Name:** Bad OpCode Detected
- Error Code:** 2
- Description:** The PLC Operating Software (Operating Software) generates this error when it detects a bad instruction in the user program.
- Correction:**
- (1) Reload the user program, and references (if any).
 - (2) Verify that excessive noise is not causing the problem. If it is, take appropriate measures as described previously.
 - (3) Replace the CPU module.

Name: PC Overflow
Error Code: 3
Description: The PLC operating software generates this fault if it starts to execute user logic outside of the established user logic memory.
Correction (1) Reload the user program.
 (2) Verify that excessive noise is not causing the problem. If it is, take appropriate measures as previously described.

Name: Program Syntax Error
Error Code: 4
Description: The user logic program contains an illegal instruction list sequence.
Correction Delete or change the illegal sequence. The program check function of the Hand-Held Programmer can be used to determine the step number of the illegal sequence.

Password Failure

The Fault Group **Password Failure** (Group 132) occurs when the PLC CPU receives a request to change to a new privilege level and the password included with the request is not valid for that level. The default Fault Action for this group is **Informational**.

Correction: Retry the request with the correct password.

Null System Configuration for Run Mode

The Fault Group **Null System Configuration for RUN Mode** (Group 134) occurs when the PLC transitions from *STOP* to one of the *RUN* modes and a Configuration File is not present. The transition to *RUN* is permitted, but no I/O scan occurs. The effect of this fault is to suspend the I/O sweep. The default Fault Action for this group is **Fatal**.

Correction: Download a Configuration File.

PLC CPU Software Failure

Faults in the Fault Group **PLC CPU Software Failure** (Group 135) are generated by the operating software of the Series 90-20 PLC CPU. They occur at many different points of system operation. When a **Fatal** fault occurs the PLC CPU *immediately* transitions into a special Error Sweep mode. The only activity permitted when the PLC is in this mode is communications with the programmer. The only method of clearing this condition is to cycle power on the PLC. The default Fault Action for this group is **Fatal**.

Name: User Memory Could Not Be Allocated
Error Code: 1 through B
Description: The PLC Operating Software (Memory Manager) generates these errors when software requests the Memory Manager to allocate or deallocate a block or blocks of memory from user RAM that are not legal. These errors should *not* occur in a production system.
Correction: Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.

Name: System Memory Unavailable
Error Code: D
Description: The PLC Operating Software (I/O Scanner) generates this error when its request for a block of system memory is denied by the Memory Manager because no memory is available from the system memory heap. It is *Informational* if the error occurs during the execution of a DO I/O Function Block. It is *Fatal* if it occurs during power-up initialization or auto configuration.
Correction: Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.

Name: System Memory Could Not Be Freed
Error Code: E
Description: The PLC Operating Software (I/O Scanner) generates this error when it requests the Memory Manager to deallocate a block of system memory and the deallocation fails. This error can only occur during the execution of a DoI/O Function Block.
Correction: (1) Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.
(2) Perform the corrections for Corrupted Memory.

Name: Invalid Scan Request of the I/O Scanner
Error Code: 10
Description: The PLC Operating Software (I/O Scanner) generates this error when the operating system or Do I/O Function Block scan requests neither a full nor a partial scan of the I/O. This should *not* occur in a production system.
Correction: (1) Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.

Name: Unknown IOC Type Found
Error Code: 12
Description: The PLC Operating Software (I/O Scanner) generates this error when an unknown IOC type is found in one of the I/O Scanner's data structures.
Correction: (1) Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.

Name: PLC Operating Software Error
Error Code: 13
Description: The PLC Operating Software generates this error when certain PLC Operating Software problems occur. This error should *not* occur in a production system.
Correction: (1) Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.

Name: PLC Operating Software Error
Error Code: 27 through 4E
Description: The PLC Operating Software generates these errors when certain PLC Operating Software problems occur. These errors should *not* occur in a production system.
Correction: Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.

Name: System Memory Errors
Error Code: 50 through 53
Description: The PLC Operating Software generates these errors when its request for a block of System Memory is denied by the Memory Manager because no memory is available or contains errors.
Correction: (1) Display the PLC Fault Table on the Programmer. Contact GE Fanuc PLC Technical Support, giving them all the information contained in the fault entry.
(2) Perform the corrections for Corrupted Memory.

Chapter 5

Power Supply and I/O Modules

This chapter contains specifications and wiring information for each of the available Series 90-20 I/O and power supply modules. The following table is provided as an aid to the location of I/O module specifications and wiring information in this chapter. I/O modules are listed by catalog number, module description, number of I/O points, and to quickly locate a particular I/O module - the page number where its description starts.

Table 5-1. Guide to Page Location for I/O Module Specifications

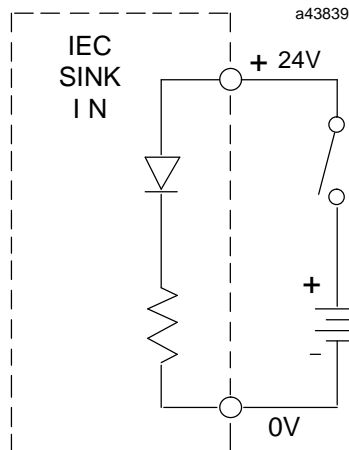
Catalog Number	Description of Module	I/O Points	Page Number
IC692MAA541	120 VACIn/120VACOut/120VAC PS	16in/12out	5-4
IC692MDR541	24 VDC in (Pos/Neg)/RelayOut/120VAC PS	16in/12out	5-13
IC692MDR741	24 VDC in (Pos/Neg)/RelayOut/24VAC PS	16in/12out	5-23

Definition of Positive and Negative Logic

The IEC definitions for positive logic and negative logic, as applied to Series 90-20 I/O modules, are defined as follows.

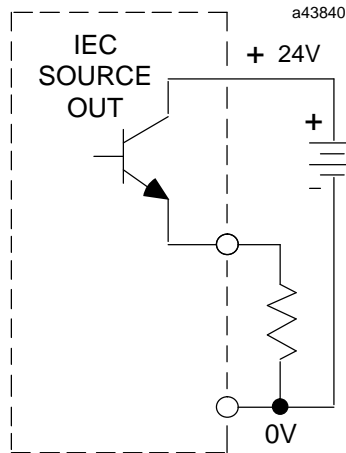
Positive Logic - Input Points

Positive logic input points are equivalent to IEC sink input points. Input modules designed with positive logic characteristics sink current from the input device to the user common or negative power bus. The input device is connected between the positive power bus and the input terminal.



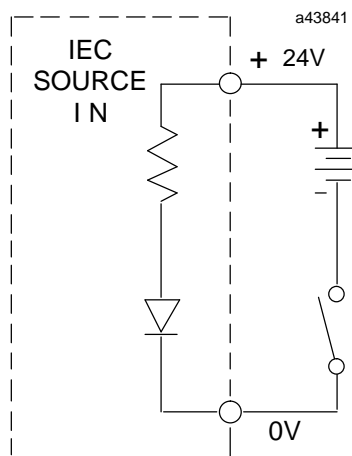
Positive Logic - Output Points

Positive logic output points are equivalent to IEC source output points. Output modules designed with positive logic characteristics source current to the loads from the user common or positive power bus. The load is connected between the negative power bus and the module output.



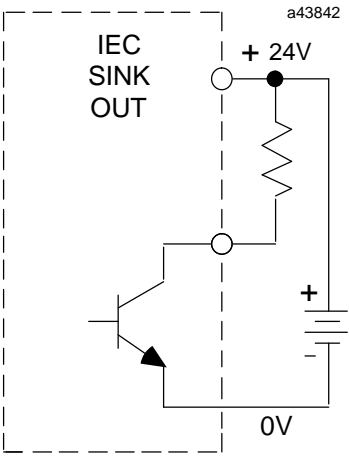
Negative Logic - Input Points

Negative logic input points are equivalent to IEC source inputs. Input modules designed with negative logic characteristics source current through the input device to the user common or positive power bus. The input device is connected between the negative power bus and the input terminal.



Negative Logic - Output Points

Negative logic output points are equivalent to IEC sink outputs. Output modules designed with negative logic characteristics sink current from the loads to the user common or negative power bus. The load is connected between the positive power bus and the output terminal.



120 VAC Input/120 VAC Triac Output/120 VAC Power Supply Module IC692MA A541

120 VAC Input, 16 Points

The 120 volt AC input circuits provide 16 input points with one common power input terminal. The input circuits are reactive (resistor/capacitor) inputs. Current into an input point results in a logic 1 in the input status table (%I). Input characteristics are compatible with a wide range of user-supplied input devices, such as pushbuttons, limit switches, and electronic proximity switches. Power to operate the field devices must be supplied by the user. The input circuits require an AC power source, they cannot be used with a DC power source.

Table 5-2. Specifications for 120 Volt AC Input Circuits

Rated Voltage:	120 volts AC
Input Voltage Range:	0 to 132 volts AC, 50/60Hz, sinusoidal
Inputs per Module:	16 (one group with a single common)
Isolation:	1500 volts RMS between field side and logic side
Input Current:	12.2 mA (typical) at rated voltage
Input Characteristics:	
On-state Voltage	79 to 132 volts AC
Off-state Voltage	0 to 20 volts AC
On-state Current	6 mA minimum
Off-state Current	2.2 mA maximum
On response Time	30 ms maximum
Off response Time	45 ms maximum
Operating Temperature:	0° to 60°C (32° to 140°F)
Storage Temperature:	-40° to +85° C (-40° to +185° F)
Humidity:	5 to 95% non-condensing

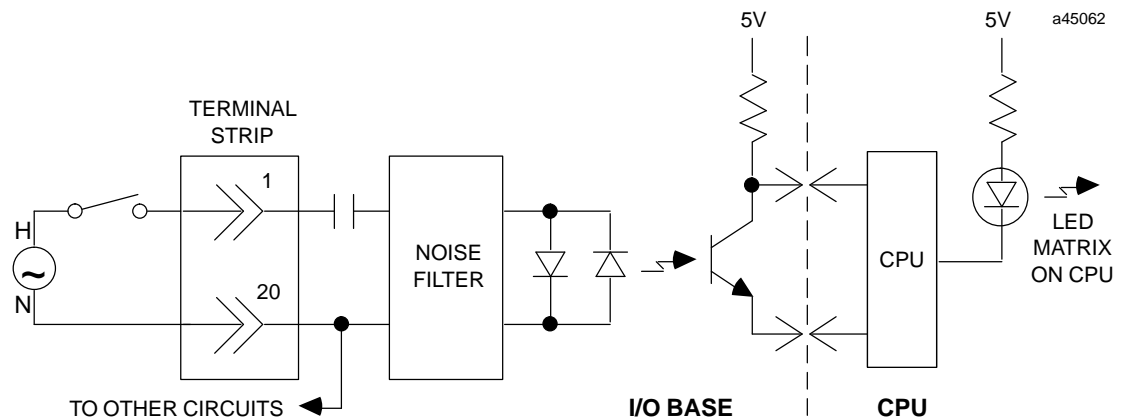


Figure 5-1. Typical 120 VAC Input Circuit

120 VAC Output - 1 Amp, 11 Points

The 120 volt, 1 Amp AC output circuits provide 11 output points in two isolated groups with 6 and 5 points in each group, respectively. Each group has a separate common associated with it. The two commons are not tied together inside the module. This allows each group to be used on different phases of the AC supply, or they can be powered from the same supply. Each group is protected with a 7 amp fuse for each common, and an RC snubber is provided for each output to protect against transient electrical noise on the power line. This module provides a high-degree of inrush current (10x the rated current) which makes the outputs suitable for controlling a wide range of inductive and incandescent loads. AC Power to operate loads connected to outputs must be supplied by the user. This module requires an AC power source, it can not be used with a DC power source.

User replaceable fuses are supplied internally on the common of each output group. This fuse does not guarantee that the output point will be protected by a direct short. It is recommended that each output point be externally fused (minimum 1 amp) to protect the output point. For lighter loads, the internal common fuse (7 amp) can be replaced with a 1 amp fuse to protect the output point without adding the external fusing.

Table 5-3. Specifications for 120 Volt AC Output, 1 Amp Circuits

Rated Voltage:	120 volts AC
Output Voltage Range:	85 to 132 volts AC, 50/60Hz
Outputs per Module:	11 (two groups, one with 5 and one with 6 outputs)
Isolation:	1500 volts RMS between field side and logic side; 500 volts RMS between each group
Output Current:	1 amp maximum per point with all circuits active. 2 amps maximum with adjacent points off - see derating tables.
Output Characteristics:	
Inrush Current	10 amps maximum for one cycle
Minimum Load Current	50 mA
Output Voltage Drop	1.5 volts maximum
Output Leakage Current	2.5 mA maximum
On Response Time	1 ms maximum
Off Response Time	1/2 cycle maximum
Operating Temperature:	0° to 60°C (32° to 140°F)

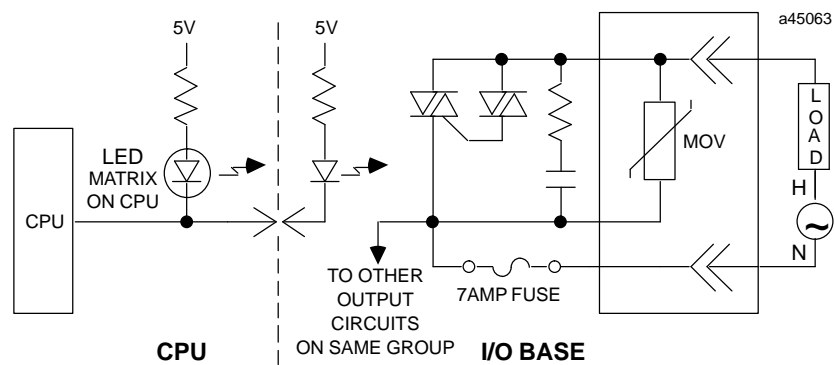


Figure 5-2. Typical 120 VAC Triac Output Circuit

Bleeder Resistor Calculation for Light Loads

Leakage current is present through the output circuit when the output point is off. This leakage current is caused by surge absorber circuits. Light loads, such as neon lamps or small relays, are susceptible to being turned on or prevented from turning off by this small leakage current. A bleeder resistor in parallel with the load will prevent turn-on of these devices.

The required size of this bleeder resistor can be calculated as follows:

1. Calculate the resistor value.

$$R_{\text{bleeder}} = \frac{V_{\text{min}}}{I_{\text{leak}}}$$

where V_{min} is the minimum voltage required to turn on the load, I_{leak} is the maximum leakage current through the output, and R_{bleeder} is the maximum resistor value.

2. Choose the next lower standard resistor value as the actual bleeder resistor.
3. Calculate the power rating required for the resistor.

where V_{load} is the normal voltage on the load.

Example:

The minimum voltage required to turn on a neon light is 20VAC. The normal voltage applied is 120VAC.

$$R = \frac{(20\text{VAC})}{2.5\text{mA}} = 8000 \text{ ohms}$$

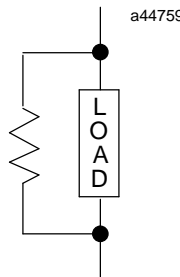
Choose 7500 ohms which is the closest standard value resistor that is less than 8000 ohms.

$$P = \frac{120 \times 120}{7500}$$

$$P = 3.84 \text{ watts}$$

Choose the next highest standard power rating rating for the resistor, which is 5 watts. The selected bleeder resistor for this example is 7.5K ohms, 5 watts.

Typical Bleeder Resistor Connection



High Speed Counter Input Circuits

Two 24VDC negative logic input circuits are available to provide inputs for Type A counter inputs: count and strobe/preload. These circuits are self-powered by an internal 12VDC supply.

Table 5-4. High Speed Counter Input Circuit Specifications

Rated Input Voltage	12/24voltsDC
Input Voltage Range:	0 to 30 volts DC
Input Current:	2.8mA typical
Input Threshold Voltage †	ON : 0.7V maximum OFF : 10V minimum
Response Time (low filter):	10mstypical
Response Time (high filter):	40μstypical
Isolation Voltage:	500 volts field side to logic side
Operating Temperature:	0° to 60°C (32° to 140°F)

† Threshold Voltage (with respect to count or strobe (+) input and common (-)).

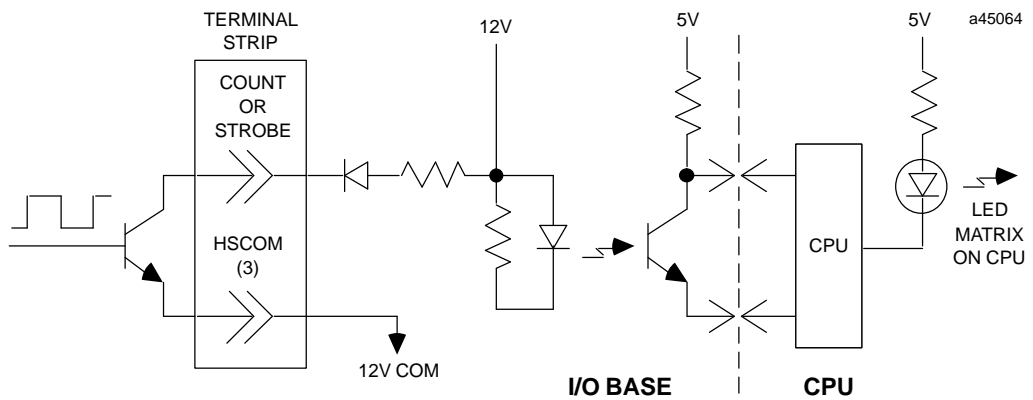


Figure 5-3. Typical High Speed Counter Input Circuit

These inputs are only used for the High Speed Counter function and their ON/OFF status is not reported in the input status table. With the high frequency selection, the highest frequency guaranteed to be detected is 10Khz with a 50/50 duty cycle (50ms ON, 50ms OFF). The maximum frequency in the low frequency mode is 30 Hz (16.7 ms ON, 16.7 ms OFF).

5/12/24 Volt DC Negative Output Q1 or High Speed Counter Output - 1.5 Amp

The 5/12/24 volt DC negative logic 1.5 Amp output circuit provides one isolated output point. The output circuit is designed to have negative logic characteristics in that it sinks current from the loads to the user common or negative power bus. The output device is connected between the positive power bus and the module output. The output characteristics are compatible with a wide range of user-supplied load devices, such as: motor starters, solenoids, and indicators. Power to operate the field devices must be supplied by the user. Power to control the output point is provided by an internal 12 volt supply.

Table 5-5. Specifications For 5/12/24 Volt DC Negative Logic - 2 Amp Output Circuit

Rated Voltage:	5/12/24voltsDC
Output Voltage Range:	5 to 30 volts DC
Outputs per Module	1
Isolation:	500 volts RMS between field side and logic side; 500 volts RMS between each group
Output Current:	1.5 amps maximum at 12 to 24 VDC, 60°C (140°F) 2 amps maximum at 12 to 24 VDC, 0 to 25°C (32 to 77°F) 75 amps maximum at 5 to 11 VDC, 60 °C (140°F)
Output Characteristics	
Output Voltage Drop	0.2 volts maximum, 12 to 24 VDC, 25°C (32 to 77°F)
Off-state Leakage	250 μA maximum, 0.4V maximum, 5 to 11 VDC 25°C (77°F)
On Response Time	0.1 ms
Off Response Time	0.1 ms
Operating Temperature	0° to 60°C (32° to 140°F)

- (1) Note that the Q1 common is tied internally to the high speed counter common.
- (2) It is recommended that this output be fused with an external fuse since the internal fuse for the circuit is not easily replaceable (soldered into board).

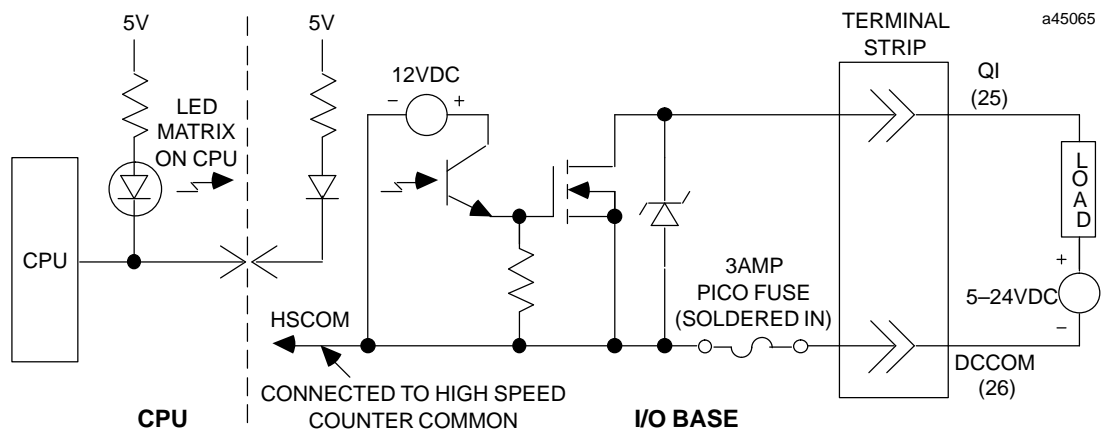


Figure 5-4. Typical High Speed Counter Output Circuit

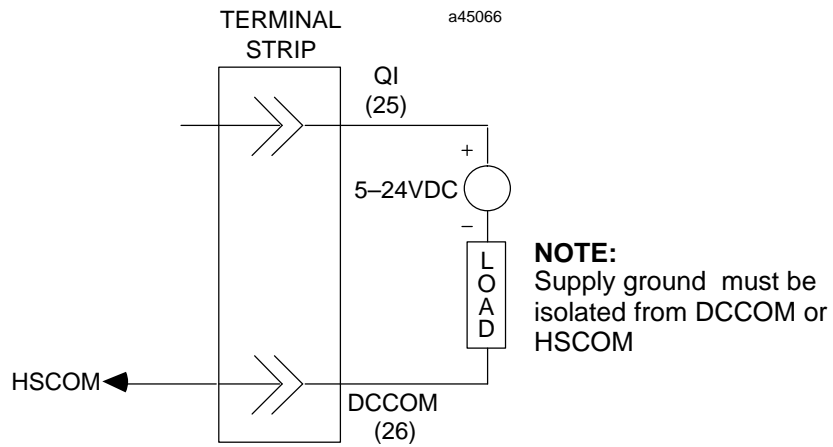


Figure 5-5. Alternate Connection as Positive Output

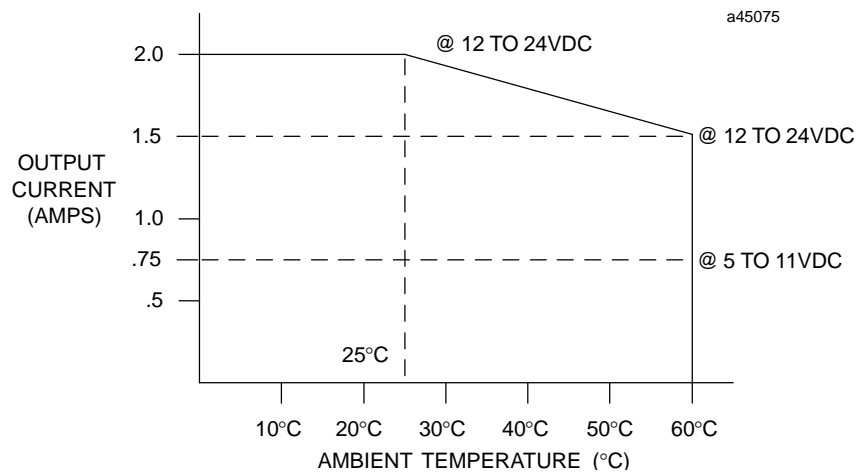


Figure 5-6. Temperature vs. Current for 5/12/24 VDC Negative Output Q1 or High Speed Counter Output

120 VAC Power Supply

Table 5-6. Specifications for 120 VAC Power Supply

Range	102 volts AC to 132 volts AC
Frequency	47 to 63 Hz
Input Current	200mA, maximum
Hold-up	10 ms, minimum
Inrush Current	2 amps maximum

Replacing Fuses

The Series 90-20 PLC provides replaceable fuses on this module for output points. To replace these fuses:

- Remove power from the unit.
- Remove the CPU module.
- Confirm that power is removed from the I/O Power Supply baseplate. Use a small screw driver or fuse extractor to remove the fuse from the clips on the I/O circuit board. The output fuses are located near the output terminal board. The output fuses are located on the common of several circuits; therefore, if a fuse is blown it will prevent the entire group associated with it from working. The following figure shows the location of the fuses on the I/O Base module, and the table following the figure provides a list of these fuses with location, rating, part number, third party source, and the type of fuse.

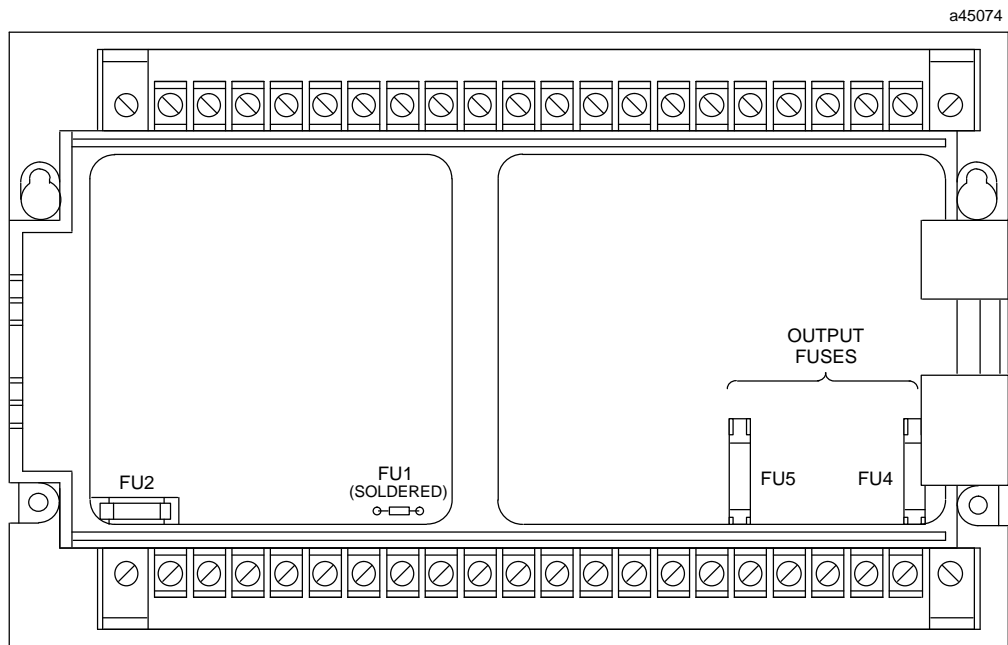


Figure 5-7. Location of Fuses for IC692MAA541 I/O Power Supply Base

Warning

Failure to remove power when changing fuses could cause severe or fatal injury to the operator or maintenance personnel.

Table 5-7. List of Fuses

Location	Current Rating	GE Fanuc Fuse Part Number	Third Party Source and Part Number	Fuse Type
FU1	3A	259A9598P10	Littlefuse,239003	Soldered-in
FU2	250mA	44A724627-120	Bussman,GMD0.25 Littlefuse,239.250	Replaceable
FU4,FU5	7A	259A9578P18	Bussman,AGC-07 Littlefuse,312007	Replaceable

Field Wiring Information

The following figure provides wiring information for connecting user supplied input and output devices and a power source to the 120 VAC Input/120 VAC Output Module.

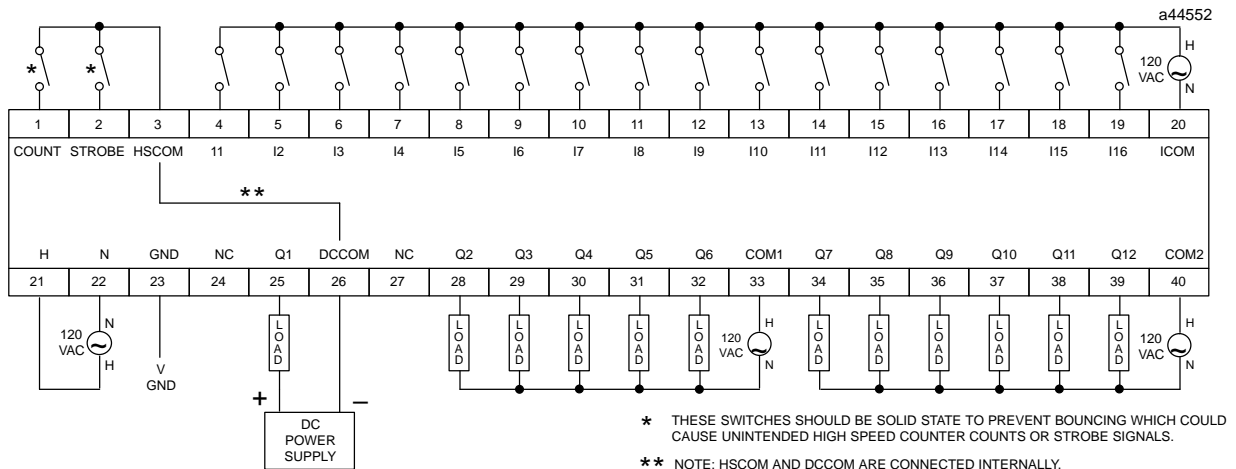


Figure 5-8. Field Wiring Connections - IC692MAA541

Derating Information

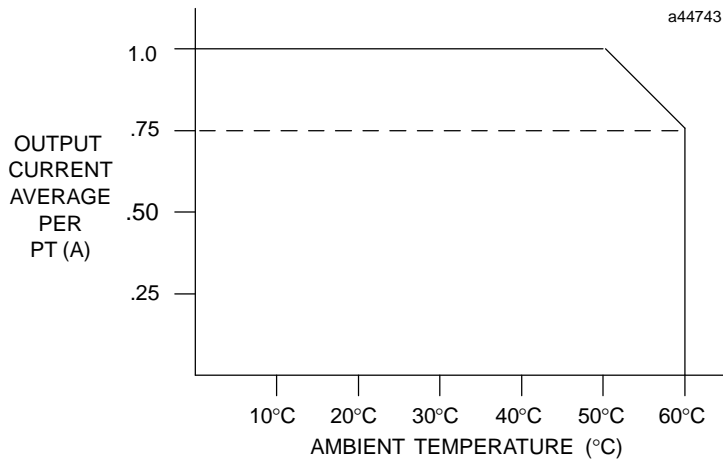


Figure 5-9. Triac Output Current vs. Temperature (Q2 - Q12)

Note

The following maximum current ratings must be followed so that the current limitations of this module are not exceeded.

- 4A maximum per group of four consecutive outputs below 50°C.
- 3A maximum per group of four consecutive outputs 60°C.
- 2A maximum on any one point. Keep high current points farthest apart.

Examples of Current for Output Points (Q2 - Q12)

	Output	Example 1	Example 2	Example 3
At 60°C	Q2	2A	1A	.75A
	Q3	0	1A	.75A
	Q4	1A	0	.75A
	Q5	0	1A	.75A

	Output	Example 1	Example 2	Example 3
Below 50°C	Q2	2A	2A	1A
	Q3	0	0	1A
	Q4	2A	1A	1A
	Q5	0	1A	1A

Note

For non-standard orientation the temperature rating is derated by 10°C.

DC Input/Relay Output/120 VAC Power Supply Module IC692MDR541

24 VDC Positive/Negative Logic Input, 16 Points

The 24 volt DC positive/negative logic input circuits provide 16 input points in two groups, with 4 inputs in one group and 12 inputs in the other group. The input circuits are designed to have positive or negative logic characteristics. To connect the inputs as positive logic circuits, the input switching device is connected between the positive power bus and the module input. The negative bus is connected to the input circuit common. To connect the inputs as negative logic circuits, the input switching device is connected between the negative power bus and the module input. The positive bus is connected to the input circuit common. Current into an input point results in a logic 1 in the input status table (%I).

Input characteristics are compatible with a wide range of input devices, such as: pushbuttons, limit switches, and electronic proximity switches. Power to operate field devices and the input circuits is supplied by an isolated +24 VDC supply. This power supply is limited to 220 mA maximum.

Table 5-8. Specifications For 24 Volt DC Positive/Negative Logic Input Circuits

	Negative Logic Mode	Positive Logic Mode
Points/Common	12 (I5-I16) and 4 (I1-I4 ; includes 2 High Speed Counter inputs)	
Rated Input Voltage	24 volts DC	24 volts DC
Input Voltage Range	0 to 30 volts DC	0 to 30 volts DC
Input Current	7.3mA typical	7.3mA typical
Input Threshold Voltage	ON: 15V minimum OFF: 5V maximum	15V minimum 5V maximum
Input Threshold Current	ON: 4mA maximum OFF: 1.5mA minimum	4mA maximum 1.5mA minimum
Response Time	4mstypical	4mstypical
Isolation Voltage:	1500V RMS field side to logic side 1500V RMS between each group if one group is powered by an external 24V power supply.	
Operating Temperature:	0° to 60°C 32° to 140°F	

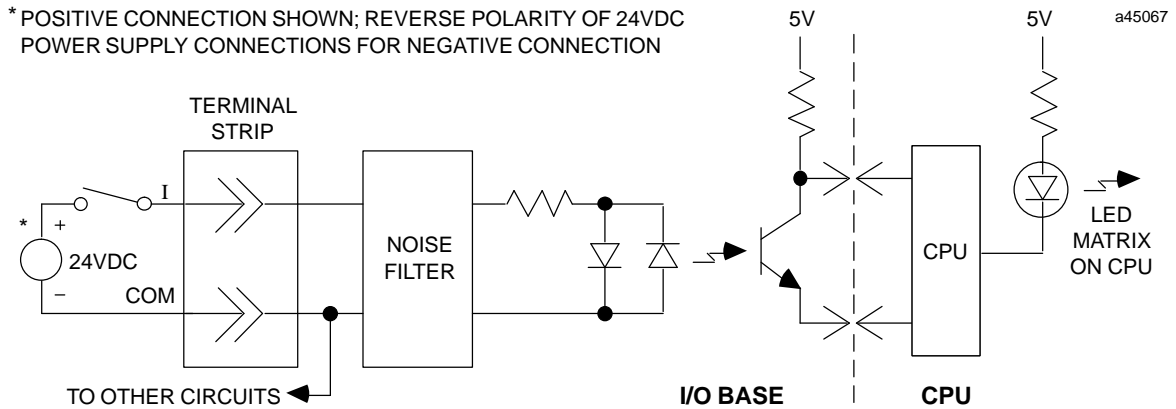


Figure 5-10. Typical 24 VDC Positive/Negative Logic Input Circuit

Relay Output, Normally Open, 2 Amp - 11 Points

The 2 Amp Relay Output circuits provide 11 normally-open relay circuits for controlling output loads provided by the user. The output switching capacity of each of these circuits is 2 amps. The output points are arranged in three groups; one with three outputs and two with four outputs. Each group has a common power output terminal. The relay outputs can control a wide range of user-supplied load devices, such as: motor starters, solenoids, and indicators. Power for the internal relay circuits is provided by the +5 volt DC internal supply. The user must supply the AC or DC power to operate field devices. Each common has a 10 amp replaceable fuse.

Table 5-9. Specifications For Relay Output, 2 Amp Circuits

Operating Voltage:	5 to 30 volts DC 5 to 250 volts AC
Outputs per Module:	11 (three groups: one with 3 outputs; two with 4 outputs)
Isolation:	1500 volts RMS
Leakage Current:	1 mA at 240 VAC maximum
Maximum Load:	2 amps resistive maximum per output
Minimum Load:	10 mA
Maximum Inrush:	20 amps maximum for one cycle
On Response Time:	15 ms maximum
Off Response Time:	15 ms maximum
Operating Temperature:	0° to 60°C (32° to 140°F)
Contact Life:	(also refer to the following table)
Mechanical:	20 x 10 ⁶ operations
Electrical:	200,000 operations resistive load (2A), typical.

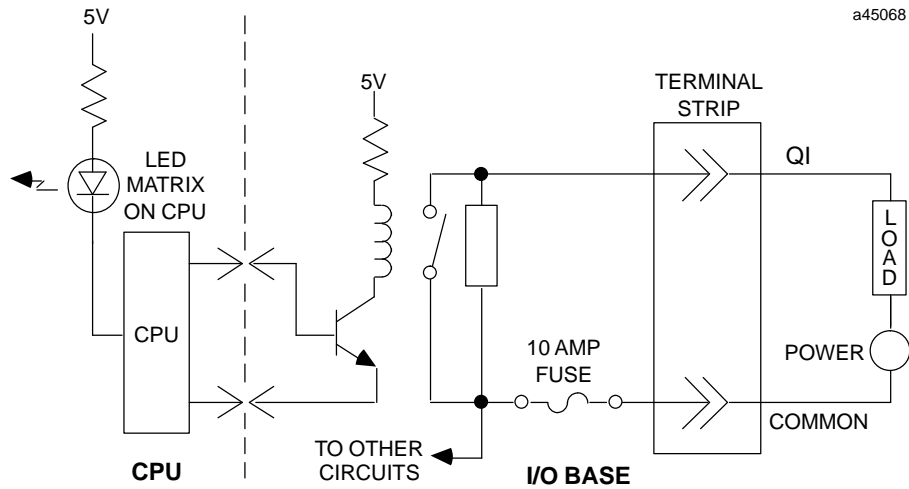


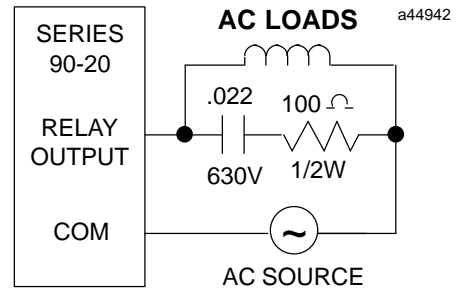
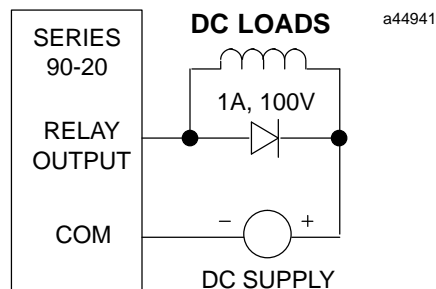
Figure 5-11. Typical Relay Output Circuit

Table 5-10. Typical Contact Life

Voltage	Current		Typical Operations
	Resistive	Lamp and Solenoid	
240VAC, 120VAC, 24VDC	2A	.6A	200,000
240VAC, 120VAC, 24VDC	1A	.3A	400,000
240VAC, 120VAC, 24VDC	.5A	.1A	800,000

User replaceable fuses are supplied internally on the common of each output group. This fuse does not guarantee that the output point will be protected by a direct short. It is recommended that each output point be externally fused (minimum 2 amp) to protect the output point. For lighter loads, the internal common fuse (10 amps) can be replaced with a 5 amp fuse to protect the output point without adding the external fusing.

Relay contact life, when switching inductive loads, will approach resistive load contact life if suppression circuits are used. The following figures are examples of typical suppression circuits for DC and AC loads. The 1A, 100V diode shown in the DC load typical suppression circuit is an industry standard 1N4934.



High Speed Counter Input Circuits, I1 and I2

Two 12/24VDC input positive/negative logic circuits are available to provide inputs for type A counter inputs: count and strobe/preload. These circuits function as either %I1 and %I2 or as count and strobe/preload inputs. These inputs share the same common as %I3 and %I4. The inputs can be connected either as positive or negative inputs as described in the DC input circuit section. With the high frequency selection, the highest frequency guaranteed to be detected is 10Khz with a 50/50 duty cycle (50ms ON, 50ms OFF). The maximum frequency in the low frequency mode is 30 Hz (16.7 ms ON, 16.7 ms OFF).

Table 5-11. High Speed Counter Input Circuit Specifications

	Negative Logic Mode	Positive Logic Mode
Rated Input Voltage:	12VDC/24VDC	12VDC/24VDC
Input Voltage Range:	0 to 30VDC	0 to 30VDC
Input Current:	8.2 mA typical at 24VDC 2.8 mA typical at 12VDC	8.2 mA typical at 24VDC 2.8 mA typical at 12VDC
Input Threshold Voltage	ON: 11.5V minimum OFF: 5V maximum	11.5V minimum 5V maximum
Input Threshold Current	ON: 2.9 mA maximum at 11.5V OFF: .5 mA minimum at 5V	2.9 mA maximum at 11.5V .5 mA minimum at 5V
Response Time (low filter):	10mstypical	10mstypical
Response Time (high filter):	40μstypical	40μstypical
Isolation Voltage:	1500V field to logic side	1500V field to logic side
Operating Temperature:	0° to 60°C (32° to 140°F)	0° to 60°C (32° to 140°F)

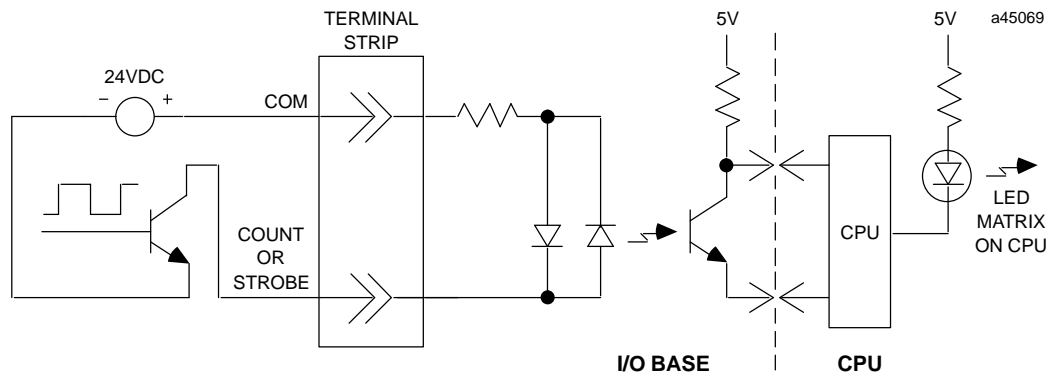


Figure 5-12. High Speed Counter Circuit - Negative Logic Connection

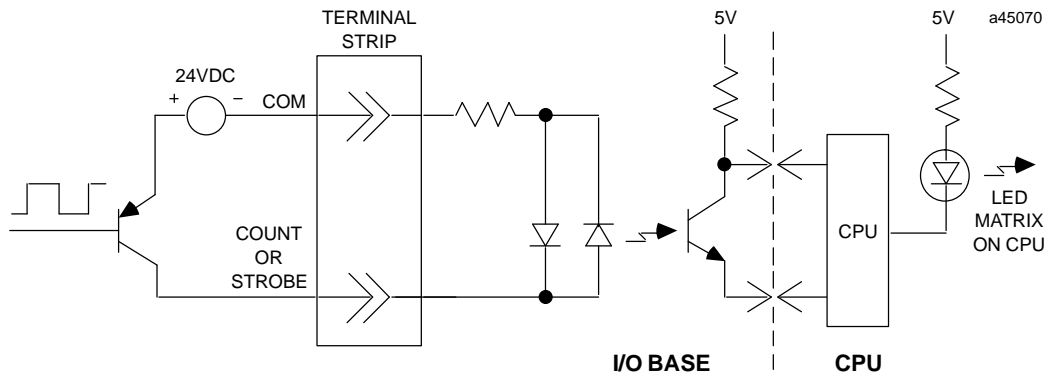


Figure 5-13. High Speed Counter Circuit - Positive Logic Connection

5/12/24 Volt DC Negative Output Q1 or High Speed Counter Output-1.5 Amp

The 5/12/24 volt DC negative logic 1.5 Amp output circuit provides one isolated output point. The output circuit is designed to have negative logic characteristics in that it sinks current from the loads to the user common or negative power bus. The output device is connected between the positive power bus and the module output.

The output characteristics are compatible with a wide range of user-supplied load devices, such as: motor starters, solenoids, and indicators. Power to operate the field devices must be supplied by the user.

Table 5-12. Specifications For 5/12/24 Volt DC Negative Logic - 1.5 Amp Output Circuit

Rated Voltage:	5/12/24VDC
Output Voltage Range:	5 to 30 VDC
Outputs per Module:	1
Isolation:	1500 volts RMS between field side and logic side; 500 volts to other groups
Output Current:	2 amps maximum, 12 to 24 VDC, 0 to 25 °C (32 to 77 °F); see derating diagram below 1.5 amps maximum, 12 to 24 VDC, 60 °C (140 °F) .75 amps maximum, 5 to 11 VDC, 60 °C (140 °F)
Output Characteristics:	
Output Voltage Drop	0.2 volts maximum at 12 to 24 VDC, 25 °C (77 °F) 0.4 volts maximum at 5 to 11 VDC, 25 °C (77 °F)
Off-state Leakage	250 μA maximum
On Response Time	.1 ms maximum
Off Response Time	.1 ms maximum
Operating Temperature:	0° to 60°C (32° to 140°F)

It is recommended that this output be externally fused since the internal fuse is not easily replaceable (it is soldered into the board).

Caution

Connecting the power supply to DCPWR (+) and DCCOM (-) with polarity reversed will cause an internal fuse to blow.

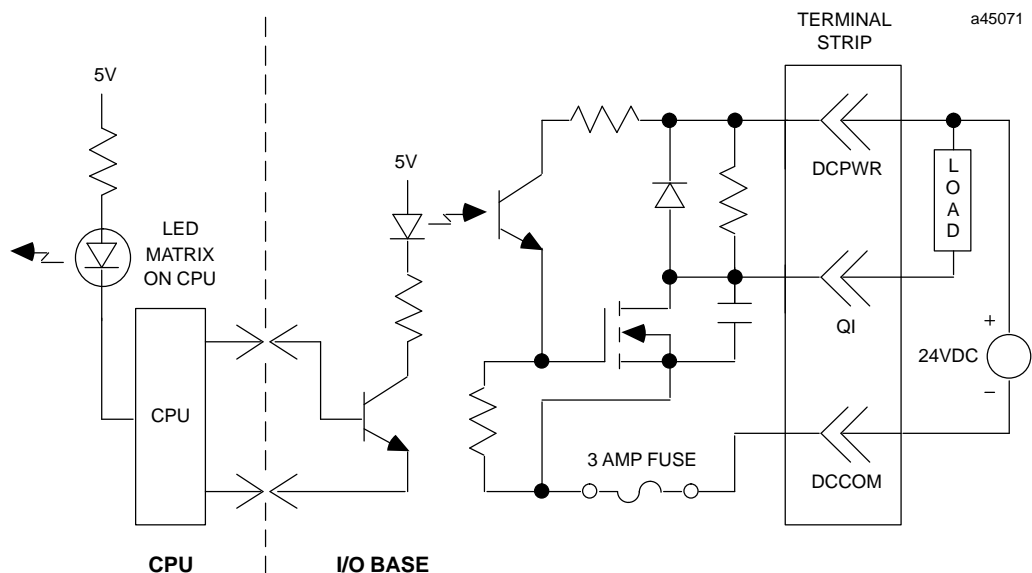


Figure 5-14. Typical 5/12/24 Volt DC Negative Logic Output Circuit

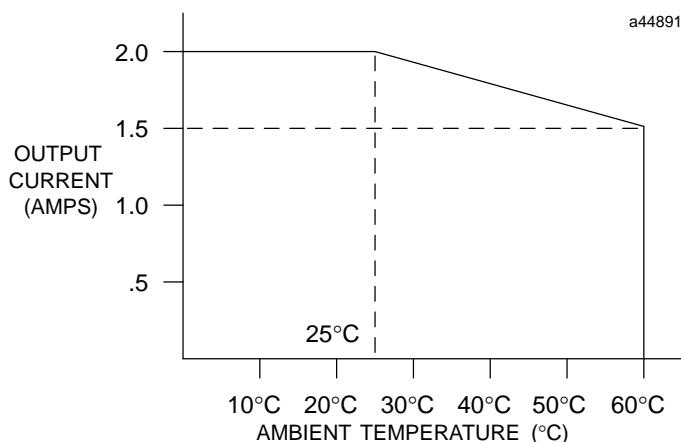


Figure 5-15. Temperature vs. Current for High Speed Counter Output

24 VDC Power Supply On DC Input Modules

An isolated 24 VDC output power supply is available on the I/O Base modules with DC input circuits. This supply is available for user devices and is typically used to power the DC input circuits at about 7.3 mA per input. The combination of input circuit current and external device current must not exceed 220 mA.

120 VAC Power Supply

Table 5-13. Specifications for 120 VAC Power Supply

Range	102 volts AC to 132 volts AC
Frequency	47 to 63 Hz
Input Current	250mA, maximum
Hold-up	10 ms, minimum
Inrush Current	2.4 amps maximum

Table 5-14. Specifications for 24 VDC Power Supply

Voltage:	24 VDC, " 10%
Current:	220mA maximum
Fusing:	.25 amp fast blow (removable)

Replacing Fuses

The Series 90-20 PLC provides replaceable fuses for output points and for a user accessible 24 volt power supply (on the dc input versions of I/O Power Supply baseplate only). To replace these fuses:

- Remove power from the unit.
- Remove the CPU module.
- Confirm that power is removed from the I/O Power Supply baseplate. Use a small screw driver or fuse extractor to remove the fuse from the clips on the I/O circuit board. The 24 volt power supply fuse is located near the input terminal board. The output fuses are located near the output terminal board. The output fuses are located on the common of several circuits; therefore, if a fuse is blown it will prevent the entire group associated with it from working. The following figure shows the location of the fuses on the I/O Base module, and the table following the figure provides a list of these fuses with location, rating, part number, third party source, and the type of fuse.

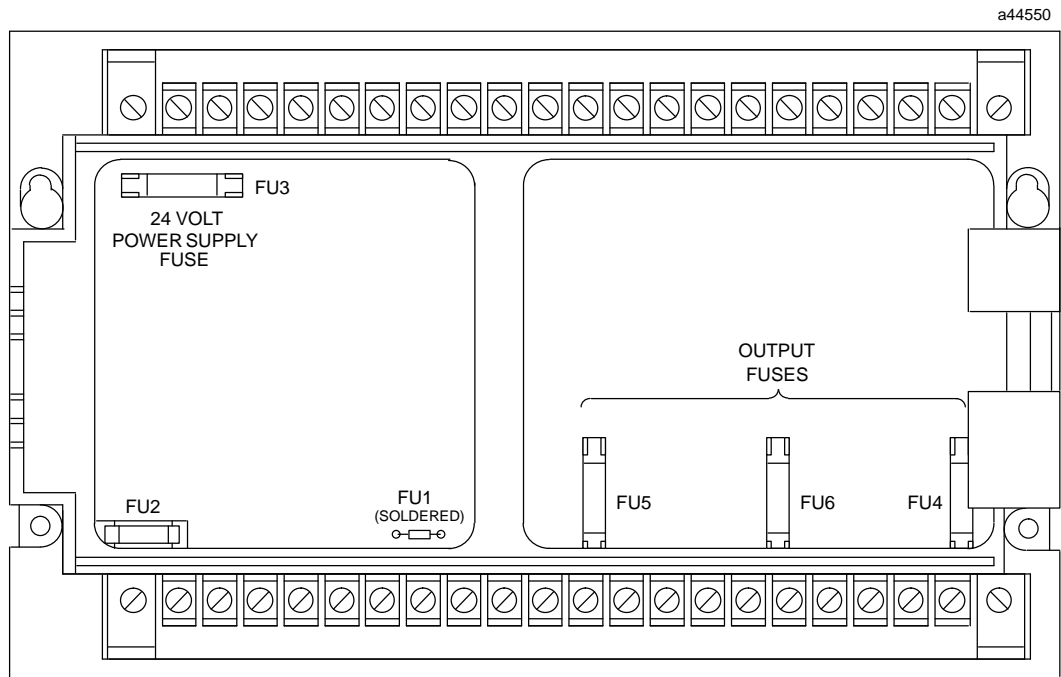


Figure 5-16. Location of Fuses

Warning

Failure to remove power when changing fuses could cause severe or fatal injury to the operator or maintenance personnel.

Table 5-15. List of FusesDC in Relay Out/120 VAC PS module

Location	Current Rating	GE Fanuc Fuse Part Number	Third Party Source and Part Number	Fuse Type
FU1	3A	259A9598P10	Littlefuse,239003	Soldered-in
FU2	250mA	44A724627-105	Bussman,GMD0.50 Littlefuse,239.500	Replaceable
FU3	250mA	259A9578P6	Bussman,AGC-1/4 Littlefuse,312.250	Replaceable
FU4, FU5, FU6	10A	259A9578P19	Bussman,AGC-10 Littlefuse,312010	Replaceable

Field Wiring Information

The following figure provides wiring information for connecting user supplied input and output devices and power sources for the 120 VAC Power Supply/ DC input / Relay Output Base Module.

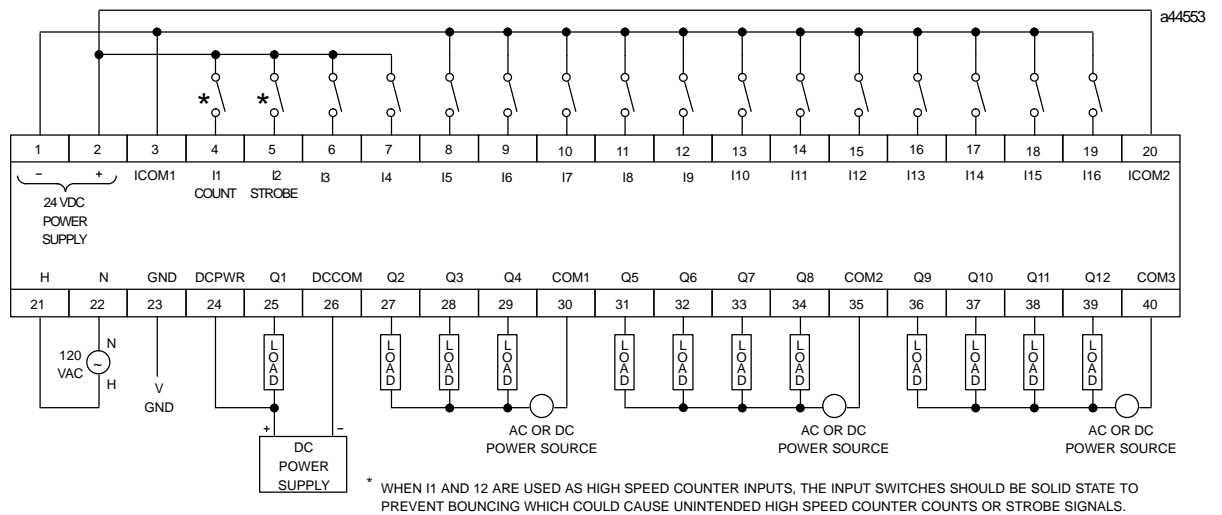


Figure 5-17. Field Wiring Information - IC692MDR541

Note

Although I1 through I4 are shown connected as positive Logic; and I5 through I16 are shown connected as negative Logic, all inputs can be connected as either positive or negative logic.

Derating Information

The following figure provides derating information for this module.

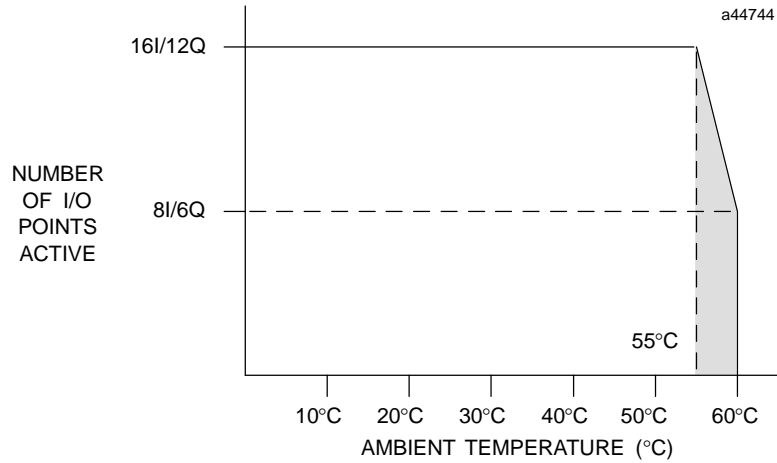


Figure 5-18. Temperature vs. Input/Output Points for IC692MDR541

Notes

- (1) The Hand-Held Programmer or RS-422/RS-485 to RS-232 Converter should not be connected continuously in the shaded region (above 55°C (131°F)).
- (2) For non-standard orientation, derate the temperature in the above figure by 10°C (18°F).

DC Input/Relay Output/240 VAC Power Supply Module IC692MDR741

24 VDC Positive/Negative Logic Inputs 16 Points

The 24 volt DC positive/negative logic input circuits provide 16 input points in two groups of 4 and 12 points. The input circuits are designed to have positive or negative logic characteristics. To connect the inputs as positive logic circuits, the input switching device is connected between the positive power bus and the module input. The negative bus is connected to the input circuit common. To connect the inputs as negative logic circuits, the input switching device is connected between the negative power bus and the module input. The positive bus is connected to the input circuit common. Current into an input point results in a logic 1 in the input status table (%I). Input characteristics are compatible with a wide range of input devices, such as: pushbuttons, limit switches, and electronic proximity switches. Power to operate field devices and the input circuits is supplied by an isolated +24 VDC supply. This power supply is limited to 220 mA maximum.

Table 5-16. Specifications For 24 Volt DC Negative/Positive Logic Input Circuits

	Negative Logic Mode	Positive Logic Mode
Points/Common	12 (I5-I16) and 4 (I1-I4; includes 2 High Speed Counter inputs)	
Rated Input Voltage	24 volts DC	24 volts DC
Input Voltage Range	0 to 30 volts DC	0 to 30 volts DC
Input Current	7.3mA typical	7.3mA typical
Input Threshold Voltage	ON: 15V minimum OFF: 5V maximum	ON: 15V minimum OFF: 5V maximum
Input Threshold Current	ON: 4mA maximum OFF: 1.5mA minimum	ON: 4mA maximum OFF: 1.5mA minimum
Response Time	4mstypical	4mstypical
Isolation Voltage:	1500V RMS field side to logic side 1500V RMS between each group if one group is powered by an external 24V power supply.	
Operating Temperature:	0° to 60°C (32° to 140°F)	

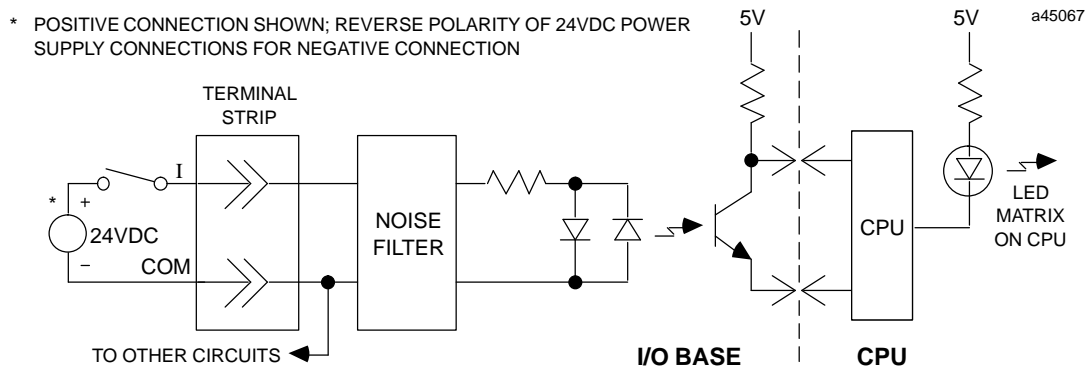


Figure 5-19. Typical 24VDC Positive/Negative Logic Input Circuit

Relay Output, Normally Open, 2 Amp - 11 Points

The 2 amp relay output circuits provide 11 normally-open relay circuits for controlling output loads provided by the user. The output switching capacity of these circuits is 2 amps. The output points are arranged in three groups; one group with three outputs and two groups with four outputs each. Each group has a common power output terminal. The relay outputs can control a wide range of user-supplied load devices, such as: motor starters, solenoids, and indicators. Power for the internal relay circuits is provided by the +5 volt internal supply. The user must supply the AC or DC power to operate field devices. There are 10 amp replaceable fuses on each common.

Table 5-17. Specifications For Relay Output, 2 Amp Circuits

Operating Voltage:	5 to 30 volts DC 5 to 250 volts AC
Outputs per Module:	11 (three groups: one with 3 outputs and two with 4 outputs each)
Fusing:	10A fuse on each common (replaceable)
Isolation:	1500 volts RMS
Maximum Load:	2 amps resistive maximum per output
Minimum Load:	10 mA
Leakage Current:	1 mA maximum at 240 VAC
Maximum Inrush:	20 amps maximum for one cycle
On Response Time:	15 ms maximum
Off Response Time:	15 ms maximum
Operating Temperature:	0° to 60°C (32° to 140°F)
Contact Life:	(also refer to the following table)
Mechanical:	20 x 10 ⁶ operations
Electrical:	200,000 operations resistive load (2A), typical.

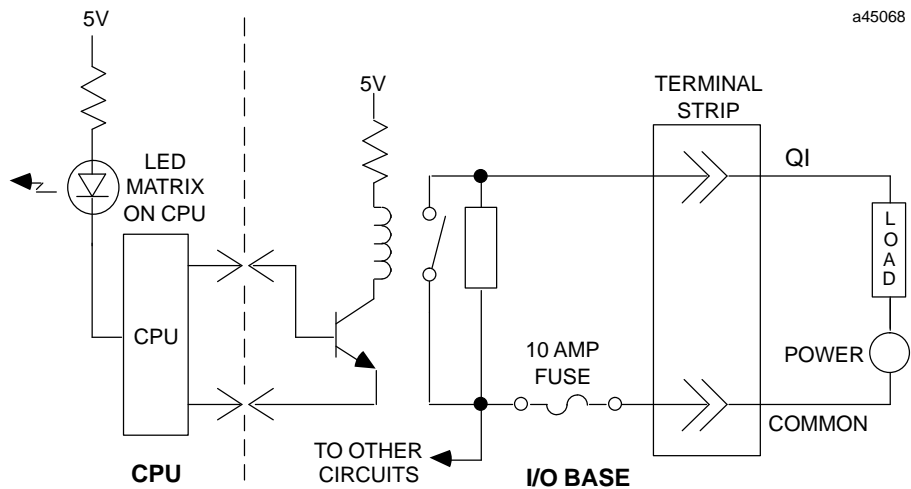


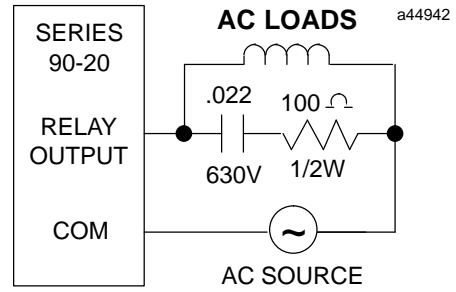
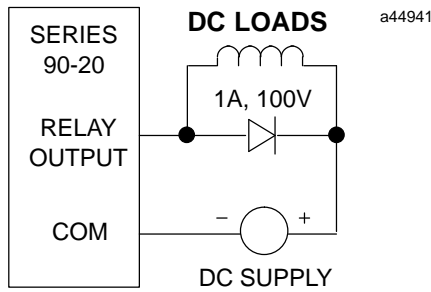
Figure 5-20. Relay Output Circuit

Table 5-18. Typical Contact Life

Voltage	Resistive	Current Lamp and Solenoid	Typical Operations
240VAC, 120VAC, 24VDC	2A	.6A	200,000
240VAC, 120VAC, 24VDC	1A	.3A	400,000
240VAC, 120VAC, 24VDC	.5A	.1A	800,000

Relay contact life, when switching inductive loads, will approach resistive load contact life if suppression circuits are used. The following figures are examples of typical suppression circuits for AC and DC loads. The 1A, 100V diode shown in the DC load typical suppression circuit is an industry standard 1N4934.

Typical Suppression Circuits



Fusing

User replaceable fuses are supplied internally on the common of each output group. This fuse does not guarantee that the output point will be protected by a direct short. It is recommended that each output point be externally fused (minimum 2 amp) to protect the output point. For lighter loads, the internal common fuse can be replaced with a 5 amp fuse to protect the output point without adding the external fusing.

High Speed Counter Input Circuits, I1 and I2

Two 12/24VDC input positive/negative logic circuits are available to provide inputs for type A counter inputs: count and strobe/preload. These circuits function as either %I1 and %I2 or as count and strobe/preload inputs. These inputs share the same common as %I3 and %I4. The inputs can be connected either as positive or negative inputs as described in the DC input circuit section. With the high frequency selection, the highest frequency guaranteed to be detected is 10Khz with a 50/50 duty cycle (50ms ON, 50ms OFF). The maximum frequency in the low frequency mode is 30 Hz (16.7 ms ON, 16.7 ms OFF).

Table 5-19. High Speed Counter Input Circuit Specifications

	Negative Logic Mode	Positive Logic Mode
Rated Input Voltage:	12VDC/24VDC	12VDC/24VDC
Input Voltage Range:	0 to 30VDC	0 to 30VDC
Input Current:	8.2 mA typical at 24VDC 2.9 mA typical at 12VDC	8.2 mA typical at 24VDC 2.9 mA typical at 12VDC
Input Threshold Voltage ON:	11.5V minimum	11.5V minimum
OFF:	5V maximum	5V maximum
Input Threshold Current ON:	2.9 mA maximum at 11.5V	2.9 mA maximum at 11.5V
OFF:	.5 mA minimum at 5V	.5 mA minimum at 5V
Response Time (low filter):	10mstypical	10mstypical
Response Time (high filter):	40[micro]stypical	40μstypical
Isolation Voltage:	1500V field to logic side	1500V field to logic side
Operating Temperature:	0° to 60°C (32° to 140°F)	0° to 60°C (32° to 140°F)

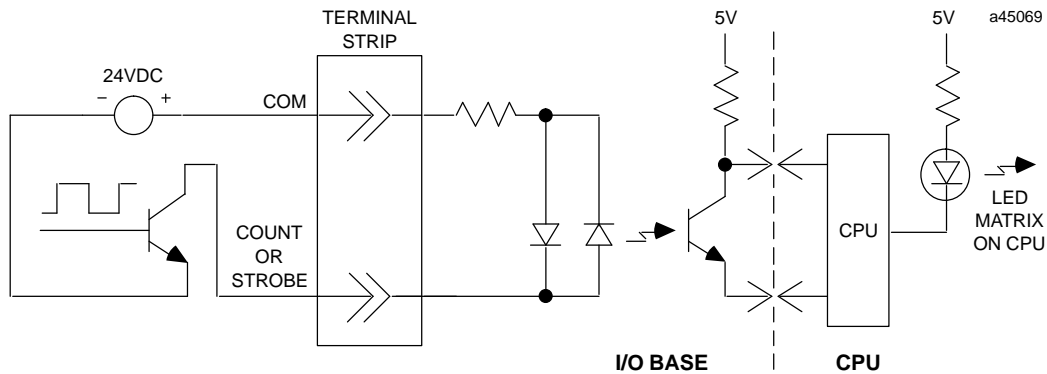


Figure 5-21. High Speed Counter Input Circuit

5/12/24 Volt DC Positive Output Q1 or High Speed Counter Output-1 Amp

The 5/12/24 volt DC positive logic 1 Amp output circuit provides one isolated output point. The output circuit is designed to have positive logic characteristics in that it sources current to the loads from the user common or positive power bus. The output device is connected between the negative power bus and the module output. The output characteristics are compatible with a wide range of user-supplied load devices, such as: motor starters, solenoids, and indicators. Power to operate the field devices must be supplied by the user.

Table 5-20. Specifications For 5/12/24 Volt DC Negative Logic - 1 Amp Output Circuit

Rated Voltage:	5/12/24VDC
Output Voltage Range:	5 to 30 VDC
Outputs per Module:	1
Fusing:	2 amp soldered-in fuse
Isolation:	1500 volts RMS between field side and logic side; 1500 volts RMS to other groups
Output Current:	1 amp maximum per point at 12 to 24 VDC .5 amp maximum at 5 to 11 VDC
Output Characteristics:	
Output Voltage Drop	0.4 volts maximum at 12 to 24 VDC, 1 amp, 25°C (77°F) 0.8 volts maximum at 5 to 11 VDC, 1 amp, 25°C (77°F)
Off-state Leakage	250 µA maximum
On Response Time	.1 ms maximum
Off Response Time	.1 ms maximum
Operating Temperature:	0° to 60°C (32° to 140°F)

It is recommended that this output be externally fused since the internal fuse is not easily replaceable (soldered into board).

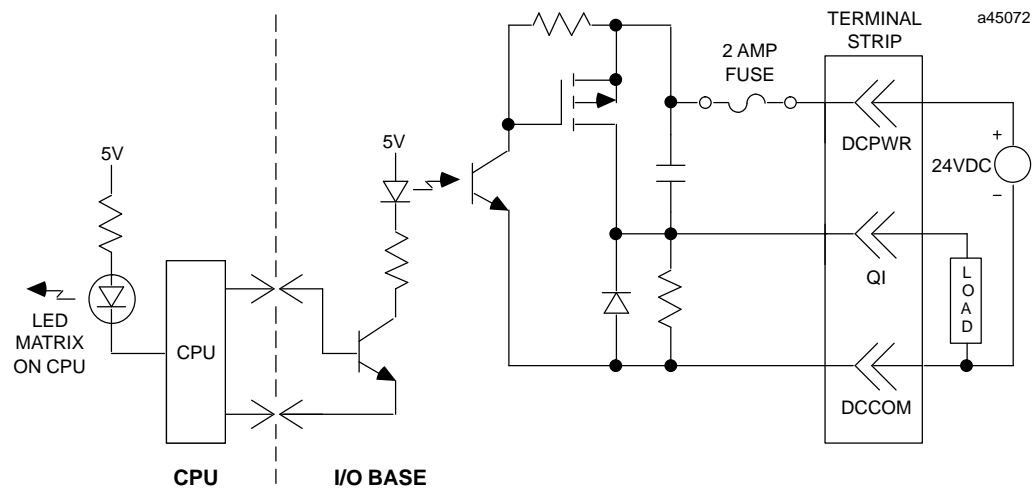


Figure 5-22. Typical 5/12/24 VDC Positive Logic Output Circuit

Caution

Connecting the power supply to DCPWR (+) and DCCOM (-) with polarity reversed will cause an internal fuse to blow.

240 VAC Power Supply

Specifications for the 240 VAC power supply requirements for this module are as listed below.

Table 5-21. Specifications for 240 VAC Power Supply

Range:	204 volts AC to 264 volts AC
Frequency:	47 to 63 Hz
Input Current:	130mA, maximum
Hold-up:	10 ms, minimum
In-rush Current:	1.3 amps maximum

24 VDC Output Power Supply On DC Input Modules

An isolated 24 VDC output power supply is available on the I/O Power Supply Base modules that have DC input circuits. This supply is available for user devices and is typically used to power DC input circuits at about 7.3 mA per input. The combination of input circuit current and external device current must not exceed 220 mA.

Table 5-22. Specifications for 24 VDC Power Supply

Voltage:	24 VDC, " 10%
Current:	220mA maximum
Fusing:	.25 amp fast blow (removable)

Field Wiring Information

The following figure provides wiring information for connecting user supplied input and output devices and power sources for the DC Input/Relay Output Base Module with 240 VAC power supply.

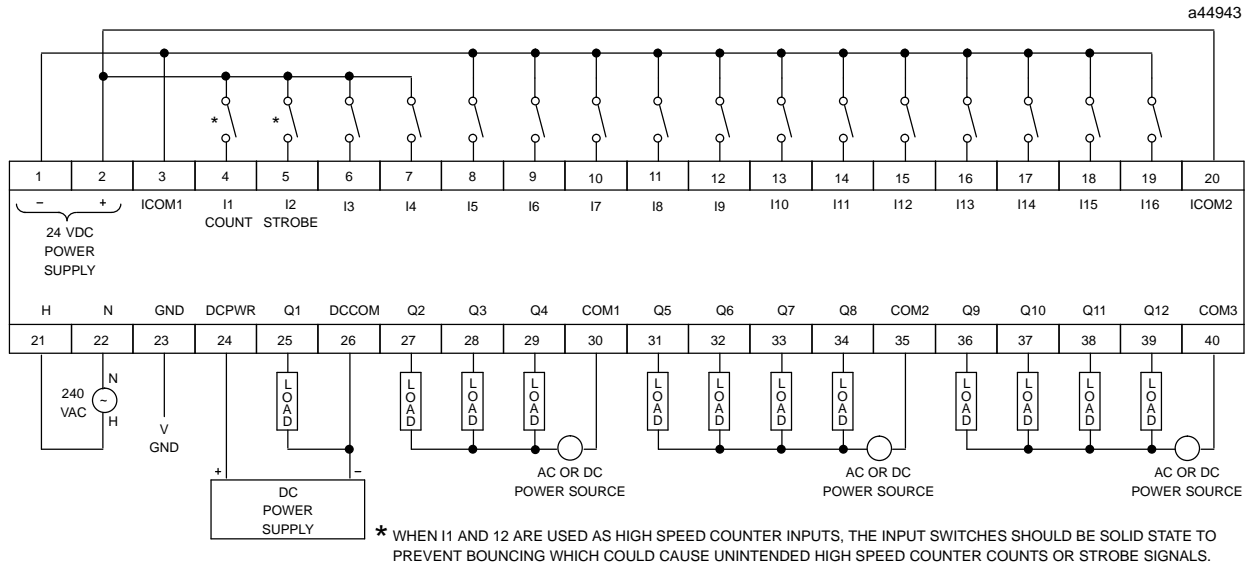


Figure 5-23. Field Wiring Information - IC692MDR741

Note

Although I1 through I4 are shown connected as Positive Logic and I5 through I16 are shown connected as Negative Logic, all inputs can be connected as either positive or negative logic.

Derating Information

The following figure provides derating information for this module.

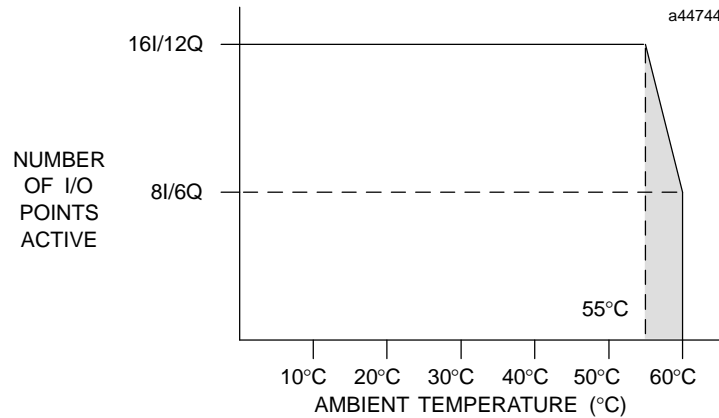


Figure 5-24. Temperature vs. Input/Output Points for IC692MDR741

Notes

- (1) The Hand-Held Programmer or RS-422/RS-485 to RS-232 Converter should not be connected continuously in the shaded region (above 55°C (131°F)).
- (2) For non-standard orientation, derate the temperature in the above figure by 10°C.

Replacing Fuses

The Series 90-20 PLC provides replaceable fuses for output points and for a user accessible 24 volt power supply (on the dc input versions of baseplate only). To replace these fuses:

- Remove power from the unit.
- Remove the CPU module.
- Confirm that power is removed from the I/O Power Supply baseplate. Use a small screw driver or fuse extractor to remove the fuse from the clips on the I/O circuit board.
- The 24 volt power supply fuse is located near the input terminal board. The output fuses are located near the output terminal board.
- The output fuses are located on the common of several circuits; therefore, if a fuse is blown it will prevent the entire group associated with it from working.

The following figure shows the location of the fuses on the I/O Base module, and the table following the figure provides a list of these fuses with location, rating, part number, third party source, and the type of fuse.

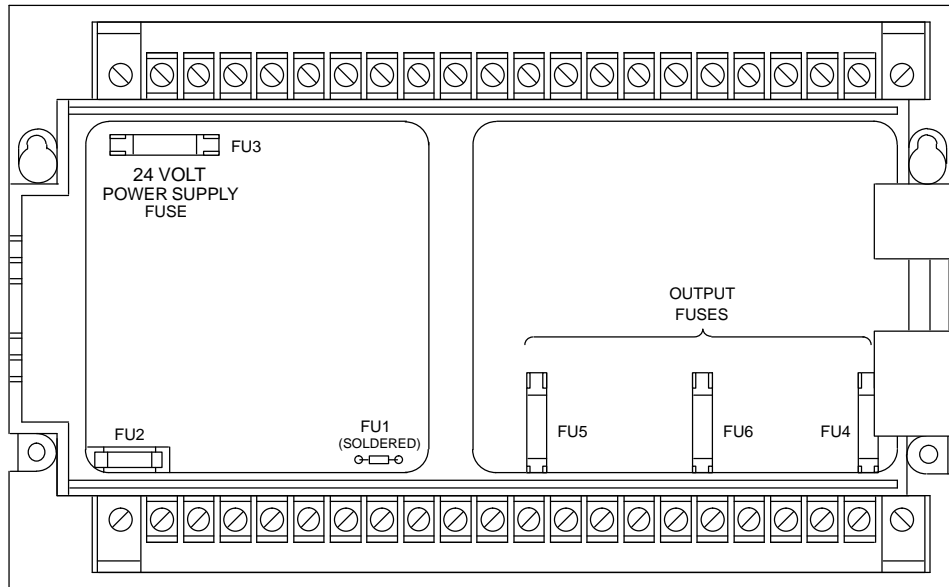


Figure 5-25. Location of Fuses

Warning

Failure to remove power when changing fuses could cause severe or fatal injury to the operator or maintenance personnel.

Table 5-23. List of Fuses

Location	Current Rating	GE Fanuc Fuse Part Number	Third Party Source and Part Number	Fuse Type
FU1	3A	259A9598P10	Littlefuse,239002	Soldered-in
FU2	250mA	44A724627-120	Bussman,GMD0.25 Littlefuse,239.250	Replaceable
FU3	250mA	259A9578P6	Bussman,AGC-1/4 Littlefuse,312.250	Replaceable
FU4, FU5, FU6	10A	259A9578P19	Bussman,AGC-10 Littlefuse,312010	Replaceable

Chapter 6

Series 90-20 High Speed Counter

The Series 90-20 PLC hardware configuration includes a built-in High Speed Counter function. This chapter describes:

- Overview and uses of the High Speed Counter
- Basic High Speed Counter features
- High Speed Counter inputs and outputs
- Configurable features on the High Speed Counter

Overview and Uses of the High Speed Counter

The High Speed Counter function provides direct processing of rapid pulse signals up to 10 kHz for industrial control applications such as:

- Turbine flowmeter
- Meter proving
- Velocity measurement
- Material handling
- Motion control
- Process control

Direct processing means that the High Speed Counter is able to sense inputs, process the input count information, and control one output independently of the user ladder program.

The High Speed Counter uses 16 words of input memory. This consists of 16 bits of discrete input memory (%I) and 15 words of analog input memory (%AI). These inputs are updated once per CPU sweep. The High Speed Counter also uses 16 bits of discrete output memory (%Q) which are transferred once per sweep.

The High Speed Counter is configured using the Series 90-30/20/Micro Hand-Held Programmer or the Logicmaster 90-30/20/Micro programming software Configurator function. Many features can be configured from the user's application program as well. Each feature is set to a factory default configuration which is suitable for many applications. There are no jumpers or DIP switches to set. LEDs in the CPU LED matrix indicate the status of the High Speed Counter inputs and outputs.

The Series 90-20 High Speed Counter is a Type A counter which has one 16-bit counter. This counter can be programmed to count either up or down. The counter accepts two inputs:

- Count input which increments or decrements a 16 bit accumulator.
- Preload/Strobe input which either preloads a user defined value into the accumulator or strobes the accumulator into a register. In addition, the counter has one dc output (Q1), with programmable on and off output presets.

Additional High Speed Counter Features

- Two DC inputs with input voltage range of 12 to 30 VDC;
- One DC output;
- Counts per timebase register;
- Software configuration;
- Individual LEDs that provide a visual indication of Count input, preload/strobe input and output status.

One input is used as a count signal. The other input is used as a strobe or preload inputs depending on user configuration. The dc output can be used to drive indicating lights, solenoids, relays, and other devices.

Power sources for input and output points must be supplied by the user or by the +24 VDC Isolated output on the Series 90-20 I/O power supply base module.

A Counts per Timebase register indicates the number of counts in a given time interval. The Counts per Timebase data is a 16-bit signed number. The sign indicates up counts (+) or down counts (-). The Timebase value is specified in milliseconds and ranges from 10 to 65535 milliseconds (increments of 10 milliseconds).

All configuration parameters are stored in non-volatile memory in the PLC. An initial (default) set of configuration parameters is used in the absence of user changes to configuration.

Basic Features

Direct processing: The module is able to sense inputs, count and directly control one output without the need to communicate with a CPU.

Selectable counter operation: Counter operation may be configured to count either up or down.

Continuous or single-shot counting: The counter can be configured to operate in either continuous or single-shot mode:

Continuous Counter Mode: If either the upper or lower count limit is exceeded, the counter wraps around to the other limit and continues.

Single-Shot Counter Mode: The counter counts to either limit and stops. When the counter is at the limit, counts in the opposite direction back it off the limit. The Accumulator can also be changed by loading a new value from the CPU or by applying a Preset Input.

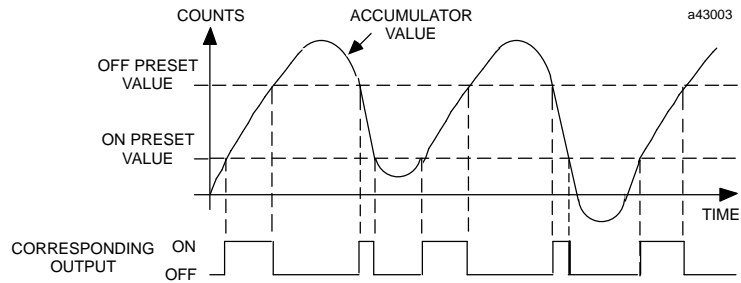
Access to Accumulator: An internal memory location stores the accumulated count. The CPU can read the value in the accumulator, or set it from the application program. The accumulator value, which may be either positive or negative, is represented as a two's complement number.

Accumulator adjust: The Accumulator may be adjusted. The adjustment is an 8-bit signed two's complement offset value that is sent from the CPU whenever an adjustment is required.

Selectable Input Filters: The Count and Preload/Strobe inputs can be configured for a high-frequency filter or a low-frequency filter.

Count Rate: Maximum count rate is 10 kHz with the high-frequency filter.

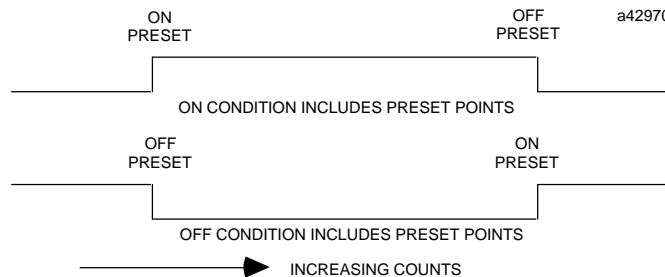
Selectable On/Off Output Presets: The High Speed Counter output has two Preset points, ON and OFF. The output state indicates when the counter accumulator value lies between the defined points. For example:



The output polarity may be configured to be either on or off between points by the relative location of the ON/OFF presets as shown below.

Table 6-1. Output Polarity Configuration

Preset Closest to Low Limit	Output ON	Output OFF
ON	> = ON Preset < = OFF Preset	> OFF Preset < ON Preset
OFF	< OFF Preset > ON Preset	< = ON Preset > = OFF Preset



Counts per Timebase can be used to measure the rate of counting: The high speed counter stores the number of counts that have occurred in a specified period of time. A timebase value from 10 millisecond to 65535 milliseconds is configurable (increments of 10 ms)

Inputs: Inputs include a count input and preload/strobe input. Input filters can be configured for high frequency or low frequency operation.

Count Input: A rising current edge on a count input will increment or decrement the Accumulator depending on the configuration. For each count, the CT LED will remain on long enough to be seen. If the counts occur very frequently the LED will appear to be ON continuously.

Preload/Strobe Input (Preload mode): The High Speed Counter has a user adjustable preload register. The contents of this register determine the value the counter resets to when the Preload input goes active. The default value of the Preload register is zero.

The Preload input is positive-edge sensitive. It may be configured to have either the high-frequency or low-frequency filter. The default is high-frequency.

If Preload occurs during counting, preload data with a resolution of $C - 1$ count is stored in the accumulator and a Preload flag is set to indicate to the CPU that a Preload occurred. In addition, the preload/strobe LED (marked PL) will turn on as long as the accumulator equals the preload value.

Preload/Strobe Input (Strobe mode): This input is positive edge-sensitive. The high-frequency filter is enabled in the strobe mode.

When the strobe signal goes active, count data with a resolution of one count is stored in the Strobe register and a Strobe flag is set to indicate to the CPU that a strobe value was captured. This value remains in the Strobe register until the Strobe signal goes active again, at which time it is overwritten. Each time the CPU acknowledges receipt of the Strobe flag, the application program should clear it.

High Speed Counter Output (Q1): The High Speed Counter dc output can be used to drive indicating lights, solenoids, relays, and other devices. The output is also capable of driving CMOS level loads. The output is a dc output, with power supplied from a user supplied power source. It is protected against short circuits by an internal fuse (soldered on board).

The output can be programmed to turn on or off when the accumulated count reaches appropriate values.

Configuring the High Speed Counter

There are two schemes that may be followed for configuration of the High Speed Counter:

- The Hand-Held Programmer may be used for on-line configuration.
- Off-Line configuration can be accomplished using the Logicmaster 90-30/90-20 configurator software.

When the High Speed Counter module is enabled, configuration data entered by the user, in response to the Hand-Held Programmer screens, is stored in the configuration memory area of the PLC. When the configuration is complete, the PLC stores this configuration data.

The %I, %AI, and %Q reference locations are fixed in the Series 90-20 PLC.

Circuit Specifications

Refer to Chapter 5 for electrical and circuit specifications for the High Speed Counter (tables 5-4 and 5-11).

Field Wiring

It is recommended that the following procedures be followed when routing and connecting field wiring from user devices to the High Speed Counter inputs and outputs.

- Turn-off power to the PLC before connecting field wiring.
- All low level signal wires should be run separately from other field wiring.
- AC power wiring should be run separately from DC field wiring.

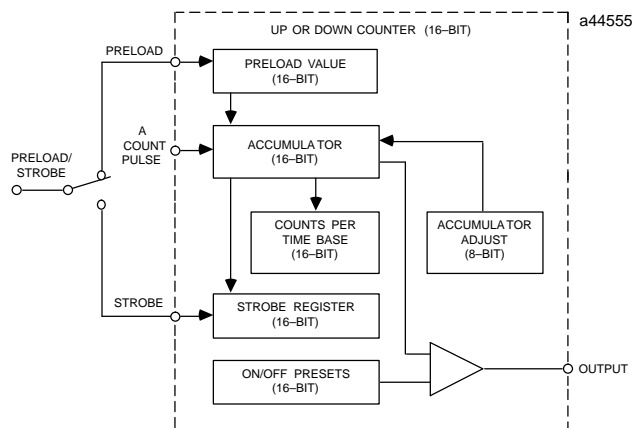
Warning

You should calculate the maximum current for each wire and observe proper wiring practices. Failure to do so may cause injury to personnel or damage to equipment.

- Field wiring should not be routed close to any device that could be a potential source of electrical interference.
- If severe noise problems are present, additional power supply filtering or an isolation transformer may be required.
- Ensure that proper grounding procedures, as previously described, are followed to minimize potential safety hazards to personnel.
- Label all wires to and from I/O devices. Record circuit identification numbers or other pertinent data on the inserts which go in the module's faceplate door.

Operation of a Type A Counter

The High Speed Counter may be independently configured to count either up or down. Details of the Type A counter are shown in the figure below. The counter has an Accumulator register, Counts per Timebase register, one Strobe register and one set of on/off Preset values. The counter has two inputs: Preload/Strobe and Count Pulse, and one output.



Since the Preload/Strobe input in the preload mode is normally used to perform the reset function for each counter, the Preload default value has been set to 0 (zero). However, the Preload value may be configured to any value within the counter's selected range. The Preload for each counter is positive current edge-sensitive. When a preload input occurs, the configured preload value is inserted into the Accumulator and a Preload flag is set to indicate this to the CPU. If the application program uses this flag indication, then it should clear the flag before the next preload occurs. A rising edge on the Preload input always preloads the Accumulator regardless of the state of the Preload flag.

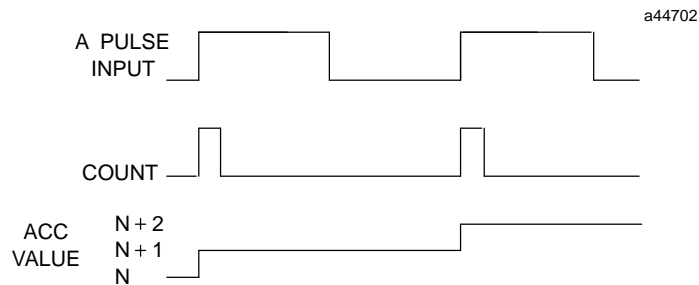
The Preload/Strobe input in the strobe mode is positive current edge-sensitive. When the Strobe signal goes active, the current value in the accumulator is stored in the associated Strobe register and a Strobe flag is set to indicate to the CPU that a strobe value was captured. This value remains in the Strobe register until the Strobe signal goes active again and is overwritten. Each time the CPU acknowledges receipt of the Strobe flag, it will stay on until the application program clears it. The Strobe input always updates the Strobe register with the latest Accumulator value regardless of the state of the Strobe flag.

The Preload/Strobe input in the strobe mode always uses the high-frequency filter. Preload inputs and Count inputs can be configured to use either the high-frequency filter, or a low-frequency filter. The low-frequency filter reduces the effect of signal noise.

The value in the Accumulator may be adjusted by writing an offset adjustment value to the Accumulator. This adjustment may be any value between -128 and +127. The adjustment value is added to the contents of the accumulator.

Timing For Type A Counter

The Count signal shown in the following illustrations represents an internal signal that indicates where counting occurs with respect to the pulse input. Counting always occurs on the low-to-high current transition of the Pulse input.



CPU Interface:

Data Transfer Between High Speed Counter and CPU

During each I/O scan, the High Speed Counter automatically sends 16 status bits (%I) and 15 words (%AI) of register data values to the CPU. In return, during each I/O scan, the CPU sends 16 bits (%Q) of output data to the module. COMMREQ function blocks in the user program can be used to send additional data commands to the module. Information about configuration, programming, and monitoring High Speed Counter operation using a Hand-Held Programmer can be found later in this chapter under *Configuration of the High Speed Counter*, page 6-19.

Data Automatically Sent By The High Speed Counter

The 15 register data words (%AI) represent:

- Latest Counts per Timebase value;
- Contents of the Accumulator(s);
- Contents of the Strobe registers;
- Error code.

The 16 status bits (%I) represent:

- Strobe flag status;
- Preload flag status;
- Disable status;
- Output status;
- Module ready status;
- Error status.

These status bits are sent to the CPU as inputs, and can influence outputs sent from the CPU to the module. Data formats for the High Speed Counter are shown on the following pages.

Data Automatically Sent To The High Speed Counter

The 16 output bits (%Q) represent:

- Strobe flag reset;
- Preload flag reset;
- Clear error flag;
- Output enable.

All of this data is transferred from the High Speed Counter to the CPU once per I/O scan. The I/O scan is active while the CPU is in the RUN mode or STOP ENABLED mode.

Additional Data Sent To The High Speed Counter Using a COMMREQ Function Block

- Load accumulator;
- Load count limits;
- Load accumulator increment;
- Load output presets;
- Load accumulator preload;
- Load time base;
- Count direction;
- Fail mode;
- Filter selection;
- Preload/Strobeselection;
- Enable/Disablecounter;
- Continuous/Singleshotcounting.

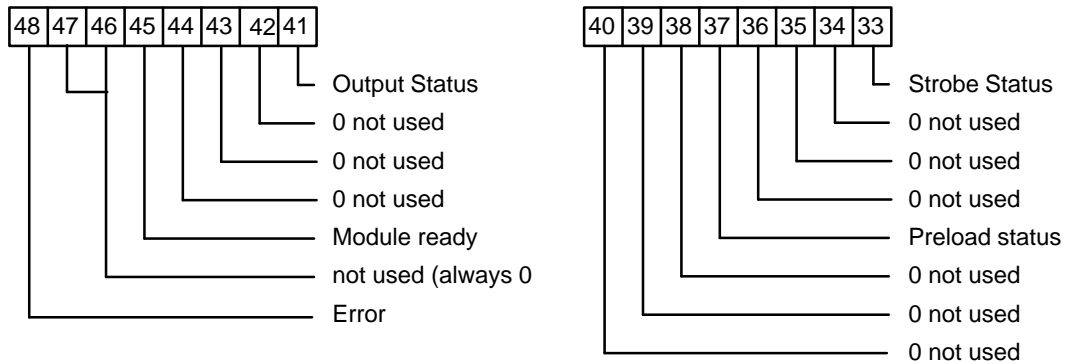
%AI And %I Data Sent

The 15 words (%AI) of Analog Input data values and 16 status bits (%I) sent to the CPU are described below.

Table 6-2. Description of %AI Data

Reference	Description	Value
%AI01	ModuleStatus Code	
%AI02	Counts per timebase	0 to 32767
%AI03-%AI05	Not used but occupied	0
%AI06	Accumulator	-32768 to 32767
%AI07	StrobeRegister	-32768 to 32767
%AI08-%AI15	Not used but occupied	0

Status Bits (%I)



Strobe/Preload Status: The High Speed Counter sets these bits when a strobe or preload occurs. The CPU must clear the bit using the corresponding Reset Strobe/ResetPreload output.

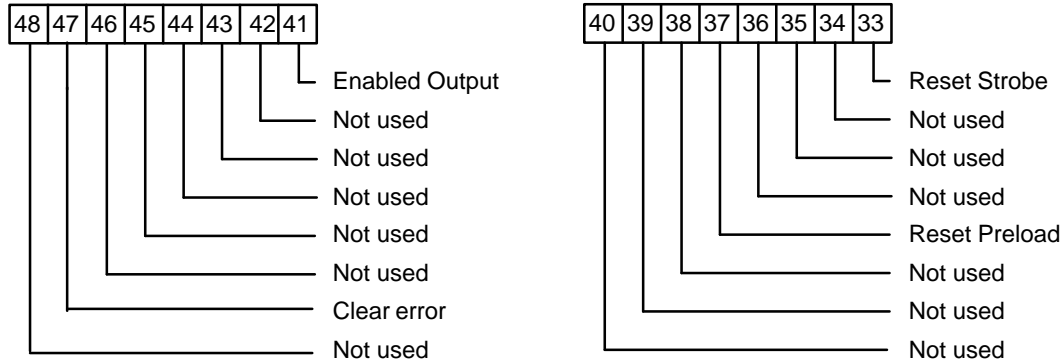
Output Status: The High Speed Counter uses this bit to indicate the ON or OFF commanded status of the output.

Module Ready: Always a 1 on the Series 90-20. The 90-30 rack mounted high speed counter module sets this bit to 1 after successfully completing its powerup tests.

Error: Set to indicate an error condition. When this occurs, the error code is returned in the High Speed Counter Status code (word 1). When the error is acknowledged by the CPU, it should be cleared by sending the Clear Error output.

%Q Data Sent From CPU To High Speed Counter

Once each I/O scan, the CPU sends 16 bits (%Q) of data to the High Speed Counter. The application program can use these %Q references to send commands to the High Speed Counter. The %Q data format for the counter is shown below.



Reset Strobe: Clears the High Speed Counter’s corresponding Strobe input status bit (as described on the previous pages). For example, Reset Strobe bit 1 is used to reset the module’s Strobe status bit 1. If the corresponding Strobe input status changes to 1, the program logic should set this bit to 1 and then back to 0 on the next I/O scan.

Reset Preload: Clears the High Speed Counter’s corresponding Preload input status bit. For example, reset Preload bit 5 is used to reset the module’s Preload status bit 5. If the corresponding Preload input status changes to 1, the program logic should set this bit to 1 and then back to 0 on the next I/O scan.

Outputs Enable/Disable: Bit 8 is used to enable or disable the module’s outputs. If this bit is 0, the High Speed Counter output will not turn on.

Clear Error: Set by the CPU to clear error after it has been acknowledged.

High Speed Counter Status Codes

The High Speed Counter Status Code in the %AI Input Data contains the error codes returned to the PLC. These codes are set as a result of message or configuration command errors. To clear this code, the clear error bit in the discrete outputs (%Q) should be set.

Table 6-3. Error Codes Returned

Code	Description	Code	Description
0	No Errors	6	Invalid Counter Number
1	Unused	7-9	Reserved
2	Unused	10	Reserved
3	Invalid Command	11	Counter Limit Error
4	Invalid Parameter	12-14	Reserved
5	Invalid Sub-Command		

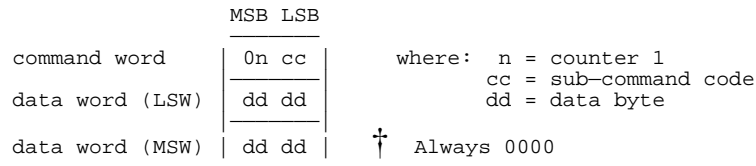
Error codes are defined as follows:

- InvalidCommand: Command number received was invalid for the High Speed Counter.
- InvalidParameter: Configuration parameter received was invalid.
- InvalidSub-Command: Sub-Command code in the Data Command Word was invalid.
- Invalid Counter Number: Counter number in the Data Command Word was not 1.
- Counter_LimitError: Counter configuration limit was rejected because new limit set would be incompatible (High limit < > Low limit).

Sending Data Commands To The High Speed Counter

In addition to the %Q discrete output data which is sent every sweep to the High Speed Counter, there are a series of commands which can be sent by the PLC (using the COMMREQ function block) to change the various operating parameters of the counters. These commands are all 6 bytes in length.

The format for Data Commands is as follows:



The following table lists the Data Command words in both decimal and hexadecimal numbers. Following the table is a description of each command and a simple example.

Data Commands for the High Speed Counter

Table 6-4. Data Commands

Command Name	<i>Command Word</i>	
	Decimal	Hexadecimal
Load Accumulator	01 01	01 01
Load HiLimit	01 02	01 02
Load LoLimit	01 03	01 03
Load Acc Increment	01 04	01 04
Set Cntr Direction	01 05	01 05
Load Timebase	01 06	01 06
Load ON Preset	01 11	01 0B
Load OFF Preset	01 21	01 15
Load Preload	01 31	01 1F

Load Accumulator

Command Code = 01H

Used to set any value within counter limits directly into the Accumulator.
Example: To set Counter to 1234H, load COMMREQ command registers with:

Command word: 0101

LS data word: 1234

MS data word: 0000

- Load Hi Limit** Command Code = 02H
- Load Lo Limit** Command Code = 03H
Used to set the Hi and Lo limits to any value within the counter range.
Example: To change the upper limit of counter to 10000 (2710H), load registers with:
Command word: 0102
LS data word: 2710
MS data word: 0000
Note that if the limits are loaded in the wrong order, they may be rejected and an error flag will be set. To avoid this, remember to always move the Lo Limit first when shifting the limits down or the Hi Limit first when shifting the limits up.
- Load Acc Increment** Command Code = 04H
Used to offset a counter accumulator by a small number of counts (up to +127 or -128). Only the least significant byte of data is used with this command.
Example: To offset counter by -7 counts, load:
Command word: 0104
LS data word: 00F9
MS data word: 0000
This may be done at any time, even while the counter is counting at maximum rate. If the offset causes the counter to exceed its limits, the excess will be treated just like any other overflow, i.e., if the Continuous mode is selected, the counter will wraparound through the other limit, or if the Single-Shot mode is selected the counter will stop at the limit.
- Set Cntr Direction** Command Code = 05H
Used to change the count direction (up or down). Only the LSB of the first data word is used for this command (00 = up, 01 = down).
Example: To set the direction of counter to down, load:
Command word: 0105
LS data word: 0001
MS data word: 0000
- Load Timebase** Command Code = 06H
Used to change the time interval referenced by the counter when computing its counts/timebase register data.
Example: To change the timebase for counter to 600 ms (258H), load:
Command word: 0106
LS data word: 0258
MS data word: 0000
Note that the maximum range of the counts/timebase (CTB) register is +32767 and -32768 counts. The length of the timebase and the maximum count frequency should be co-ordinated so that these limits are not exceeded. The indication will roll over from (+) to (-) or (-) to (+) if exceeded.

- | | |
|------------------------|--|
| Load ON Preset | Command Code = 0BH |
| Load OFF Preset | Command Code = 15H
Used to set up the output turn on/off points within the counter range.
There is one output associated with the counter.
Example: To set counter output to turn on at 5000 (1388H counts, load:
Command Code: 010B
LS data word: 1388
MS data word: 0000
and off at 12000 (2EE0H) counts, load:
Command Code: 0115
LS data word: 2EE0
MS data word: 0000 |
| Load Preload | Command Code = 1FH
Used to change the count value that will be loaded into the counter accumulator when the preload input is activated.
Example: Make counter start at 2500 (09C4H) counts at its preload signal, load:
Command word: 011F
LS data word: 09C4
MS data word: 0000 |

Sending Data With the COMMREQ Function

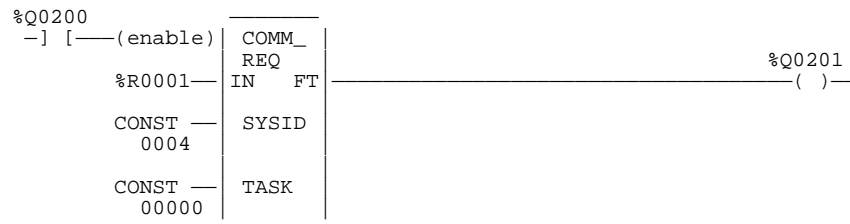
The PLC ladder program sends the Data Commands using the COMMREQ (Communication Request) function. The COMMREQ requires that all its command data be placed in the correct order in the CPU memory before it is executed. It should then be executed by a one-shot to prevent sending the data to the High Speed Counter multiple times. A description of the COMMREQ function and its command block data follows along with a ladder example which uses registers %R0001 to %R0014 for the COMMREQ command block & status register.

COMMREQ Function Block Description

The Communications Request (COMMREQ) function is a conditionally executed function that communicates a particular request, through the ladder logic program, to the High Speed Counter.

Communications Request Function Block Format

The ladder logic representation of the COMMREQ is as follows:



The Communications Request function block has four inputs and one output. The first input is an enable input. Generally a one-shot coil is used to enable the COMMREQ

function. This prevents multiple messages from being sent. The second input (IN) is the starting location of the COMMREQ command block. The SYSID input is used to indicate which rack and slot to send the message to (physical location of High Speed Counter module). The SYSID is always 0004 for the Series 90-20 PLC.

In the above example, the SYSID (0004 (in Hexadecimal)) points to rack 0, slot 4 and the COMMREQ command block starts at Register 0001. The last input (TASK) is ignored during High Speed Counter communications and should be set to zero.

Command Block

The command block for DATA Commands is composed of 10 words of information arranged in the following fashion: (all values in hexadecimal unless otherwise indicated). Use the Block Move (BLKMV) command to move these values to the Register tables (refer to the *Logicmaster 90-30/20/Micro Programmable Controllers Reference Manual*, GFK-0467, for information on using the Block Move function).

Table 6-5. Description of Command Block for DATA Commands

Location	Data	Description
%R0001	0004	Always 0004 for this High Speed Counter application
%R0002	0000	Not used (Always zero)
%R0003	0008	COMMREQ status data type (8 = registers), see Table below for data type codes.
%R0004	000D	COMMREQ status location -1 (%R0014)
%R0005	0000	Not used
%R0006	0000	Not used
%R0007	E201	Command type (E2 - specific message to High Speed Counter) and Command Parameter (1 = write)
%R0008	0006	Byte length of data to High Speed Counter
%R0009	0008	Data type (8 = registers), see Table below for data type codes.
%R0010	000A	Start location of data -1 (%R0011)
%R0011	nnnn	Command word (Table 6-2)
%R0012	nnnn	LS data word
%R0013	nnnn	MS data word

Table 6-6. COMMREQ Data Type Codes

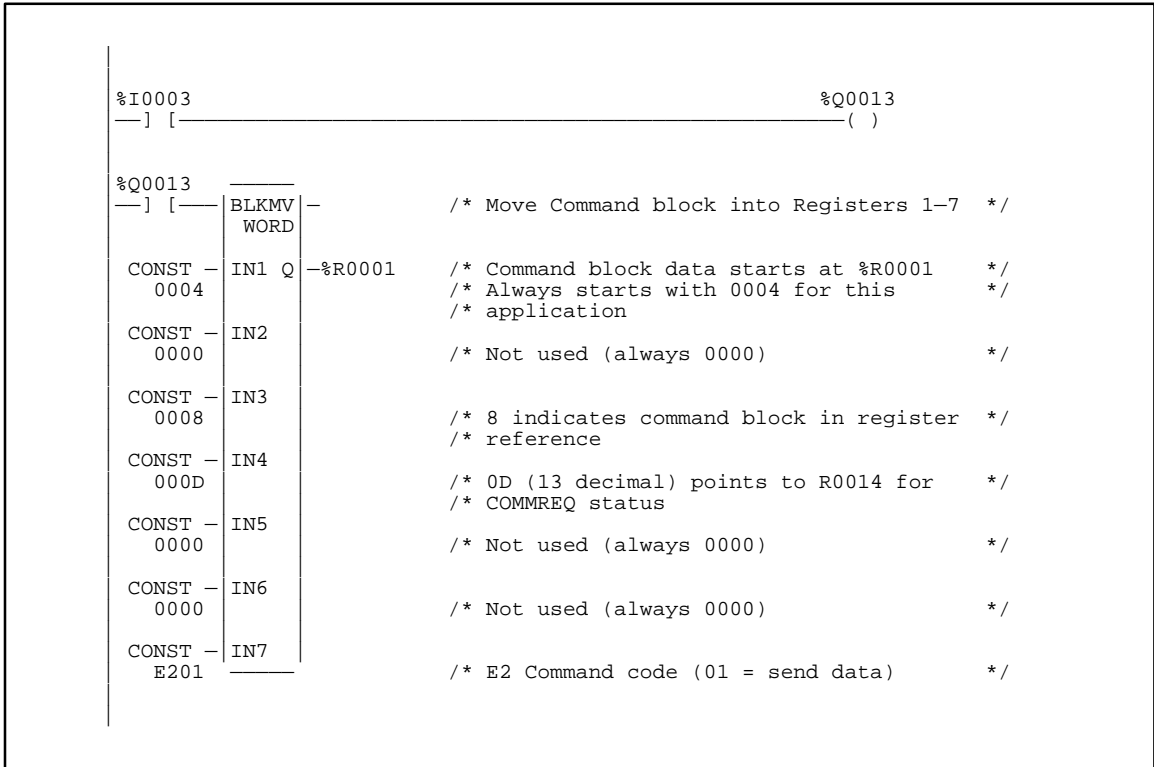
For This Data Type		Enter This Number	
		Decimal	Hexadecimal
%I	Discrete Input	28	1C
%Q	Discrete Output	30	1E
%R	Register	8	08
%AI	Analog Input	10	0A
%AQ	Analog Output	12	0C

Example of Sending Data Commands

An example of ladder logic for sending data commands to the High Speed Counter via COMMREQ function blocks is shown below. In this example, the COMMREQ command block is located in registers %R0001 through %R0013 and the COMMREQ status is returned in %R0014. The command to send the data is initiated by the conditional input %I3 which sets output %Q013 for one sweep.

Note that register reference pointers in the COMMREQ command block are one less than the register number pointed to, e.g. 000D (13) indicates R0014 as the COMMREQ status register.

Note that the comments within / . . . */ in the ladder logic example have been included for information purposes only. They are not generated by the Logicmaste 90-30/20/Micro programming software.*



```

%Q0013
---] [--- BLKMV --- /* Move data into registers 8 through 14 */
          WORD
CONST - IN1 Q -%R0008 /* Length of data sent by COMMREQ to HSC */
0006 /* (Always 6 for Data Commands) */
CONST - IN2 /* Data type (8 = registers) */
0008 /*
CONST - IN3 /* Start location of data (R0011) */
000A /*
CONST - IN4 /* First word (Command Word) */
0101 /* In this case, load Accumulator 1 */
CONST - IN5 /* LSW of data. This and next word will */
2211 /* load the Accumulator with 44332211H. */
CONST - IN6 /* MSW of data. */
0000 /*
CONST - IN7 /* Zero the COMMREQ status word (R0014) */
0000 /* before the COMMREQ is called. */

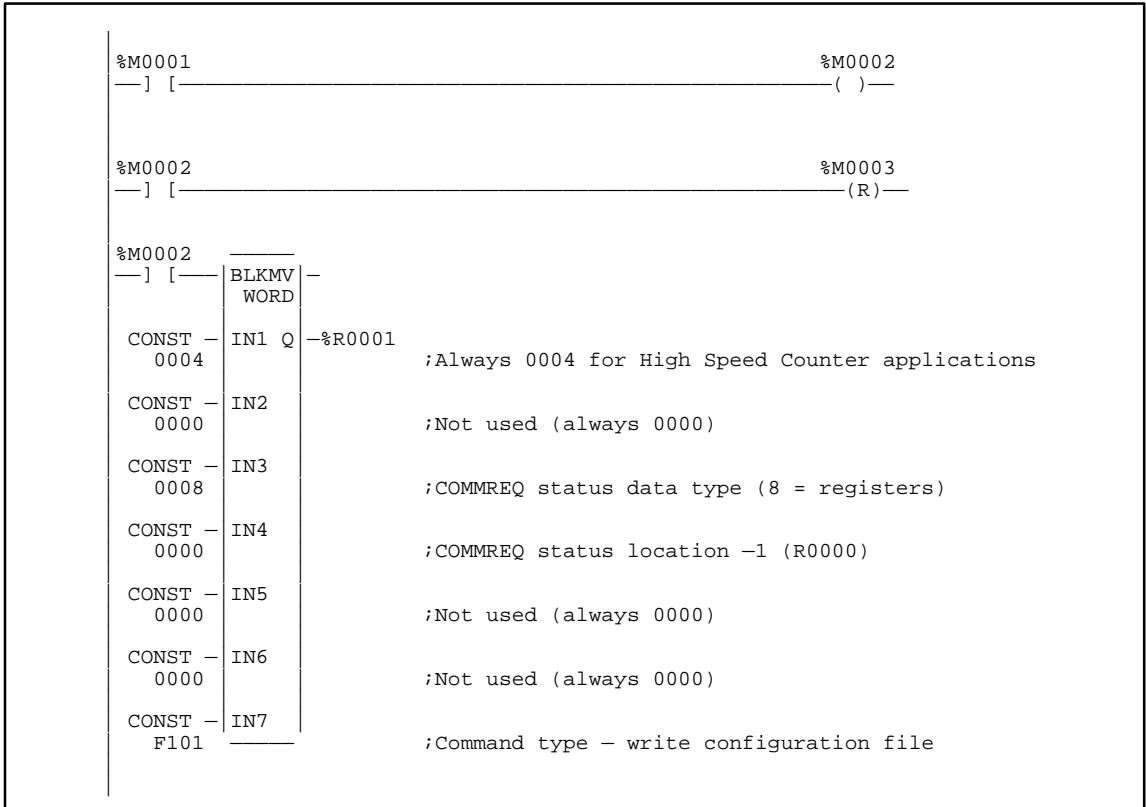
/* Now call the COMMREQ to send the message */

%Q0013
---] [--- COMM --- %T0051
          REQ --- ( )---
          %R0001 - IN FT + /* COMMREQ will set output %T0051 if failure */
          /* detected when sending message. */
          CONST - SYSID /* Command block data starts in R0001 */
0004 /* High Speed Counter is in rack 0, slot 4 */
CONST - TASK /* Input not used (always 0) */
00000000

```

Example of Changing Configuration with COMMREQ

The High Speed Counter configuration can be changed by using the write configuration command F1. An example of the ladder logic for this is shown below.



```
%M0002
---] [--- BLKMV ---
          WORD ---

CONST -- IN1 Q --%R0009
000B    ;Byte length of data to High Speed Counter

CONST -- IN2
0008    ;Data type (8 = registers)

CONST -- IN3
0014    ;Start location of data -1 (R0021)

CONST -- IN4
0000    ;not used

CONST -- IN5
0000    ;not used

CONST -- IN6
0000    ;not used

CONST -- IN7
0000    ;not used

%M0002
---] [--- BLKMV ---
          WORD ---

CONST -- IN1 Q --%R0021
0000    ;HSC Config Bytes 1/2 (see Byte Definition below)
          (fail mode = normal, count input filter = high
          preload filter = high)
          ;HSC Config Bytes 3 and 4 (not used)

CONST -- IN2
0000    ;HSC Config Bytes 3 and 4 (not used)

CONST -- IN3
008A    ;HSC Config Bytes 5/6 (see Byte Definition below)
          (strobe enabled, count mode = 1 shot, count
          direction = down)

CONST -- IN4
0000    ;HSC Config Bytes 7 and 8 (not used)

CONST -- IN5
0000    ;HSC Config Bytes 9 and 10 not used

CONST -- IN6
0000    ;HSC Config Byte 11 (not used)

CONST -- IN7
0000    ;not used
```


Configuration of the High Speed Counter

This section describes the configurable features of the High Speed Counter, which are:

- Preload/Strobe and Count input filters
- Count direction
- Continuous or Single-Shot counting
- Timebase for measuring count rate
- Upper and lower count limits
- On and off presets for outputs
- Preload counter value
- Output Fail Mode

Configurable Features

The following table summarizes the configuration features, selections, and default configuration values.

Table 6-7. Configurable Features

Features	Selections	Default
Enable Counter	Enable/disable	disable
Preload Input filter	high/lowfrequency	highfrequency
Count input filter	high/lowfrequency	highfrequency
Count Up or Down	Up/down	up counter
Count mode	Continuous/single-shot	continuous
Counttimebase	10 - 65535mS	1000mS
Countlimits	-32768 to +32767	upper = +32767, lower = 0
Output Presetpositions	select ON and OFF positions	ON = +32767, OFF = 0
Preloadvalue	-32768 to +32767	0
Output Failmode	Normal, OFF, hold	Normal

Enable Counter

The Series 90-20 High Speed Counter is defaulted to the disabled mode.

Input Filters

By default, the count and preload/strobe inputs have a built-in high-frequency filter. This can be changed to a low-frequency filter for the count input and preload/strobe input when used in the preload mode. The high frequency filter is always selected in the strobe mode. The low-frequency filter reduces the effect of signal noise.

Counter Direction

The High Speed Counter provides one unidirectional counter. This counter can be configured to count either up or down. The default is Up.

Continuous or Single-Shot Counting

The High Speed Counter has programmable count limits that define its range. The counter can either count continuously within these limits, or count to either limit, then stop.

In the continuous counting mode, if either the upper or lower limit is exceeded, the counter wraps around to the other limit and continues counting. Continuous counting is the default mode.

If single-shot is selected, the counter will count to its upper or lower limit, then stop. When the counter is at the limit and the count direction is changed, new counts will count it back off the limit. The Accumulator can also be changed by loading a new value from the CPU or by applying a Preset Input.

Counter Timebase

The timebase represents a span of time which can be used to measure the rate of counting. For example, the program may be required to monitor the number of count pulses which are occurring every 30 seconds.

A timebase from 10 milliseconds to 65535 milliseconds can be selected for each counter. The counter timebase is set to 1 second (1000 milliseconds) by default. The module stores the number of counts that occurred during the last-completed timebase interval in the Counts/Timebase register. The range of the Counts/Timebase register is -32768 and +32767 counts. The timebase value selected should not allow the Counts/Timebase register to overflow at the maximum count frequency. If it does, the sign of the Counts/Timebase will change from (+) to (-) or (-) to (+).

Count Limits

Each counter can be assigned upper and lower count limits. All Accumulator preload values and output on/off preset values must lie within these limits. The upper (high) limit is the most positive, and the lower limit is the most negative. Both can be positive, or both can be negative, but the high limit is always greater than the low limit.

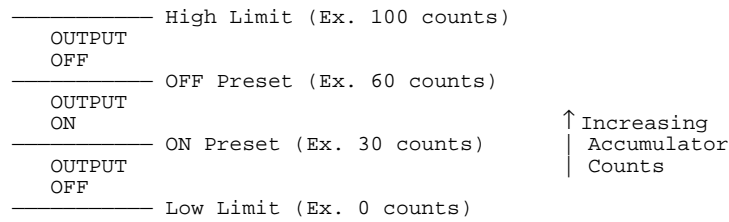
If the Accumulator value is outside the new limits when the limits are changed it is automatically adjusted to the low limit value. If the new limits are incompatible (high < low or low > high), then they will be rejected and the old limits retained. In this case a counter limit error code will be returned. To avoid this situation when the limits are changed one at a time, a good rule to follow is: always move the high limit first when shifting the limits up and always move the low limit first when shifting them down.

Output Preset Positions

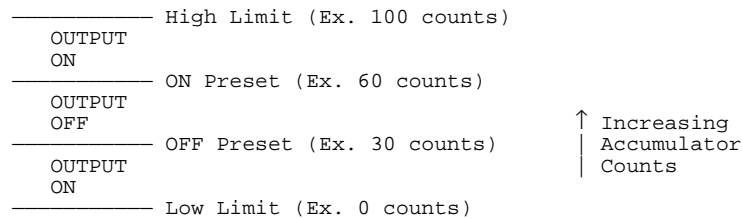
The counter output has a preset ON and OFF position. The output state indicates when the counter accumulator value is between the ON and OFF points.

Preset closest to low limit	Output ON	Output OFF
ON	> = ON Preset < = OFF Preset	> OFF Preset < ON Preset
OFF	< OFF Preset > ON Preset	< = ON Preset > = OFF Preset

The following illustration may be helpful to indicate the count regions where the High Speed Counter output is ON and OFF.



In the above example, the output is ON when the accumulator is between 30 and 60 and OFF when it is between 0 and 30 and 60 to 100.

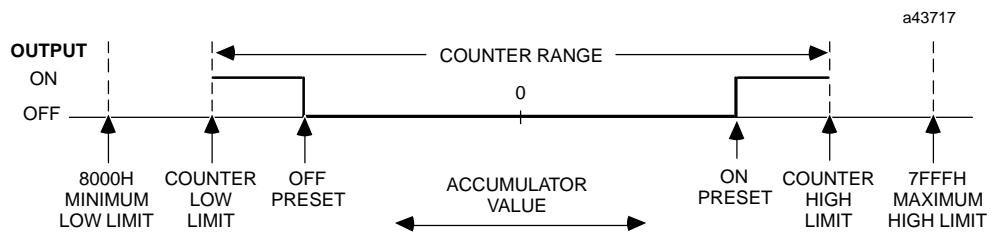


In the above example, the output is OFF when the accumulator is between 30 and 60 and ON when it is between 0 and 30 and 60 to 100.

Location of Preset Points

The Preset points may be located anywhere within the counter range. When the accumulator value is between the Preset points, the output ON/OFF state will always be that of the lowest (most negative) Preset point. When the accumulator value is NOT between the Preset points, the output ON/OFF state will be that of the most positive preset. This is true regardless of the counter direction.

The following example compares the output state and accumulator value of a 16 bit counter.



If both preset points are within the counter range, the output always switches at the Preset points. If only one of the Preset points is programmed within the counter range, then the counter limits will function as the other Preset point. In the continuous mode, the output will switch when wraparound occurs.

If neither of the Preset points is in the counter range then the output state will not change; it will always be the state of the most positive Preset. If both Preset points are equal and out of range, the output will always be OFF. If both Preset points are equal and within the counter range, then the output will only be on for one count value - as defined by the Preset points.

Separation of Preset Points

The count accumulators are compared to the Presets at 0.5 millisecond intervals. Therefore, to guarantee that the outputs will always switch, the Preset points must be separated by at least the number of counts received in a 0.5 millisecond time period. For example:

```
If maximum count rate = 10kHz;  
then minimum count separation = (10,000 Hz x .0005 sec) = 5 counts.
```

Preload Value

A starting count value can be specified which will be used when the Preload input is activated. If the counter should be reset to 0, enter 0 as the Preload value; this is the default value.

Preload values within the configured counter limits should always be used. When preload values outside the counter limits are used, a preload input will have the following effect:

1. A preload value greater than the counter high limit initially sets the Accumulator to the preload value. If down counts are being received every 0.5 milliseconds then the Accumulator is counted down from the preload value. Whenever a 0.5 millisecond period occurs during which no counts are received or up counts are received the Accumulator is immediately adjusted for overflow. The overflow adjustment depends on the counter mode selected (continuous or one-shot).
2. A preload value less than the counter low limit initially sets the Accumulator to the preload value. If no counts are currently being received the Accumulator stays at the preload value. If up counts are currently being received the Accumulator is counted up from the preload value. When down counts are received the Accumulator is immediately adjusted for underflow according to the selected counter mode (continuous or one-shot).

Output Fail Mode

If the CPU stops, the High Speed Counter output can respond in three different ways:

1. It can continue to operate normally, processing the inputs and controlling the output according to its configuration (NORMAL);
2. It can force the output to turn off (FRCOFF);
3. The module can hold the outputs at the current state (HOLD).

These responses remain in effect until the CPU returns to operation or the module is power-cycled.

Configuration Programming

This section defines the messages and actions required to configure the Series 90-20 High Speed Counter. If you want to use the High Speed Counter with a configuration different than the default configuration, you must change the configuration to suit your needs. Configuration of the High Speed Counter can be accomplished in three ways:

1. By using the Series 90-30 Hand-Held Programmer;
2. By using the configurator function in the Logicmaster 90-30/90-20 Software Programming package;
3. By sending data using the COMMREQ command in ladder logic programs.

Defaults

The Series 90-20 High Speed Counter defaults to a disabled mode. The user must enable the counter to allow operation.

Configuration With Hand-Held Programmer

Configuration can be done using the Hand-Held Programmer. After powering up the Series 90-20 PLC with the High Speed Counter enabled, enter the Configuration mode by pressing the MODE 4 and ENT keys in sequence (the CPU must be in STOP mode). Using the Down Arrow key, sequence to the slot 4. Press the READ key, then ENT. ENT is the Enter key and when pressed tells the system to invoke the operation specified by the keys pressed prior to ENT.

The following tables list all of the configuration parameters in the Series 90-20 High Speed Counter and the abbreviations for those parameters as they are displayed on the Hand-Held Programmer.

Table 6-8. Abbreviations for Configuration Parameters

Parameter Number	Abbreviation	Value 1	Value 2	Value 3	Description	Default
1	ENABLE	DISABLED	ENABLED	-	Enable High Speed Counter	DISABLED
2	CNTRTYPE	TYPEA	-	-	Counter Type (notconfigurable)	TYPEA
3	FAILMODE	NORMAL	FRCOFF	HOLD	Output Failure Mode	NORMAL
4	CNTFIL	HIGH	LOW	-	Count Input Filter	HIGH
5	PLD/STB	PLD	STB	-	Preload/StrobeSelect	PLD
6	PLD/STBFIL	HIGH	LOW	-	Preload/StrobeInputFilter	HIGH
7	CTR1 DIR	UP	DOWN	-	Counter Direction	UP
8	CTR1 MODE	CONT	1 SHOT	-	Counter Mode	CONT
9	TIMEBS	-	-	-	Time Base	1000mS
10	HILIM	-	-	-	High Limit	+32767
11	LO LIM	-	-	-	Low Limit	0
12	ON PST	-	-	-	ON Preset	+32767
13	OFF PST	-	-	-	OFF Preset	0
14	PRELD	-	-	-	Preload	0

PLC I/O Scanner Configuration

Before the PLC allows the High Speed Counter configuration screens to be viewed, it presents the following I/O Scanner Configuration screens. In the Series 90-20 the I/O configuration is fixed.

```
R0:04 HSC      <S
I16:I0033-I0048
```

On the first line of the screen display, R0 indicates the rack number, 04 is the slot number, and S indicates that the CPU is in STOP mode. On the second line, I16 shows that this module has 16 bits of discrete Input data (%I). This is the data transferred from the High Speed Counter to the PLC each sweep. %I33 indicates the start address for the 16 %I high speed counter bits. The rack and slot numbers are used only on Series 90-30 Model 311 and 331 systems if the same program is transferred to those PLCs.

```
R0:04 HSC      <S
Q16:Q0033-Q0048
```

%Q33 indicates the start address for 16 discrete control bits sent to the High Speed Counter each PLC sweep.

```
R0:04 HSC      <S
AI15:AI001-AI015
```

AI01 is the location of the 15 words of return data from the High Speed Counter. This data is the count accumulator, the strobe register and other pertinent data transferred from the High Speed Counter to the PLC each sweep.

The next series of screens are the actual count parameters for the High Speed Counter. For the filter times, count modes and count directions, press the -/+ key to toggle the screen display, then press the ENT key to record the value. If you change your mind about a parameter, press the CLR key instead of ENT and the original value will be recalled. To get to the next screen in the series, simply press the right arrow key. To backup to previous parameters (screens), use the left arrow key.

Configuration Screens

Screen 1 – Enable High Speed Counter

```
R0:04 HSC    <S  
HSC:DISABLED
```

The High Speed Counter is defaulted to being disabled. To enable the High Speed Counter press the -/+ key one time. The word ENABLED will be displayed. Then press the ENT key to enable the High Speed Counter.

Screen 2 – Counter Type

This screen indicates that the High Speed Counter is a Type A counter. This screen is informational only since you cannot change the type of counter on the Series 90-20 PLC. The Series 90-30 Model 311 or Model 331 High Speed Counter module supports the Type B and Type C counters in addition to the Type A.

```
R0:04 HSC    <S  
CNTR TYPE:TYPE A
```

Screen 3 – Output Default/Module Failure Mode

```
R0:04 HSC    <S  
FAIL MODE:NORMAL
```

This screen selects the state the outputs will assume if the PLC goes to stop mode with I/O disabled. NORMAL indicates that the outputs will continue to operate under control of the counter. FRCOFF causes the outputs to be forced off if the PLC stops, while HOLD causes the High Speed Counter to retain the last state of the output points before the PLC stops.

Screen 4 – Count Filter

```
R0:04 HSC    <S  
CNT FIL:HIGH
```

This screen allows you to specify the filters applied to the count input. The HIGH selection is used to allow higher count rates up to 10 Khz. The LOW filter is used to allow only low frequency signals to be recorded as counts. This is used when the count rate is slow and there is a possibility of high frequency noise coupling into the signal.

Screen 5 – Preload/Strobe Select

```
R0:04 HSC      <S  
PLD/STB:PLD
```

The default is to use the second input as a preload input. To change this input to a strobe input, press the -/+ key one time then press the ENT key. Once the strobe mode is selected, the filter for that input is forced to the HIGH mode.

Screen 6 – Preload Filter

```
R0:04 HSC      <S  
PLD FIL:HIGH
```

This screen allows you to select high or low frequency filtering for the preload input in the same manner as the count filter.

Screen 7 – Counter Direction

```
R0:04 HSC      <S  
CTR DIR:UP
```

This screen allow you to specify the direction the counter will operate in - either up or down counting.

Screen 8 – Counter Mode

```
R0:04 HSC      <S  
CTR MODE:CONT
```

This screen specifies the Counter Mode - continuous or one shot. When configured in the continuous (CONT) mode, the counter will roll over to the low limit once the accumulator passes the high limit. In the one shot (1 SHOT) mode, the counter will stop when the high (or low - if counting down) limit is reached.

Screen 9 – Time Base Value

```
R0:04 HSC    <S  
TIME BS:100
```

This screen allows you to enter the time base that is used to configure the counts per time base calculation. The default is 1000 milliseconds (1 second). To enter a new value, select the value using the numeric keys on the Hand-Held Programmer then press the ENT key to record the value.

Screen 10 – High Limit

```
R0:04 HSC    <S  
HI LIM: 32767
```

This screen is used to specify the highest (most positive) value the count accumulator can obtain. The default is 32767 which is the maximum value the counter can handle. As with the time base, use the Hand-Held Programmer numeric keys to change the value, then press the ENT key to record it. Pressing the CLR key instead of the ENT key will cancel the entry.

Screen 11 – Low Limit

```
R0:04 HSC    <S  
LOW LIM: 0
```

This screen specifies the lowest (most negative) value for the count accumulator. The default value is 0.

Screen 12 – ON Preset Value

When the counter accumulator reaches this value (depending also on the value of the OFF preset) the appropriate output is turned on (depending on the state, either enabled or disabled, of the output control flags in the %Q data word).

```
R0:04 HSC    <S  
ON PST: 0
```

Screen 13 – OFF Preset Value

```
R0:04 HSC    <S  
OFF PST:    0
```

This value is used in conjunction with the ON preset to indicate at what accumulator value the associated output point will be turned off.

Screen 14 – Preload Value

```
R0:04 HSC    <S  
PRELD:      0
```

This parameter specifies the value that will be loaded into the accumulator when the appropriate preload input on the terminal strip is asserted.

Application Example

Following is an example of a typical application using the Series 90-20 High Speed Counter. This example is an RPM Indicator.

RPM Indicator

Feature Used:
Counts/Timebase Register

The High Speed Counter can be used as a position/motion indicator when connected to a feedback device (such as an encoder) that is coupled to a rotary motion. RPM indication can be obtained directly from the counter's Counts/Timebase register (CTB) or derived from it by a simple calculation.

The RPM is given by:

$$RPM = \frac{CTB}{PPR * T}$$

where: CTB = counts/timebase reading from the counter
PPR = pulses/revolution produced by the feedback device
T = timebase expressed in minutes

Note that if the pulses/revolution is some integer power of 10, then setting the timebase for 6, 60, 600, 6000, or 60,000 will yield a direct reading of RPM in the CTB register with an assumed decimal placement.

Example 1

If feedback produces 1000 pulses/revolution, CTB reading = 5210, and the timebase is configured for 600 ms: then T = 600 ms / 60000 ms/min = .01 and 1/T = 100

$$RPM = 5210 / 1000 \times 100 = 521$$

CTB reading is RPM with .1 RPM resolution.

Example 2

Assume the same conditions as example 1, except the timebase is now set to 60 ms, which gives T = 60/60000 = .001 and 1/T = 1000.

Since the motion is turning at the same speed as in example 1, the CTB reading now equals 521

$$\text{and } RPM = 521 / 1000 \times 1000 = 521.$$

CTB reading is now RPM with 1 RPM resolution.

Command and Error Code Reference Charts

High Speed Counter Data Commands			Error Codes (% AI Word 1)	
Command		Definition	Code	Description
Decimal	Hexadecimal			
			0	No errors
			1	Unused
			2	Unused
01	01	Load Accumulator n	3	Invalid Command
02	02	Load Hi Limit n	4	Invalid Parameter
03	03	Load Lo limit n	5	Invalid Sub-Command
04	04	Load Acc Increment n	6	Invalid Counter Number
05	05	Set Ctr n Direction (A only)	7	Reserved
06	06	Load Timebase n	8	Reserved
08	08	Reserved	9	Reserved
11	0B	Load ON Preset n.1	10	Home Position Error
12	0C	Reserved	11	Counter 1 Limit Error
13	0D	Reserved		
14	0E	Reserved		
21	15	Load OFF Preset n.1		
22	16	Reserved		
23	17	Reserved		
24	18	Reserved		
31	1F	Load Preload n.1		
32	20	Reserved		
50	32	Reserved		

Terminal Assignments

Terminal assignments for each I/O baseplate are described in Chapter 5. The terminal board label also provides a visual guide for field wiring to the High Speed Counter inputs.

This glossary consists of two parts. The first part is a glossary of terms for the Series 90-20 PLC; the second part is a glossary of basic instructions and reference types.

Glossary of Terms for the Series 90-20 PLC

Address	A number following a reference type, which together refer to a specific user reference (e.g., for %Innnnn, %I is the reference type and nnnnn is the address).
Alarm Processor	A software function which time-stamps and logs I/O and system faults in two tables that can be displayed by the programmer or uploaded to a host computer or other coprocessor.
Analog	An electrical signal activated by physical variables representing force, pressure, temperature, flow, etc.
AND (Logical)	A mathematical operation between bits. All bits must be 1 for the result to be 1.
Annotation	Optional explanatory text in a program. Three basic types of annotation include nicknames, reference descriptions, and comments.
Application Program	A program written by the user for control of a machine or process, that is, the application.
ASCII	An acronym for <i>American Standard Code for Information Interchange</i> , which is an 8-bit (7 bits plus 1 parity bit) code used for data.
Backup	A duplicate version of a program, created prior to editing the program.

Battery Connector	A connector wired to a lithium battery which connects the battery to the CMOS RAM memory devices. The battery connector is plugged into a receptacle accessed through an opening on the faceplate of the CPU module.
Baud	A unit of data transmission. Baud rate is the number of bits per second transmitted.
Bit (Binary Digit)	The smallest unit of memory. It can be used to store only one piece of information that has two states (for example, One/Zero, On/Off, Good/Bad, Yes/No). Data that requires more than two states (e.g., numerical values 000 to 999) requires multiple bits (see Word).
Bus	An electrical path for transmitting and receiving data.
Byte	A group of binary digits operated on as a single unit. In the Series 90-20 PLC, a byte is eight bits.
Command Line	The fourth line at the top of the Logicmaster 90-30/20/Micro programming software display screen. This line displays typed data and command entries.
Comment	A rung explanation consisting of up to 2048 characters of text.
CONFIG.SYS File	A file that describes the system requirements for the software. The CONFIG.SYS file must be custom-tailored to fit the specific hardware configuration of your system and Logicmaster 90-30/20/Micro requirements.
Configuration File Set	Two types of files constitute a configuration file set, the I/O configuration (I/O config) file which contains data on the I/O system and the CPU configuration file which contains data on the PLC itself.
Configuration Software	That portion of the Logicmaster 90-30/20/Micro programming software package which provides the tools for configuration of I/O and many system parameters.
Constant	A fixed value or an item of data that does not vary. A constant can be stored in a register.

Counter	A function which can be programmed to control other devices according to a preset number of on/off transitions.
CPU (Central Processing Unit)	The central device or controller that interprets user instructions, makes decisions, and executes the functions based on a stored application program.
Data Memory	User references within the CPU which are accessible by the application program for storage of discrete or register data.
Directory	A file which contains the names and specifications of other files in the computer.
Discrete	The term <i>discrete</i> includes both real and internal I/O that are one-bit user references.
Disk	A hard disk or floppy diskette, used as an information storage and retrieval device.
DOS (Disk Operating System)	A group of utility programs which provide the structure for system operations on a personal computer.
Drive	A floppy diskette drive or hard disk drive. The identification of the drive, such as Drive A.
Firmware	A series of instructions contained in ROM (Read Only Memory) which are used for internal processing functions. These instructions provide the structure for application program operations.
Function Key	A key (F1 through F10) whose function is controlled by software. This function may change within the program. Logimaster 90-30/20/Microsoftware displays the current assignments of these function keys at the top of the screen.
Grounding Terminal	A terminal on each power supply which must be connected to earth ground (through the AC power source) to ensure that the rack is properly and safely grounded.
Hardware	All of the mechanical, electrical, and electronic devices that comprise the Series 90-20 PLC and its applications.

Help Screens	Instructive text screens. Help is displayed by pressing the ALT and H keys simultaneously to display context-sensitive help pertaining to all programming and configuration functions, ALT-I to access the Help display for mnemonics, or ALT-K to display a listing of all special keys.
Hexadecimal	A numbering system, having 16 as a base, represented by the digits 0 through 9, then A through F.
Input Module	An I/O module that converts signals from user devices to logic levels that can be used by the CPU.
Input Scan Time	The time required for the CPU to scan all inputs for new input values.
I/O (Input/Output)	That portion of the PLC to which field devices are connected and which isolates the CPU from electrical noise.
I/O Power Supply Base Module	A Series 90-20 module that contains a power supply, I/O conditioning circuits and terminal strips for user field wiring connections.
I/O Electrical Isolation	A method of separating field wiring from logic level circuitry. Typically, this is accomplished through the use of solid-state optical isolation devices.
I/O Fault Table	A fault table listing I/O faults. These faults are identified by time, date, and location.
K (Kilo)	An abbreviation for kilo or exactly 1024 in the language of computers.
Ladder Diagram	A graphic representation of combinational logic.
LED (Light Emitting Diode) Status Display	A display consisting of a group of LEDs with two rows of eight LEDs at the top of each discrete I/O module. Each LED in the two groups of eight indicates the state of the respective input or output point on the board.

Link	Horizontal and vertical links are used to carry power around an element in a ladder logic program, or to place elements in parallel or series with one another.
List	A group of consecutive storage locations in memory, used for data manipulation. The beginning address and length of the list are set up in the user program. Data is accessed from either the top or the bottom of the list.
Load	The function used to transfer programs to the Logicmaster 90-30/20/Micro folder.
Logic Solution Time	The time required to execute all active instructions in the application program.
Main Menu	The Logicmaster 90-30/20/Micro software main menu lists all the principal system functions, and the function keys which control these functions.
Memory Card	A memory cartridge containing EEPROM memory which is inserted into a slot in the Hand-Held Programmer. This memory cartridge, provides the Hand-Held Programmer with a means for off-line storage and retrieval of the application program and system configuration data.
Microsecond	One millionth of a second. 1×10^{-6} or 0.000001 second. Microsecond may be abbreviated as μs .
Millisecond	One thousandth of a second. 1×10^{-3} or 0.001 second. Millisecond may be abbreviated as ms.
Mnemonic	An abbreviation given to an instruction. The mnemonic is usually an acronym, formed by combining initial letters or parts of words.
Monitor Mode	Monitor mode allows programs to be examined and real-time status to be displayed; but no changes of logic, reference values, or I/O overrides are allowed.
Nickname	An optional 7-character identifier for a machine reference. All nicknames used by the program will be included in its variable declaration table.

Noise	Undesirable electrical disturbances to normal signals, generally of high frequency content.
Non-Retentive Coil	A coil that will turn off upon removal of applied power to the CPU.
Non-Volatile Memory	A memory (for example, PROM) capable of retaining its stored information under no-power conditions (power removed or turned off).
Off-Line Mode	Off-Line mode is used for program development. The programmer does not communicate with the PLC in Off-Line mode; the physical communications link may be intact, but the programmer is specifically not performing communications with the PLC. Power flow display and reference values are not updated.
On-Line Changes	Changes to I/O or register references and word-for-word changes, made when the Logicmaster 90-30/20/Micro system is in On-Line mode and the programs in both are exactly the same.
On-Line Mode	On-Line mode provides full CPU communications, allowing data to be both read and written.
OR (Logical)	A logical operation between bits, whereby if any bit is a 1, the result will be a 1.
Output	Data transferred from the CPU, through a module for level conversion to be used for controlling an external device or process.
Output Devices	Physical devices such as motor starters, solenoids, etc., that are switched by the PLC.
Output Circuit	A circuit that converts logic level signals within the CPU to usable output signals for controlling a machine or process.
Output Scan Time	The time required for the CPU to update all I/O controllers with new output values. When Model 30 I/O is present, this includes the time to actually write to each module.

Panel Mounting Flange

Flanges, with mounting holes, on the sides of the I/O power supply base module used to mount the base module on an electrical panel or wall.

Parallel Communication

A method of data transfer, whereby data is transferred on several wires simultaneously.

Parity

The anticipated state, either odd or even, of a set of binary digits.

Parity Bit

A bit added to a memory word to make the sum of the bits in a word always even (even parity) or always odd (odd parity).

Parity Error

A condition that occurs when a computed parity check (checksum) does not agree with the parity bit.

Peripheral Equipment

External devices that can communicate with a PLC (e.g., programmers and printers).

PLC

An acronym for *Programmable Logic Controller*, which is a solid-state industrial control device that receives signals from user-supplied control devices, such as switches and sensors, implements them in a precise pattern determined by ladder diagram based application programs stored in user memory, and provides outputs for control of processes or user-supplied devices such as machines or motor starters. It is usually programmed in relay ladder logic and is designed to operate in an industrial environment.

PLC Fault Table

A fault table listing PLC faults. These faults are identified by time, date, and location.

Power Flow

In a ladder diagram, the symbolic flow of power represents the logical execution of program functions. For each function, it is important to know what happens when power is received and under what conditions power flow is output.

Preset Value

A numerical value specified in a function which establishes a limit for a counter or timer.

- Program Folder** A subdirectory of all the files which constitute a program, including the associated configuration files. The name of the program folder may contain up to seven characters.
- Program Name** The name of the current program. In most cases, it will be the same as the program folder. The program name may contain up to seven characters.
- Program Sweep Time**
The time from the start of one cycle of the application program to the next. The program sweep is composed of the following: perform start of sweep system tasks, read the inputs, execute the user's program, write the outputs, recover faulted boards, complete minimal checksum calculation, schedule the next sweep, communicate with the programmer and other intelligent option modules, and execute background tasks.
- Programmer** The hardware device required to run Logicmaster 90-30/20/Micro software. Connection can be through an available converter; or, as an alternative, a Work Station Interface board can be installed in the programmer to communicate with the Series 90-20 PLC.
- Programmer Port** The serial port on the CPU module accessible through a 15-pin connector, to which the programmer must be connected in order to communicate with the PLC. Both the Logicmaster 90-30/20/Micro programmer and the Hand-Held Programmer connect to this port.
- Programming Software**
That portion of the Logicmaster 90-30/20/Micro software package which is used to create ladder logic programs.
- PROM**
An acronym for *Programmable Read Only Memory*, which is a retentive digital device programmed at the factory and not easily changed by the user. PROM usually contains programs for internal system use.

RAM

An acronym for *Random Access Memory*, which is a solid-state memory that allows individual bits to be stored and accessed at random. This memory stores the Logicmaster 90-30/20/Micro software, program files, and related data while power is applied to the system. This type of memory, however, is volatile. Because data stored in RAM is lost under no-power conditions, a backup battery is required to retain the contents under those conditions. The Series 90-20 PLC uses a long-life lithium battery mounted on the power supply and PCM modules.

Read

To have data entered or to extract data from a storage device.

Reference Description

An optional text description associated with a user reference. A reference description can be used with or without a nickname. Reference descriptions are also entered in the variable declaration table.

Reference Type

A specific group of memory types in the Series 90-20 PLC (e.g., %I references discrete inputs and %Q references discrete outputs). The % symbol is used to distinguish machine references from nicknames.

Register

A group of 16 consecutive bits in register memory, referenced as %R. Each register is numbered, beginning at 00001. Register memory is used for temporary storage of numerical values and for bit manipulation.

Removable Terminal Strip

The removable assembly which attaches to the I/O power supply base module, and contains the screw terminals to which field wiring is connected.

Retentive Coil

A coil that will remain in its last state, even though power has been removed.

RUN Mode

A condition or state of the Series 90-20 PLC, where the CPU executes the application program. RUN mode executes in the RUN/OUTPUTS ENABLED mode only. In RUN/OUTPUTS ENABLED, all portions of the program sweep are executed.

Rung	A unit of ladder logic. One rung may have up to eight parallel lines of logic.
Rung Explanation	A rung explanation consists of up to 2048 characters of text. A rung explanation is associated with a specific rung by programming a Comment (COMMNT) function.
Serial Communication	A method of data transfer, whereby the bits are handled sequentially rather than simultaneously, as in parallel data transmission.
Serial Port	The port on the CPU module, accessible through a 15-pin connector, to which the programmer must be connected in order to communicate with the PLC. Both the Logicmaster 90-30/20/Micro programmer and the Hand-Held Programmer connect to this port.
Significant Bit	A bit that contributes to the precision of a number. The number of significant bits is counted, beginning with the bit contributing the most value (MSB or Most Significant Bit) and ending with the bit contributing the least value (LSB or Least Significant Bit).
Status Line	The three lines at the bottom of the display screen. The top line displays information about a PLC and the programmer. The second line identifies the current program, and the third line shows the status of the keyboard. For some programmer functions, the third line may also display additional information.
STOP Mode	A condition or state of the Series 90-20 PLC, where the CPU no longer executes the application program. STOP mode can be STOP/NOIOSCAN or STOP/IOSCAN. In STOP/NO IOSCAN mode, the PLC only communicates with the programmer and other devices (GBC, PCM, etc.), recovers faulted boards, reconfigures boards and executes background tasks. All other portions of the sweep are skipped. In STOP/IOSCAN mode, the PLC CPU can monitor I/O. This feature provides a way to monitor and debug I/O without executing the application program.
Storage	Used synonymously with memory.
Store	The function used to transfer programs from the Logicmaster 90-30/20/Microfolder to the CPU.

Sweep	The CPUs repeated execution of all program logic, I/O service, peripheral service, and self-testing. This occurs automatically, many times each second.
Teach Mode	A function used to remember a sequence of keystrokes for later recall.
Timer	A function that can be used to control the operating cycle of other devices by a preset and accumulated time interval.
User Memory	The portion of system memory in which the application program and data is stored. This memory is battery-backed CMOS RAM.
User Reference Type	A reference assigned to data which indicates the memory in which it is stored in the PLC. References can be either bit-oriented (discrete) or word-oriented (register).
Variable Declaration	The portion of a program used to create, display, and change nicknames and reference descriptions assigned to user references. Variable declarations can be displayed in a table which may have up to 2000 entries.
Verify	A function used to compare program content. The program in the current folder may be compared with a program from the CPU.
Volatile Memory	A type of memory that will lose the information stored in it if power is removed from the memory devices. It requires a backup battery for retention of contents of memory. In the Series 90-20 PLC, a lithium battery is used for this purpose.
Watchdog Timer	A timer in the CPU used to ensure that certain hardware conditions are met within a predetermined time. The watchdog timer value is 200 milliseconds.
Word	A measurement of memory length, usually 4, 8, or 16-bits long. In the Series 90-20 PLC, a word is 16-bits in length.
Write	To transfer, record, or copy data from one storage device to another (for example, from CPU to disk).

**Glossary of Basic Instructions and Reference Types
for Logicmaster 90-30/20/Micro Software Developed Programs**

Basic Instruction	Specific Term	Generic Term
- -	normally open contact	contact
- / -	normally closed contact	contact
-()-	coil	coil
-(/)-	negated coil	coil
-(S)-	SET coil	coil
-(R)-	RESET coil	coil
-()-	positive transition coil	coil
-(-)-	negative transition coil	coil
-(M)-	retentive coil	coil
-(/ M)-	negated retentive coil	coil
-(SM)-	retentive SET coil	coil
-(RM)-	retentive RESET coil	coil
-----	horizontal link	link
	vertical link	link

Reference Type	Specific Term	Generic Term
%I	input	discrete
%Q	output	discrete
%M	internal	discrete
%T	temporary	discrete
%G	global	discrete
%S	system	discrete
%SA	system	discrete
%SB	system	discrete
%SC	system	discrete
%R	register	register
%AI	analog input register	register
%AQ	analog output register	register
%Rnnnnn	nnnnn is the address	

Appendix B

Instruction Timing

The Series 90-20 PLC supports many different functions and function blocks. This appendix contains tables showing the memory size in bytes and the execution time in microseconds for each function. Memory size is the number of bytes required by the function in a ladder diagram application program.

Two execution times are shown for each function:

Execution Time	Description
Enabled	Time required to execute the function or function block when power flows into and out of the function. Typically, best-case times are when the data used by the block is contained in user RAM (word-oriented memory) and not in the discrete memory.
Disabled	Time required to execute the function when power flows into the function or function block; however, it is in an inactive state, as when a timer is held in the reset state.

Note

Timers and counters are updated each time they are encountered in the logic; timers by the amount of time consumed by the last sweep and counters by one count.

Table B-1. Instruction Timing

Function Group	Function	Enabled	Disabled	Increment	Size
		211	211	211	
Timers	On Delay Timer	144	94	-	15
	ElapsedTimer	139	114	-	15
Counters	Up Counter	125	112	-	11
	Down Counter	125	112	-	11
Math	Addition(INT)	82	45	-	13
	Addition(DINT)	94	45	-	13
	Subtraction(INT)	82	45	-	13
	Subtraction(DINT)	93	45	-	13
	Multiplication(INT)	87	45	-	13
	Multiplication(DINT)	121	45	-	13
	Division(INT)	96	45	-	13
	Division(DINT)	393	45	-	13
	ModuloDivision (INT)	90	45	-	13
	ModuloDivision (DINT)	393	45	-	13
	Square Root (INT)	188	49	-	9
	Square Root (DINT)	330	47	-	9
Relational	Equal(INT)	79	45	-	9
	Equal(DINT)	88	45	-	9
	Not Equal (INT)	79	45	-	9
	Not Equal (DINT)	88	45	-	9
	Greater Than (INT)	79	45	-	9
	Greater Than (DINT)	88	45	-	9
	Greater Than/Equal(INT)	79	45	-	9
	Greater Than/Equal(DINT)	88	45	-	9
	Less Than (INT)	79	45	-	9
	Less Than (DINT)	88	45	-	9
	Less Than/Equal(INT)	79	45	-	9
	Less Than/Equal(DINT)	88	45	-	9
	Bit Operation	LogicalAND	82	52	-
Logical OR		82	52	-	13
LogicalExclusive OR		82	52	-	13
Logical Invert, NOT		77	-	-	9
Shift Bit Left		169	95	+12.2L	15
Shift Bit Right		165	95	+12.6L	15
Rotate Bit Left		168	51	+13.2L	15
Rotate Bit Right		160	51	+12.5L	15
Bit Position		87	47	+N	13
Bit Clear		80	33	-	13
Bit Test		85	34	-	13
Bit Set		80	34	-	13

Table B-1. Instruction Timing - Continued

Function Group	Function	Enabled	Disabled	Increment	Size
		211	211	211	
Data Move	Move (INT)	80	46	+2.00N	13
	Move (BIT)	110	48	+15.00N	13
	Move (WORD)	80	46	+2.00N	13
	Block Move	90	71	-	27
	Block Move (INT)	90	71	-	27
	Block Move (WORD)	90	71	-	27
	Block Clear	66	46	+1.60N	9
	Shift Register	120	87	+2.00N	15
	Shift Register (BIT)	205	68	+1.50N	15
	Shift Register (WORD)	120	87	+2.00N	15
	Bit Sequencer	128	64	+0.10N	15
	CommunicationsRequest	131	45	-	
	Table	Array Move			
INT		279	85	+2.00N	21
DINT		280	87	+4.00N	21
BIT		354	87	+1.84N	21
BYTE		277	87	+1.00N	21
WORD		279	85	+2.00N	21
Search Equal					
INT		240	95	+1.98N	19
DINT		245	95	+5.36N	19
BYTE		217	95	+1.88N	19
WORD		240	95	+1.98N	19
Search Not Equal					
INT		239	95	+2.38N	19
DINT		244	95	+8.00N	19
BYTE		217	95	+1.88N	19
WORD		239	95	+2.38N	19
Search Greater Than					
INT		241	96	+4.76N	19
DINT		250	95	+10.64N	19
BYTE		219	95	+4.25N	19
WORD		241	96	+4.76N	19
Search Greater Than / Equal					
INT		240	95	+4.76N	19
DINT		249	95	+10.65N	19
BYTE		219	95	+4.25N	19
WORD		240	95	+4.76N	19
Search Less Than					
INT		241	95	+4.72N	19
DINT		250	96	+10.64N	19
BYTE		219	95	+4.25N	19
WORD		241	95	+4.72N	19

Table B-1. Instruction Timing - Continued

Function Group	Function	Enabled	Disabled	Increment	Size
		211	211	211	
Table (cont.)	SearchLessThan/Equal				
	INT	240	95	+4.76N	19
	DINT	250	95	+10.64N	19
	BYTE	218	95	+4.25N	19
	WORD	240	95	+4.76N	
Conversion	Convert to INT	90	45	-	9
	Convert to BCD-4	88	45	-	9
Control	Call a Subroutine	-	-	-	-
	DoI/O	490	45	-	12
	PID - ISA Algorithm	2290	113	-	15
	PID - IND Algorithm	2420	116	-	15
	End Instruction	-	-	-	-
	Service Request				
	# 6	77	45	-	9
	#13	1500	45	-	9
	#14	347	45	-	-
	#15	215	45	-	-
#16	138	45	-	9	
	Nested MCR	50	35	-	8
	Nested ENDMCR	50	35	-	

- Notes**
1. Time (in microseconds) is based on Release 3 of Logicmaster 90-30/90-20 software.
 2. For table functions, increment is in units of length specified. For bit operation functions, L = the number of bits. For Bit Position, N = the bit that is set. For data move functions, N = the number of bits or words.
 3. Size is in bytes.
 4. Enabled time for single length units of type %R, %AI, and %AQ.
 5. JUMPs, LABELs, COMMENTs, and non-nested MCRs are included in the boolean timing spec, which is 18ms/1K logic.

Appendix C

Miniconverter Kit

This appendix describes the Miniconverter Kit for use with Series 90 PLCs.

The Miniconverter Kit (IC690ACC901) consists of an RS-422 (SNP) to RS-232 Miniconverter, a 6 foot (2 meter) serial extension cable, and a 9-pin to 25-pin Converter Plug assembly. The 15-pin SNP port connector on the Miniconverter plugs directly into the serial port connector on the Series 90-30 power supply, Series 90-70 CPU or Series 90-20 CPU. The 9-pin RS-232 port connector on the Miniconverter connects to an RS-232 compatible device.

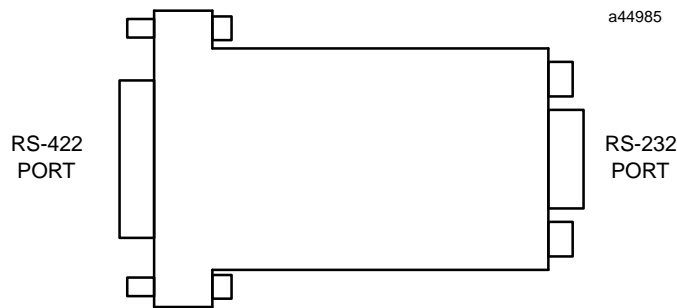


Figure C-1. Series 90 SNP to RS-232 Miniconverter

When used with an IBM PC-AT, or compatible computer, one end of the extension cable plugs into the Miniconverter's 9-pin serial port connector, the other end plugs into the 9-pin serial port of the computer. The Converter plug (supplied with kit) is required to convert the 9-pin serial port connector on the Miniconverter to the 25-pin serial port connector on the GE Fanuc Workmaster II computer, or an IBM PC-XT or PS/2 Personal Computer.

The GE Fanuc Workmaster computer requires an additional adapter (not supplied with kit - please contact your local GE Fanuc PLC distributor) for use with the Miniconverter.

The pinout of the Miniconverter is shown in the following two tables. The first table shows the pinout for the RS-232 port. The direction of signal flow is with respect to the Miniconverter.

Table C-1. Miniconverter RS-232 Port

Pin	Signal Name	Direction
2	SD - Send Data	Output
3	RD - Receive Data	Input
5	GND - Ground	n/a
7	CTS - Clear To Send	Input
8	RTS - Request To Send	Output

The pinouts were chosen to allow direct connection (using a straight through, or 1 to 1 cable (as provided with kit)) to the IBM PC-AT. Most IBM compatible computers equipped with an RS-232 port will provide a pinout compatible with the one shown above.

Table C-2 is the pinout for the Miniconverter's RS-422 serial port. The direction of signal flow is also with respect to the Miniconverter.

Table C-2. Miniconverter RS-422 Port

Pin	Signal Name	Direction
1	SHLD - Shield	n/a
5	+5 VDC - Power	Input
6	CTS(A') - Clear To Send	Input
7	GND - Ground	n/a
8	RTS(B) - Request To Send	Output
9	RT - Receive Termination	Output
10	SD(A) - Send Data	Output
11	SD(B) - Send Data	Output
12	RD(A') - Receive Data	Input
13	RD(B') - Receive Data	Input
14	CTS(B') Clear To Send	Input
15	RTS(A) - Request To Send	Output

System Configurations

The Miniconverter can be used in a point-to-point configuration as described above, or in a multidrop configuration with the host device configured as the master and one or more PLCs configured as slaves.

The multidrop configuration requires a straight through (1 to 1) cable from the Miniconverter's RS-422 port to the first slave PLCs SNP port. Other slaves will require a daisy chain connection between slaves. A maximum of eight devices can be connected in an RS-422 multidrop configuration. All of the devices must have a common ground. If ground isolation is required, you can use the GE Fanuc Isolated Repeater/Converter (IC655CCM590) in place of the Miniconverter.

When using the Miniconverter with a modem connection, it may be necessary to jumper RTS to CTS (consult the user's manual for your modem).

Cable Diagrams (Point-To-Point)

Use the following cable connections when connecting the Miniconverter to IBM PC and compatible computers with hardware handshaking..

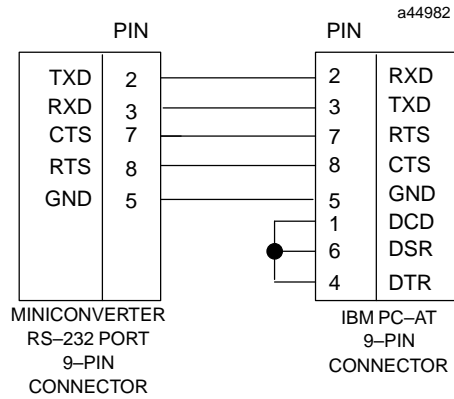


Figure C-2. Miniconverter to PC-AT

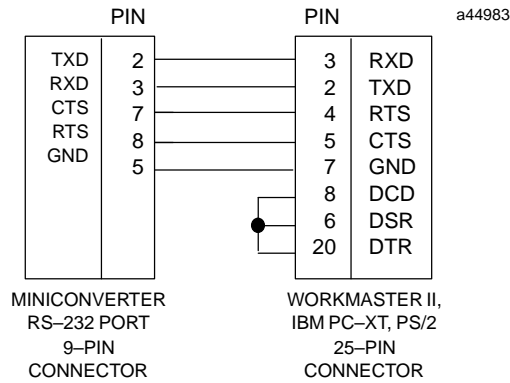


Figure C-3. Miniconverter to Workmaster II, PC-XT, PS/2

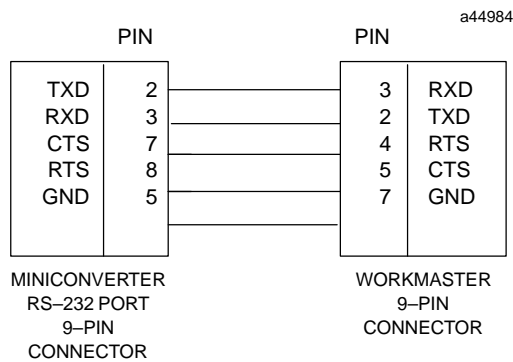


Figure C-4. Miniconverter to 9-Pin Workmaster or PC-XT Computer (Additional Adapter Required)

Table C-3. Miniconverter Specifications

Mechanical	
RS-422	15-pin D shell male for direct mounting to Series 90 serial port.
RS-232	9-pin D shell male for connection to RS-232 serial port of a Workmaster II computer or Personal Computer.
Electrical and General:	
Voltage Supply	+5 VDC (supplied by PLC power supply)
Typical Current	95 mA
Operating Temperature	0 to 70C (32 to 158F)
Baud Rate	38.4KBaud maximum
Conformance	EIA-422 (Balanced Line) or EIA-423 (Unbalanced Line)
Ground Isolation	Not provided

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